

DATA SHEET

SMV1272-079LF: Hyperabrupt Junction Tuning Varactor

Applications

- VHF TV tuners
- VCOs

Features

- Cross to Renesas HVC350B
- Low-series resistance
- High-capacitance ratio
- Ultra-small SC-79 package (MSL1, 260 °C per JEDEC J-STD-020)



Description

The SMV1272-079LF is a silicon hyperabrupt junction varactor diode specifically designed for battery operation. The specified high-capacitance ratio and low-series resistance make this varactor appropriate for low phase-noise voltage-controlled oscillators (VCOs) used at frequencies in wireless systems up to and above 2.5 GHz.

Applications for the SMV1272-079LF include low-noise and wideband UHF and VHF VCOs for GSM, PCS, CDMA, and analog phones. Table 1 describes the packaging and marking of the SMV1272-079LF varactor.

Table 1. Packaging and Marking


Single
SC-79 Green™
SMV1272-079LF Marking: Cathode and Y4
Ls = 0.7 nH



The Pb-free symbol or "LF" in the part number denotes a lead-free, RoHS-compliant package unless otherwise noted as Green™. Tin/lead (Sn/Pb) packaging is not recommended for new designs.

Electrical and Mechanical Specifications

The absolute maximum ratings of the SMV1272-079LF varactor are provided in Table 2. Electrical specifications are provided in Table 3. Typical capacitance values are listed in Table 4. The relationship between capacitance and reverse voltage is illustrated in Figure 1.

The SPICE model for the SMV1272-079LF varactor is shown in Figure 2 and the associated model parameters are provided in Table 5.

Package dimensions are shown in Figure 3, and tape and reel dimensions are provided in Figure 4.

Package and Handling Information

Instructions on the shipping container label regarding exposure to moisture after the container seal is broken must be followed. Otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly.

The SMV1272-079LF varactor is rated to Moisture Sensitivity Level 1 (MSL1) at 260 °C. It can be used for lead or lead-free soldering. For additional information, refer to the Skyworks Application Note, *Solder Reflow Information*, document number 200164.

Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. Production quantities of this product are shipped in a standard tape and reel format.

Table 2. SMV1272-079LF Absolute Maximum Ratings (Note 1)

Parameter	Symbol	Minimum	Typical	Maximum	Units
Reverse voltage	V_R			15	V
Forward current	I_F			20	mA
Power dissipation	P_{DIS}			250	mW
Operating temperature	T_{OP}	-55		+125	°C
Storage temperature	T_{STG}	-55		+150	°C

Note 1: Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device.

CAUTION: Although this device is designed to be as robust as possible, electrostatic discharge (ESD) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions should be used at all times.

Table 3. SMV1272-079LF Electrical Specifications (Note 1)

($T_{OP} = 25\text{ °C}$, Unless Otherwise Noted)

Parameter	Symbol	Test Condition	Min	Typical	Max	Units
Reverse current	I_R	$V_R = 15\text{ V}$			20	nA
Capacitance	C_T	$f = 1\text{ MHz}$ $V_R = 1\text{ V}$ $V_R = 4\text{ V}$	15.88 5.0		16.62 6.0	pF pF
Capacitance ratio	C_{TR}	$C_T @ 1\text{ V} / C_T @ 4\text{ V}$	2.8	3.0		—
Series resistance	R_S	$f = 470\text{ MHz}$, $V_R = 1\text{ V}$		0.4	0.5	Ω
Breakdown voltage	V_{BR}	$I_R = 10\text{ }\mu\text{A}$	15			V

Note 1: Performance is guaranteed only under the conditions listed in this table.

Table 4. Capacitance vs Reverse Voltage

V _R (V)	C _T (pF)	V _R (V)	C _T (pF)
0	28.96	8	4.04
1	16.21	9	3.95
2	10.37	10	3.88
3	7.30	12	3.78
4	5.60	14	3.72
5	4.72	16	3.68
6	4.36	18	3.64
7	4.17	20	3.62

Typical Performance Characteristics

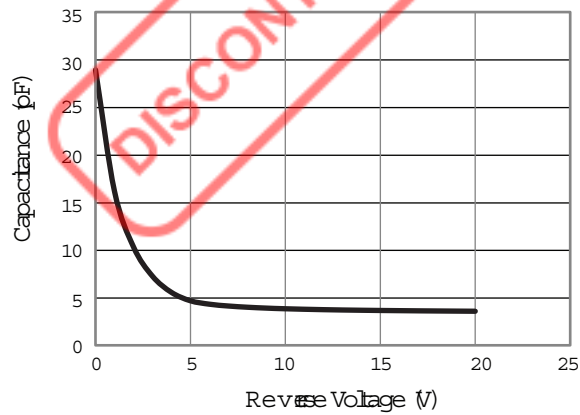


Figure 1. Capacitance vs Reverse Voltage

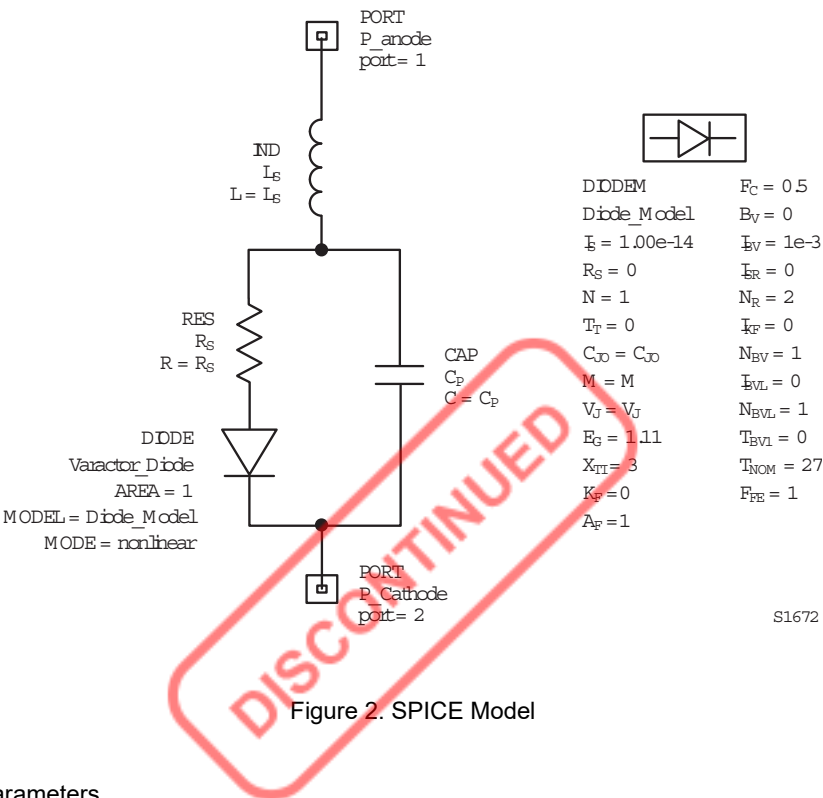
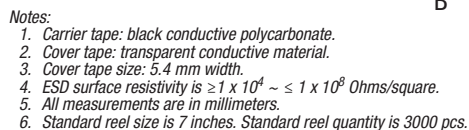


Figure 2. SPICE Model

Table 5. SPICE Model Parameters

Part Number	Cjo (pF)	Vj (V)	M	Cp (pF)	Rs (Ω)	Ls (nH)
SMV1272-079LF	29	0.38	0.60	0.07	0.50	0.70

Figure 1: Dimensions of the test specimen. The diagram shows a rectangular specimen with a central square area labeled "Polarity Indicator Area". Dimensions are given in inches and millimeters. Top width: 0.010 (0.25 mm) Min. to 0.014 (0.35 mm) Max. Bottom width: 0.060 (1.50 mm) Min. to 0.067 (1.70 mm) Max. Right height: 0.028 (0.70 mm) Min. to 0.035 (0.90 mm) Max. A horizontal dimension of 0.006 (0.15 mm) Min. is shown on the right. A legend indicates "Box or Bar" with a hatched pattern.



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