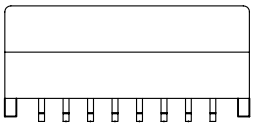
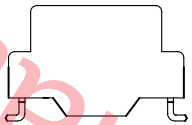
NOTES:

1. ROHS COMPLIANT
2. HEADER: DAP (DIALLYL PHTHALATE) WITH FLAMMABILITY RATING UL 94V-0 OR BETTER.
3. STORAGE TEMPERATURE: -20°C TO +125°C
4. COMPLIANCE TO J-STD:
   - A. J-STD-002: SOLDERABILITY AT 245°C REFLOW PROFILE
   - B. J-STD-020: LEVEL 1, NO MOISTURE SENSITIVE
   - C. J-STD-075: R7, 245°C MAXIMUM THROUGH REFLOW SOLDER
5. TO ORDER TAPE & REEL PACKAGING ADD A "T" SUFFIX TO THE PART NUMBER(i.e H2019FNL BECOMES H2019FNLT).





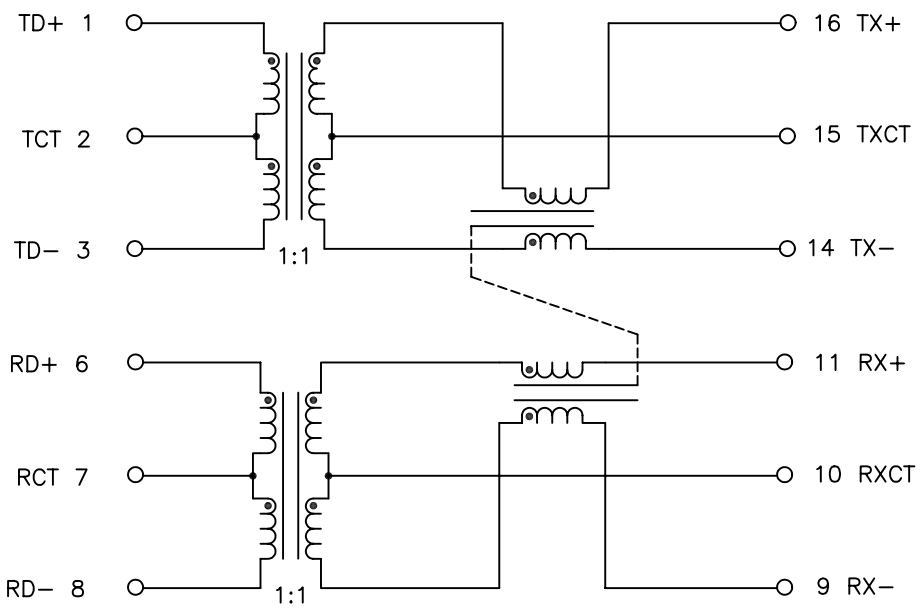


FINAL OUTLINE

www.datasheet.world

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PRODUCT DESCRIPTION	TLA DRAWING	PS DRAWING	SHEET	PART NO.	DATASHEET REV.
MDL,SIN,100P,1:1,SM,TU	H2019FNL-10	PS-2005.002-D	1 OF 3	H2019FNL	A



SCHEMATIC

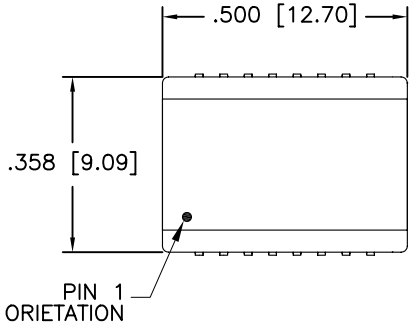
ELECTRICAL CHARACTERISTICS AT +25°C UNLESS OTHER SPECIFIED

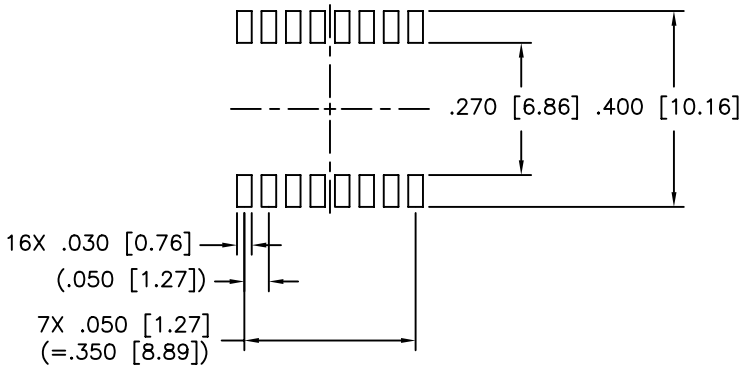
MEETS IEEE 802.3 SPECIFICATION

PARAMETER	SPECIFICATIONS		
OPERATING TEMPERATURE	0°C – 70 °C		
TURNS RATIO	1 : 1 ±2%		
POLARITY	PER SCHEMATIC		
INSERTION LOSS	100 KHz	1–125 MHz	
	−1.2 dB MAX	$-0.2-0.002*f^{1.4}$ dB MAX	
RETURN LOSS, 1000BT (Z OUT = 100 OHM ±15%)	.1–30 MHz	30–60 MHz	60–80 MHz
	−16 dB MIN	$-10+20*\log_{10}$ (f/60 MHz) dB MIN	−10 dB MIN
INDUCTANCE (OCL) (MEDIA SIDE, 0°C – 70°C)	350 uH MIN (MEASURED AT 100 KHz, 100 mVRMS AND WITH 8 mA DC BIAS)		
CROSSTALK, ADJACENT CHANNELS	1 MHz	10–100 MHz	
	−50 dB MIN	$-55+22*\log_{10}(f/10)$ dB MIN	
COMMON MODE REJECTION RATIO	2 MHz	30–200 MHz	
	−45 dB MIN	$-7+28*\log_{10}(f/200)$ dB MIN	
DC RESISTANCE, 1/2 WINDING	.65 OHMS MAX		
DC RESISTANCE IMBALANCE	±.065 OHMS MAX (CENTER TAP SYMMETRY)		
INPUT – OUTPUT ISOLATION	1500 VRMS MIN AT 60 SECONDS		

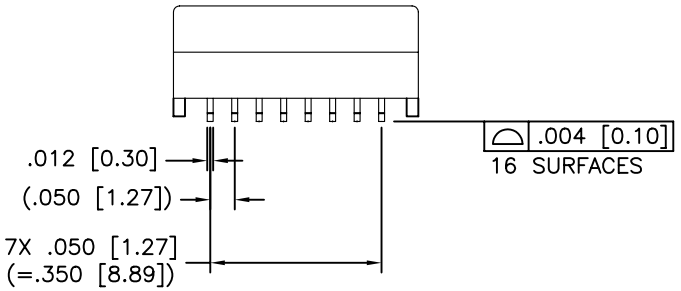
NOTE: f IS FREQUENCY IN MHz.

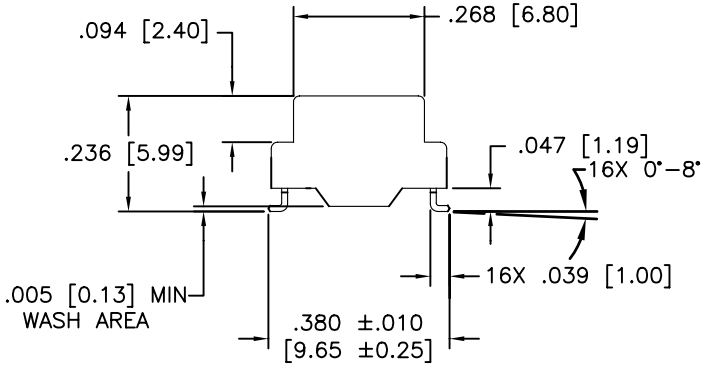
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SUGGESTED LAND PATTERN





DIMENSIONS ARE IN INCHES [MILLIMETERS] WITH THE FOLLOWING TOLERANCES: [MILLIMETERS] ARE FOR REFERENCE ONLY.
.XX= ±.01 [±0.25]
.XXX= ±.005 [±0.13]

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PRODUCT DESCRIPTION	TLA DRAWING	PS DRAWING	SHEET	PART NO.	DATASHEET REV.
MDL,SIN,100P,1:1,SM,TU	H2019FNL–10	PS–2005.002–D	3 OF 3	H2019FNL	A

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