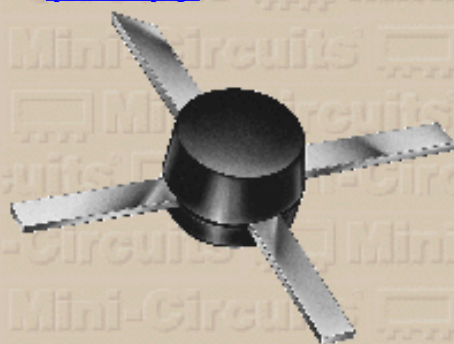


Amplifier



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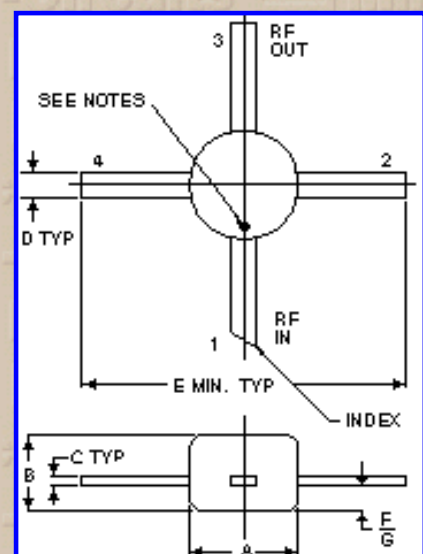
ERA-5

Frequency MHz	GAIN, dB	Maximum Power, dBm				Dynamic Range		VSWR				Absolute Maximum Rating		DC Power		Thermal resistance θ_{jc} °C/W
$f_L - f_U$	Min.	Output (1 dB Comp.)	Min.	Input (no damage)	NF dB Typ.	IP3 dBm Typ.	In DC- 3 GHz	In 3- f_U GHz	Out DC- 3 GHz	Out 3- f_U GHz		I (mA)	P (mW)	Cur- rent (mA)	Device Volt(V.)	
DC-4000	16	+18.40	+16.50	+13.00	4.30	32	+1.30	1.30	1.20	1.30		120	650	65	4.90	278

L_W =low range(f_L to $f_U/2$) U =upper range($f_U/2$ to f_U)

Pin Connections

Port	RF in	RF Out	DC	Case GND	Not Used
cb	1	3	3	2,4	-



Case Style - VV105 (inch,mm) weight: 0.015 grams.

A	B	C	D	E	F	G	H	J
.085	.060	.008	.020	.250	.012	.025		
2.159	1.524	0.203	0.508	6.350	0.305	0.635		
K	L	M	N	P	Q	R	S	T

Tolerance: .x $\pm$.1 .xx $\pm$.03 .xxx $\pm$.015 inch.

Material and Finish:

Case material: plastic. Lead finish: tin-lead plate or tin plate.

Marking:

RF input lead (1) identified by one or both of the following at factory option:

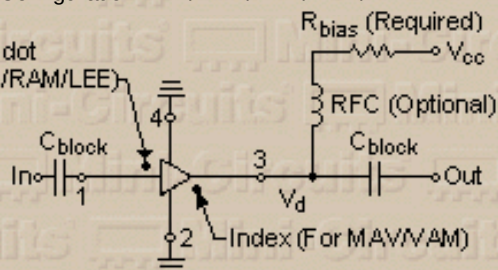
- diagonally cut lead, which may be 45°(ref) in either direction;
- orientation mark on the case. Model dash number identified by color dot or alphanumeric code on case. See specification data sheet.

Special Tolerances:

Notes:

- ERA-SM models available on tape and reel.
- Model number designated by alphanumeric code marking.
- Permanent damage may occur if any of these limits are exceeded. These ratings are not intended for continuous normal operation.
- For Amplifier Selection Guide, please click [here](#).
For Amplifier Environmental Specifications, please click [here](#).
- For Surface Mount Environmental Specifications, please click [here](#).
Re-flow soldering information is available in "[Surface Mount](#)" article.
- Low frequency cutoff determined by external coupling capacitors.
- Gain, VSWR at 2 GHz.
- Maximum Power, NF, IP3 at 1 GHz.
- f_U is the upper frequency limit for each model as shown in the table.
- Non-hermetic
- Absolute maximum ratings:
Operating temperature: -45°C to 85°C,
Storage temperature: -65° to 150°C,
Device voltage:
4.2V min., 5.5V max.
- Typical Biasing Configuration ERA/MAR/MAV/RAM/VAM

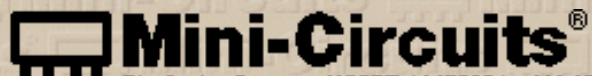
***Triangle or dot
(For ERA/MAR/RAM/LEE)



*** For RAM models, Pin 1 is identified by a diagonally cut lead.

- Supply voltage must be connected to pin 3 through a bias resistor in order to prevent damage. See [Biasing MMIC Amplifiers](#). Reliability predictions are applicable at specified current and normal operating conditions.
- General Quality Control Procedures and Environmental Specifications are given in [Mini-Circuits Guarantees Quality](#).
Hi-Rel, MIL description are given in [Hi-Rel and MIL](#)
- Prices and Specifications subjects to change without notice.

Lead width ± 0.10 inch; lead thickness ± 0.003 inch.



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