

ECN3297TF

ECN3297 is 16-channel High Voltage analog switching IC with bleed resistors on which latch-up free is realized by dielectric isolation technology.

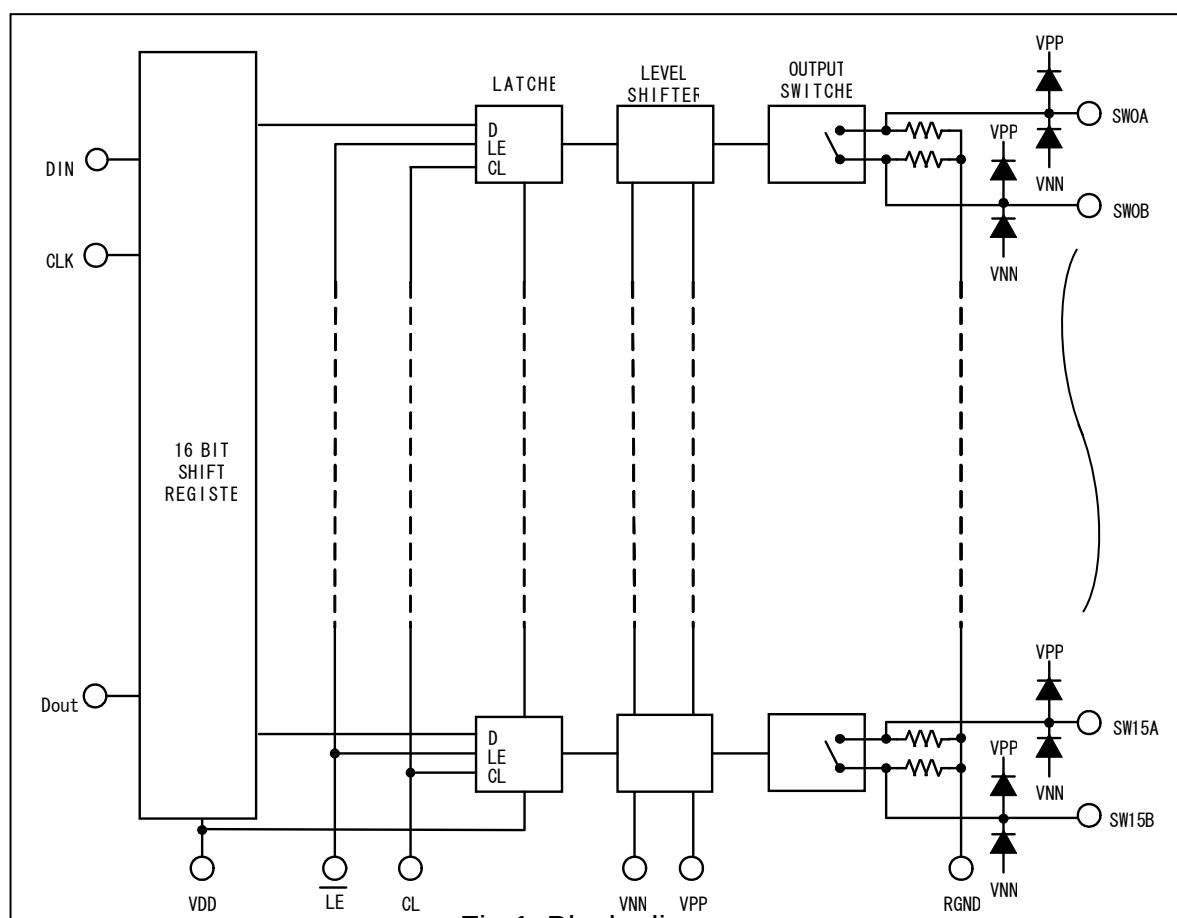
High voltage and low ON-resistance MOS switches are used as output devices controlled by a 3.3V or 5V signal. The ECN3297 is most suited to Ultrasound Imaging applications.

Functions

- * High voltage and low on-resistance MOS switches integrated.
- * 16bit shift register integrated.
- * Integrated bleed resistors on the outputs.
- * Integrated clamping diodes for overvoltage protection positive overshoot.

Features

- * Switch on-resistance: 19 Ω typ. (V_{PP}=100V,V_{NN}=-100V,I_{SIG}=5mA, 25°C)
- * Switch breakdown voltage: 220V
- * Latch-up free CMOS and High-Voltage drive circuit.
- * Power up/down sequence of power supply is free.
- * 48-pin TQFP Package (RoHS compliant)



~~Fig.1 Block diagram~~

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1. General

This Specification shall be applied to the following semiconductor integrated circuit.

- 1) Parts name : ECN3297TF
- 2) Application : Ultrasound imaging scanner and others
- 3) Structure : Monolithic IC
- 4) Package : TQFP48

2. Absolute Maximum Ratings

Table 1 Absolute Maximum Ratings

No.	Items	Symbol	Terminal	Values	Unit	Note
1	Logic power supply voltage	VDD	VDD	-0.5 to +7.0	V	Ta=25°C
2	VPP-VNN supply voltage	-	VPP, VNN	220	V	Ta=25°C
3	VPP Positive high voltage supply	VPP	VPP	-0.5 to VNN+200	V	Ta=25°C
4	VNN negative high voltage supply	VNN	VNN	-200 to +0.5	V	Ta=25°C
5	Logic input voltages	VDD	DIN, CLK, CL, LE	-0.5 to VDD+0.3	V	Ta=25°C
6	Analog signal range	-	SW0 to SW7	VNN to VPP	V	Ta=25°C
7	Operating junction temperature	Tjop	-	-20 to +125	°C	
8	Storage temperature	Tstg	-	-65 to +150	°C	
9	Power dissipation	Pw	-	1.0	W	TQFP48 Ta=70°C

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3. Electrical Characteristics

3.1 DC Characteristics

Table 2 DC Characteristics

Ta=25°C VDD=5V

No.	Items	Symbol	Spec			Unit	Test conditions	
			Min	Typ	Max			
1	Small signal switch on resistance	RONS	—	24	38	Ω	I SIG=5mA	VPP=40V, VNN=-160V
			—	17	27		I SIG=200mA	VNN=-160V
			—	19	27		I SIG=5mA	VPP=100V, VNN=-100V
			—	15	24		I SIG=200mA	VNN=-100V
			—	19	25		I SIG=5mA	VPP=160V, VNN=-40V
			—	15	25		I SIG=200mA	VNN=-40V
2	Small signal switch on resistance matching	Δ RONS	—	5	20	%	VPP=100V, VNN=-100V ISW=5mA	
3	Large signal switch on resistance	RONL	—	16	—	Ω	VPP=100V VNN=-100V	I SIG=1A
4	Value of output bleed resistance	RINT	20	35	50	k Ω	Output switch to RGND IRINT=0.5mA	
5	Switch off leakage per switch	ISOL	—	1.0	10	μ A	VSIG=VPP-10V, or VNN+10V	
6	DC offset switch (off)	DCOFF	—	10	100	mV	No load	
7	DC offset switch (on)	DCON	—	10	100	mV	No load	
8	Positive HV supply current	IPPQ1	—	10	50	μ A	All SWs off	
9	Negative HV supply current	INNQ1	—	-10	-50	μ A	All SWs off	
10	Positive HV supply current	IPPQ2	—	10	50	μ A	All SWs on, ISW=5mA	
11	Negative HV supply current	INNQ2	—	-10	-50	μ A	All SWs on, ISW=5mA	
12	IPP Supply current	IPP	—	—	6.0	mA	VPP=40V VNN=-160V	50kHz output switching frequency without load
			—	—	6.0		VPP=100V VNN=-100V	
			—	—	6.0		VPP=160V VNN=-40V	
13	INN Supply current	INN	—	—	6.0	mA	VPP=40V VNN=-160V	50kHz output switching frequency without load
			—	—	6.0		VPP=100V VNN=-100V	
			—	—	6.0		VPP=160V VNN=-40V	
14	Logic supply average current	IDD	—	—	4.0	mA	fCLK=5MHz,VDD=5.0V	
15	Logic supply quiescent current	IDDQ	—	—	10	μ A		
16	Data out source current	ISOR	0.45	0.70	—	mA	VOUT= V DD-0.7V	
17	Data out sink current	ISINK	0.45	0.70	—	mA	VOUT=0.7V	

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3.2 AC Characteristics

Table 3 AC Characteristics

Ta=25°C VDD=5V

No.	Items	Symbol	Spec			Unit	Test conditions
			Min	Typ	Max		
1	SW Turn on time	tON	–	–	5.0	μs	VSIG=VPP-10V, RL=10kΩ
2	SW Turn off time	tOFF	–	–	5.0	μs	VSIG=VPP-10V, RL=10kΩ
3	Clock frequency	fCLK	–	–	30	MHz	50% duty cycle, fData=fCLK/2 VDD=3.3V or 5.0V
4	Clock delay time to data out	tDO	16	–	51	ns	DOUT terminal, VDD=3.3V
			12	–	38	ns	DOUT terminal, VDD=5.0V
5	Output voltage spike	+VSPK	–	–	150	mV	VPP=40V, VNN=-160V, RL=50Ω
		-VSPK	–	–	-150		
		+VSPK	–	–	150		VPP=100V, VNN=-100V, RL=50Ω
		-VSPK	–	–	-150		
		+VSPK	–	–	150		VPP=160V, VNN=-40V, RL=50Ω
		-VSPK	–	–	-150		

Table 4 AC Characteristics (for reference purpose only)

Ta=25°C VDD=5V

No.	Items	Symbol	Spec			Unit	Condition
			Min	Typ	Max		
1	Off capacitance SW to GND	CSG (off)	–	6	–	pF	0V, 1MHz
2	On Capacitance SW to GND	CSG (on)	–	15	–	pF	0V, 1MHz
3	SW off isolation	KO	-30	-33	–	dB	f=5MHz, 1kΩ//15pF load
			-54	-60	–	dB	f=5MHz, 50Ω load
4	SW Crosstalk	KCR	-54	-60	–	dB	f=5MHz, 50Ω load

Note: These items are not tested when shipped.

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4. Recommended Operating Conditions

Please operate in use within the limit of recommended operating conditions detailed in Table 5.

Table 5 Recommended Operating Conditions

No	Items	Symbol	Recommended Value
1	Logic power supply voltage	VDD	3.0V to 5.5V
2	Positive high voltage supply	VPP	40V to VNN+200V
3	Negative high voltage supply	VNN	-160V to -40V
4	High-level input voltage	VIH	0.9VDD to VDD
5	Low-level input voltage	VIL	0V to 0.1VDD
6	Analog signal voltage peak to peak	VSIG	VNN+10V to VPP-10V
7	Operating free air-temperature	Ta	0°C to 70°C
8	Switching frequency	Fsw	50kHz max, Duty Cycle=50%
9	Set up time for LE	TSD	Min.60ns
10	Pulse width of LE	TWLE	Min.40ns
11	Time width of CL	TWCL	Min.40ns
12	Set up time DATA to Clock	TSU	Min.10ns
13	Hold time DATA from Clock	Th	Min.10ns
14	Maximum VSIG Slew Rate	dV/dt	Max.30V/ns

Attention ;

- 1) Power up/down sequence of power supply is arbitrary except GND terminal of IC must be powered-up first and powered-down last.
- 2) It is indispensable to make there are not to exceed a maximum rated voltage by the occurrence of the excessive voltage in case of investing and cutting of the power supply.

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5. Test Circuit

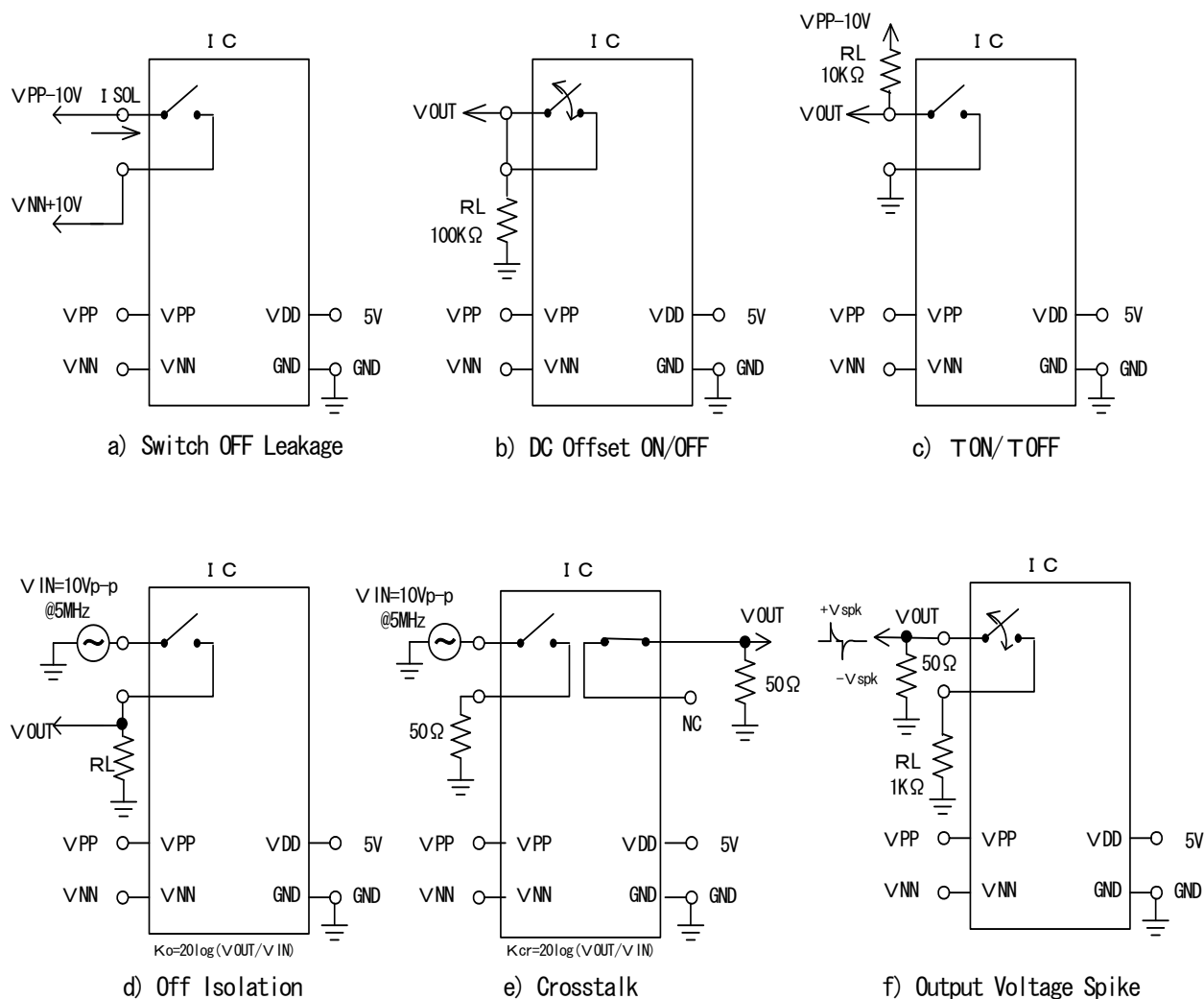


Fig. 2 Test Circuit

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6. Timing Waveforms

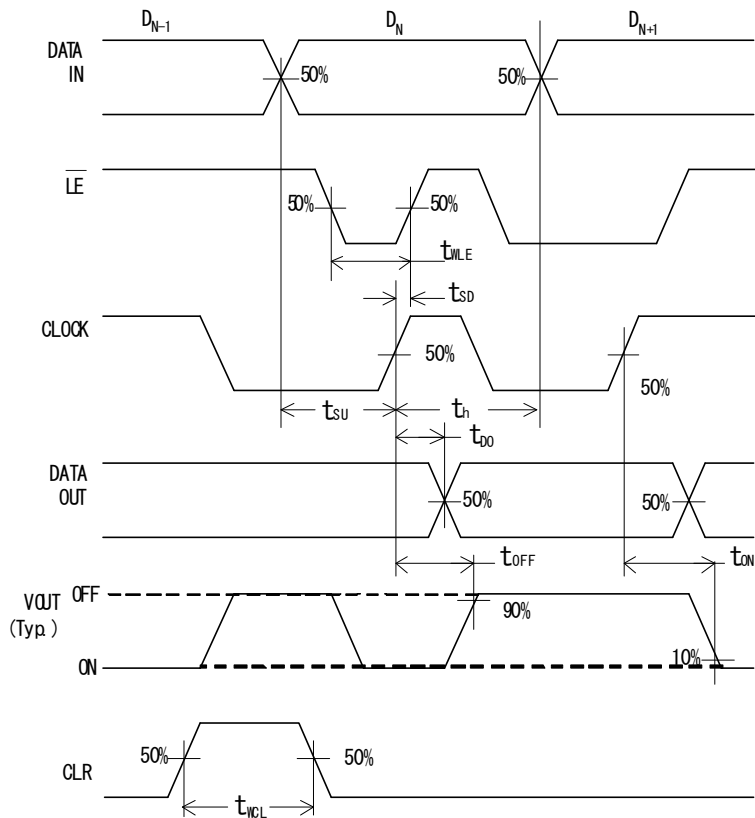


Fig. 3 Timing Waveforms

7 Truth Table

Table 6 Truth table

D0	D1	...	D7	D8	...	D15	LE	CL	SW0	SW1	...	SW7	SW8	...	SW15
L							L	L	OFF						
H							L	L	ON						
	L						L	L		OFF					
	H						L	L		ON					
							L	L							
							L	L							
			L				L	L				OFF			
			H				L	L				ON			
				L			L	L					OFF		
				H			L	L					ON		
							L	L							
							L	L							
							L	L							
							L	L							
						L	L	L							OFF
						H	L	L							ON
X	X	X	X	X	X	X	H	L	Hold Previous State						
X	X	X	X	X	X	X	X	H	OFF	OFF	OFF	OFF	OFF	OFF	OFF

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8 Pin Configuration

ECN3297TF TQFP48 (48Pin TQFP)

Table3. Pin Configuration

Pin	Functions	Pin	Functions
1	N/C	25	SW15B
2	N/C	26	SW15A
3	SW4B	27	SW14B
4	SW4A	28	SW14A
5	SW3B	29	SW13B
6	SW3A	30	SW13A
7	SW2B	31	SW12B
8	SW2A	32	SW12A
9	SW1B	33	SW11B
10	SW1A	34	SW11A
11	SW0B	35	N/C
12	SW0A	36	N/C
13	VNN	37	SW10B
14	N/C	38	SW10A
15	VPP	39	SW9B
16	N/C	40	SW9A
17	GND	41	SW8B
18	VDD	42	SW8A
19	DIN	43	SW7B
20	CLK	44	SW7A
21	$\overline{\text{LE}}$	45	SW6B
22	CLR	46	SW6A
23	DOUT	47	SW5B
24	RGND	48	SW5A

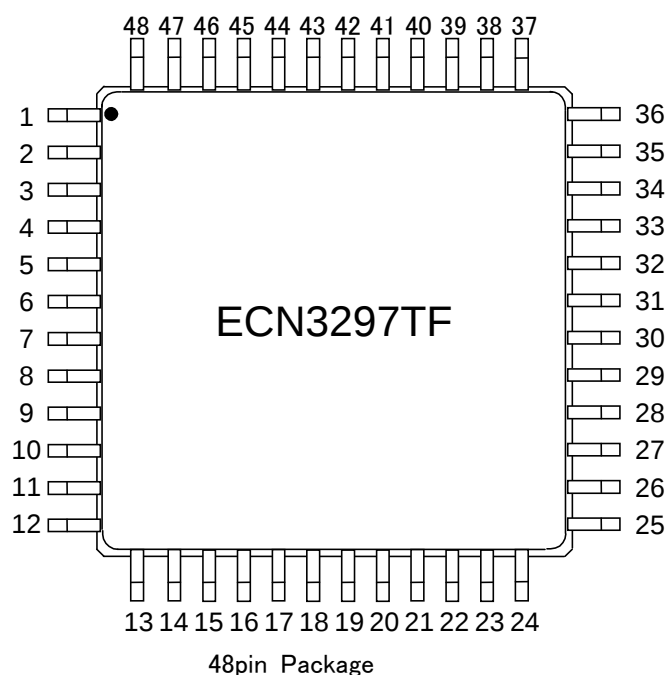
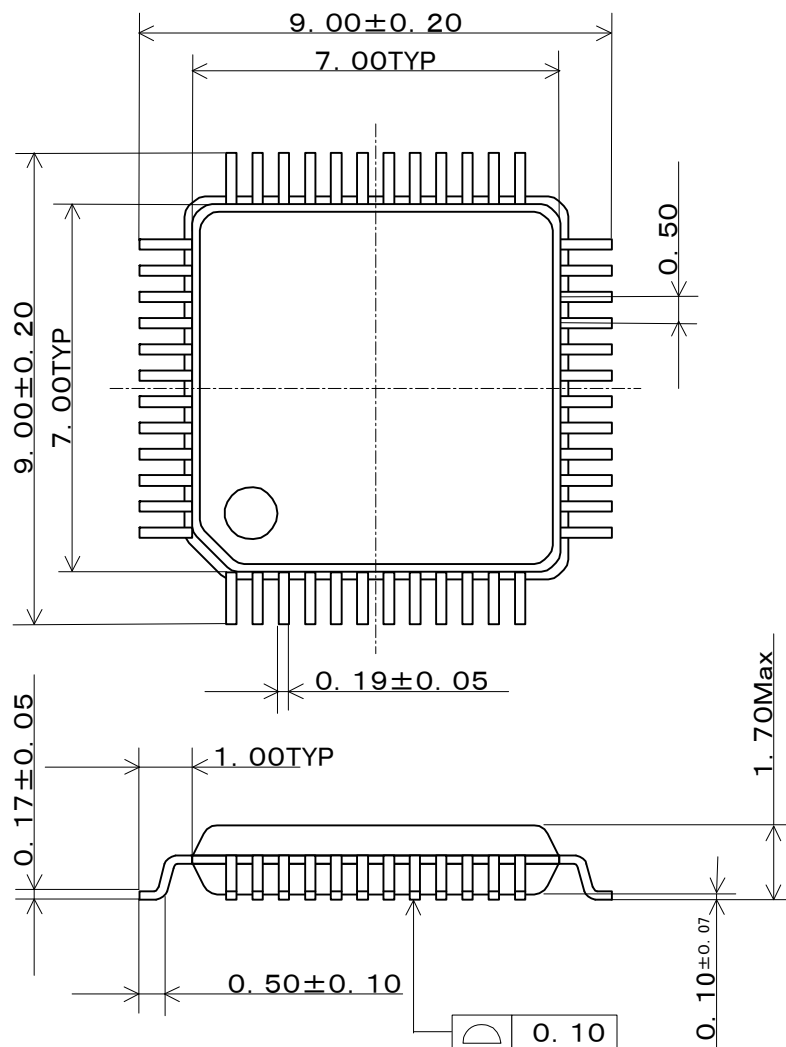


Fig. 4 Pin assignment (top View)

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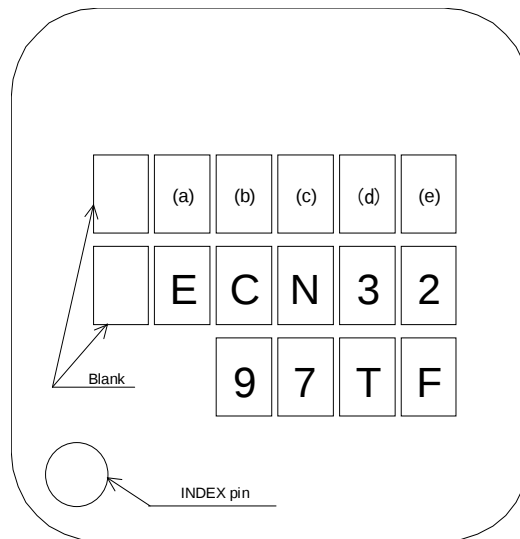
9 Package Outline

Units : mm



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10 Marking spec



Lot numbering rule

(a) :Year code (Least significant digit of Assembled year (A.D.))

(b) :Month code (Refer to following table.)

Month	1	2	3	4	5	6	7	8	9	10	11	12
Month code	A	B	C	D	E	K	L	M	N	X	Y	Z

(c),(d),(e) :Serial number within year/month code

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11 Packing Form

Packaging details are as shown below.

1.Outer and inner packing	Label IC type No. and Quantity (max 2500 IC/box) Carton tape Label IC type No. and quantity Dry pack. Inner HITACHI Outer box (cardboard box)
2.Tray	Tray Specifications Orientation of IC Enlargement Tray Index IC Index Tray dimension Unit: mm 14 positions without holes (*) (1) Material of tray is PPE containing carbon and static proof. (2) Packing quantity is max 250 IC/Tray. (3) Maximum heat resistant temperature is 150deg.

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12 Inspection

Hundred percent inspections shall be conducted on electric characteristics.

13 Important Notice

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14 Cautions

14.1 Customers are advised to follow the below cautions to protect semiconductor from electrical static discharge (ESD).

- a) IC needs to be dealt with caution to protect from damage by ESD. Material of container or any device to carry semiconductor devices should be free from ESD which may be caused by vibration while transportation. To use electric-conductive container or aluminum sheet is recommended as an effective countermeasure.
- b) Those who touch semiconductor devices such as work platform, machine and measuring and test equipment should be grounded.
- c) Workers should be grounded connecting with high impedance around 100kΩ to 1MΩ while dealing with semiconductor to avoid damaging IC by electric static discharge.
- d) Friction with other materials such as a high polymer should not be caused.
- e) Attention is needed so that electric potential will be kept on the same level by short circuit terminals when PC board with mounted IC is carried and that vibration or friction might not occur.
- f) Air conditioning is needed so that humidity should not drop.

14.2 Refer to "Precautions for Use of High-Voltage Monolithic ICs" for the other precautions and instructions on how to deal with products.

14.3 Regardless of changes in external conditions during use, "absolute maximum ratings" should never be exceeded in designing electronic circuits that employ products. In a case absolute maximum ratings are exceeded, products may be damaged or destroyed. In no event shall Hitachi be liable for any failure in products or any secondary damage resulting from use at a value exceeding the absolute maximum ratings.

14.4 Products may experience failures due to accident or unexpected surge voltages. Accordingly, adopt safe design features, such as redundancy or prevention of erroneous action, to avoid extensive damage in the event of a failure.

14.5 Products are not designed, manufactured, or warranted to be suitable for use where extremely high reliability is required (such as use in nuclear power control, aerospace and aviation, traffic equipment, life-support-related medical equipment, fuel control equipment and various kinds of safety equipment).

Inclusion of products in such application shall be fully at the risk of customers. Hitachi, Ltd. assumes no liability for applications assistance, customer product design, or performance.

In such cases it is advised customers ensure circuit and/or product safety by using semiconductor devices that assure high reliability or by means of user's fail-safe precautions or other arrangement.

(If a semiconductor device fails, there may be cases in which the semiconductor device, wiring or wiring pattern will emit smoke or cause a fire or in which the semiconductor device will burst.)

14.6 Lead-free solder is used for coating pins and the tab of this IC. Refer to "Precautions for Use of High-Voltage Monolithic ICs" for soldering conditions.

Precautions for Safe Use and Notices

If semiconductor devices are handled in inappropriate manner, failures may result. For this reason, be sure to read "Precaution for Use" before use.



This mark indicates an item about which caution is required.



CAUTION

This mark indicates a potentially hazardous situation which, if not avoided, may result in minor or moderate injury and damage to property.



CAUTION

- (1) Regardless of changes in external conditions during use "absolute maximum ratings" should never be exceed in designing electronic circuits that employ semiconductors. In the case of pulse use, furthermore, "safe operating area (SOA)" precautions should be observed.
- (2) Semiconductor devices may experience failures due to accident or unexpected surge voltages. Accordingly, adopt safe design features, such as redundancy or prevention of erroneous action, to avoid extensive damage in the event of a failure.
- (3) In cases where extremely high reliability is required (such as use in nuclear power control, aerospace and aviation, traffic equipment, life-support-related medical equipment, fuel control equipment and various kinds of safety equipment), safety should be ensured by using semiconductor devices that feature assured safety or by means of user's fail-safe precautions or other arrangement. Or consult Hitachi's sales department staff.

(If a semiconductor devices fails, there may be cases in which the semiconductor device, wiring or wiring pattern will emit smoke or cause a fire or in which the semiconductor device will burst)

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