

N- and P-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY

	V_{DS} (V)	$R_{DS(on)}$ (Ω) Max.	I_D (A) ^a	Q_g (Typ.)
N-Channel	20	0.058 at $V_{GS} = 4.5$ V	3.9	2.9 nC
		0.078 at $V_{GS} = 2.5$ V	3.3	
P-Channel	- 20	0.195 at $V_{GS} = - 4.5$ V	- 2.1	1.6 nC
		0.316 at $V_{GS} = - 2.5$ V	- 1.7	

FEATURES

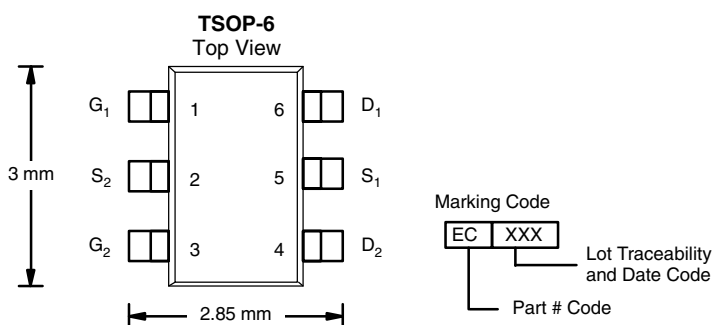
- TrenchFET[®] Power MOSFETs
- 100 % R_g Tested
- Material categorization:
For definitions of compliance please see
www.vishay.com/doc?99912



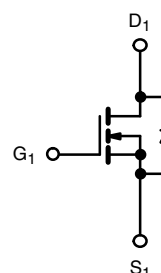
RoHS
COMPLIANT
HALOGEN
FREE

APPLICATIONS

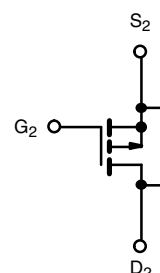
- Load Switch for Portable Devices
- DC/DC Converters
- Drivers: Motor, Solenoid, Relay



Ordering Information: Si3585CDV-T1-GE3 (Lead (Pb)-free and Halogen-free)



N-Channel MOSFET



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_A = 25$ °C, unless otherwise noted)

Parameter	Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage	V_{DS}	20	- 20	V
Gate-Source Voltage	V_{GS}	± 12		
Continuous Drain Current ($T_J = 150$ °C)	$T_C = 25$ °C	3.9	- 2.1	A
	$T_C = 70$ °C	3.1	- 1.7	
	$T_A = 25$ °C	3.5 ^{b, c}	- 1.9 ^{b, c}	
	$T_A = 70$ °C	2.8 ^{b, c}	- 1.5 ^{b, c}	
Pulsed Drain Current ($t = 300$ μ s)	I_{DM}	12	- 5	
Source Drain Current Diode Current	$T_C = 25$ °C	1.2	- 1.1	
	$T_A = 25$ °C	0.9 ^{b, c}	- 0.9 ^{b, c}	
Maximum Power Dissipation	$T_C = 25$ °C	1.4	1.3	W
	$T_C = 70$ °C	0.9	0.8	
	$T_A = 25$ °C	1.1 ^{b, c}	1.1 ^{b, c}	
	$T_A = 70$ °C	0.7 ^{b, c}	0.7 ^{b, c}	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS

Parameter		Symbol	N-Channel		P-Channel		Unit
			Typ.	Max.	Typ.	Max.	
Maximum Junction-to-Ambient ^{b, d}	t ≤ 5 s	R _{thJA}	93	110	97	115	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	75	90	78	95	

Notes:

a. Based on $T_C = 25$ °C.

b. Surface mounted on 1" x 1" FR4 board.

c. $t = 5$ s.

d. Maximum under steady state conditions is 150 °C/W for n-channel and 155 °C/W for p-channel.

SPECIFICATIONS ($T_J = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)

Parameter	Symbol	Test Conditions		Min.	Typ.	Max.	Unit	
Static								
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	N-Ch	20			V	
		V _{GS} = 0 V, I _D = - 250 μA	P-Ch	- 20				
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	I _D = 250 μA	N-Ch		15		mV/°C	
		I _D = - 250 μA	P-Ch		- 16.2			
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J	I _D = 250 μA	N-Ch		- 2.8			
		I _D = - 250 μA	P-Ch		2.5			
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	N-Ch	0.6		1.5	V	
		V _{DS} = V _{GS} , I _D = - 250 μA	P-Ch	- 0.6		- 1.5		
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 12 V	N-Ch P-Ch			± 100 ± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 20 V, V _{GS} = 0 V	N-Ch			1	μA	
		V _{DS} = - 20 V, V _{GS} = 0 V	P-Ch			- 1		
		V _{DS} = 20 V, V _{GS} = 0 V, T _J = 55 °C	N-Ch			10		
		V _{DS} = - 20 V, V _{GS} = 0 V, T _J = 55 °C	P-Ch			- 10		
On-State Drain Current ^b	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = 4.5 V	N-Ch	12			A	
		V _{DS} ≤ - 5 V, V _{GS} = - 4.5 V	P-Ch	- 5				
Drain-Source On-State Resistance ^b	R _{DS(on)}	V _{GS} = 4.5 V, I _D = 2.5 A	N-Ch		0.048	0.058	Ω	
		V _{GS} = - 4.5 V, I _D = - 1.9 A	P-Ch		0.162	0.195		
		V _{GS} = 2.5 V, I _D = 1 A	N-Ch		0.065	0.078		
		V _{GS} = - 2.5 V, I _D = - 1 A	P-Ch		0.263	0.316		
Forward Transconductance ^b	g _{fs}	V _{DS} = 10 V, I _D = 35 A	N-Ch		12		S	
		V _{DS} = - 10 V, I _D = - 1.9 A	P-Ch		1			
Dynamic ^a								
Input Capacitance	C _{iss}	N-Channel V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	N-Ch P-Ch		150 210		pF	
Output Capacitance	C _{oss}		N-Ch P-Ch		53 50			
Reverse Transfer Capacitance	C _{rss}	P-Channel V _{DS} = - 10 V, V _{GS} = 0 V, f = 1 MHz	N-Ch P-Ch		22 35			nC
			Total Gate Charge	Q _g	V _{DS} = 10 V, V _{GS} = 10 V, I _D = 3.5 A	N-Ch		
V _{DS} = - 10 V, V _{GS} = - 10 V, I _D = - 1.9 A	P-Ch				6	9		
Gate-Source Charge	Q _{gs}	N-Channel V _{DS} = 10 V, V _{GS} = 4.5 V, I _D = 3.5 A	N-Ch P-Ch		1.6 2.9	2.4 4.3		
			Gate-Drain Charge	Q _{gd}	P-Channel V _{DS} = - 10 V, V _{GS} = - 4.5 V, I _D = - 1.9 A	N-Ch P-Ch		
Gate Resistance	R _g	f = 1 MHz				N-Ch P-Ch	0.9 1.2	



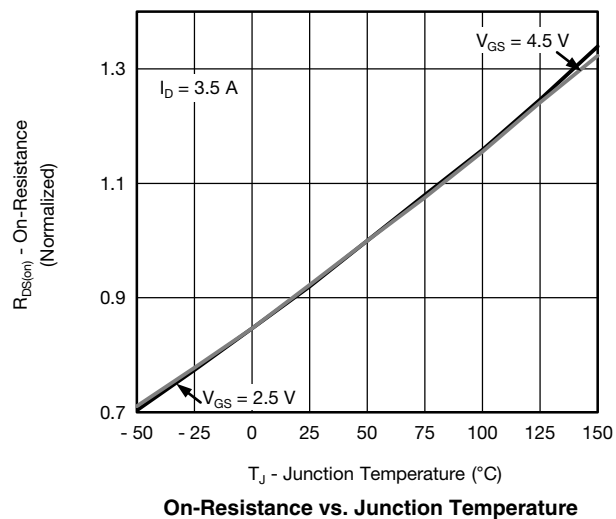
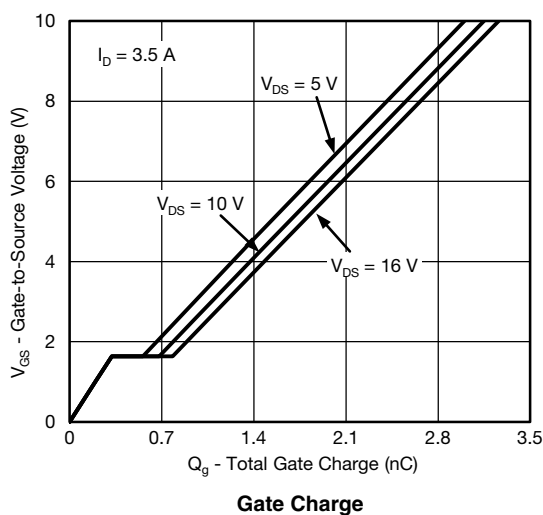
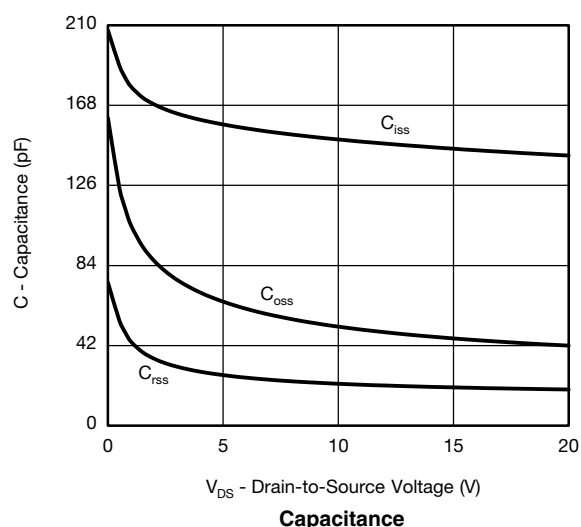
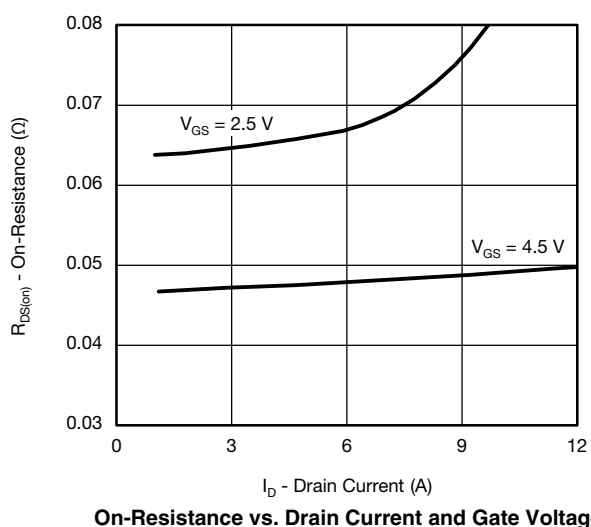
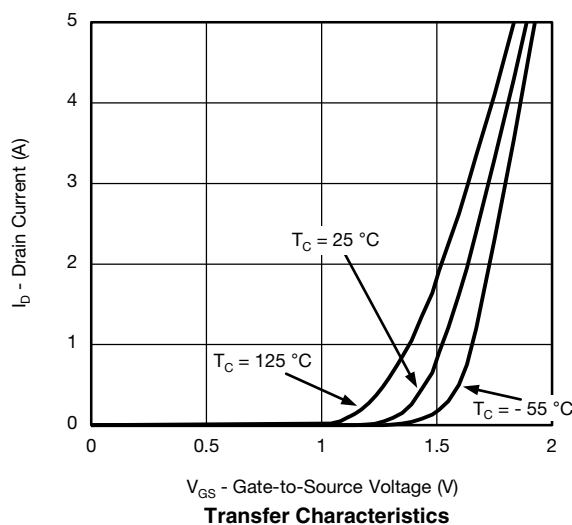
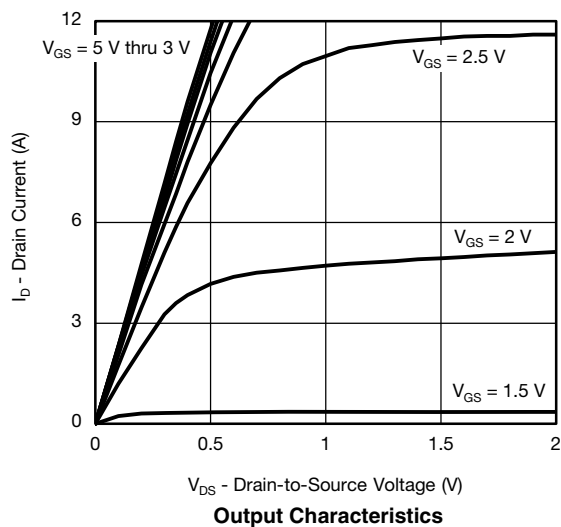
SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)							
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit	
Dynamic ^a							
Turn-On Delay Time	t _{d(on)}	N-Channel V _{DD} = 10 V, R _L = 3.6 Ω I _D ≅ 2.8 A, V _{GEN} = 10 V, R _g = 1 Ω	N-Ch		5	10	ns
			P-Ch		3	6	
Rise Time	t _r		N-Ch		20	30	
			P-Ch		10	20	
Turn-Off Delay Time	t _{d(off)}	P-Channel V _{DD} = - 10 V, R _L = 6.7 Ω I _D ≅ - 1.5 A, V _{GEN} = - 10 V, R _g = 1 Ω	N-Ch		11	20	
			P-Ch		13	20	
Fall Time	t _f		N-Ch		8	16	
			P-Ch		7	14	
Turn-On Delay Time	t _{d(on)}	N-Channel V _{DD} = 10 V, R _L = 3.6 Ω I _D ≅ 2.8 A, V _{GEN} = 4.5 V, R _g = 1 Ω	N-Ch		15	23	
			P-Ch		16	25	
Rise Time	t _r		N-Ch		37	56	
			P-Ch		16	25	
Turn-Off Delay Time	t _{d(off)}	P-Channel V _{DD} = - 10 V, R _L = 6.7 Ω I _D ≅ - 1.5 A, V _{GEN} = - 4.5 V, R _g = 1 Ω	N-Ch		25	38	
			P-Ch		13	20	
Fall Time	t _f		N-Ch		28	42	
			P-Ch		9	18	
Drain-Source Body Diode Characteristics							
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C	N-Ch			1.2	A
			P-Ch			- 1.1	
Pulse Diode Forward Current ^a	I _{SM}		N-Ch			12	
			P-Ch			- 5	
Body Diode Voltage	V _{SD}	I _S = 2.8 A, V _{GS} = 0 V	N-Ch		0.8	1.2	V
		I _S = - 1.5 A, V _{GS} = 0 V	P-Ch		- 0.8	- 1.2	
Body Diode Reverse Recovery Time	t _{rr}	N-Channel I _F = 2.8 A, dI/dt = 100 A/μs, T _J = 25 °C	N-Ch		8	16	ns
			P-Ch		21	32	
Body Diode Reverse Recovery Charge	Q _{rr}	P-Channel I _F = - 1.5 A, dI/dt = - 100 A/μs, T _J = 25 °C	N-Ch		2	4	nC
			P-Ch		11	20	
Reverse Recovery Fall Time	t _a		N-Ch		5		ns
			P-Ch		10		
Reverse Recovery Rise Time	t _b	N-Ch		3			
		P-Ch		11			

Notes:

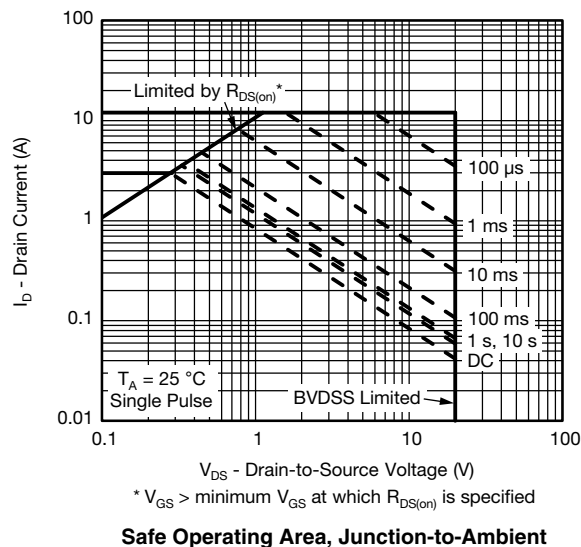
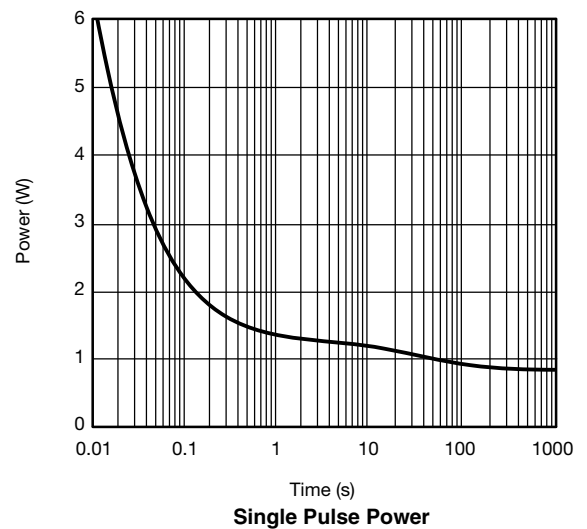
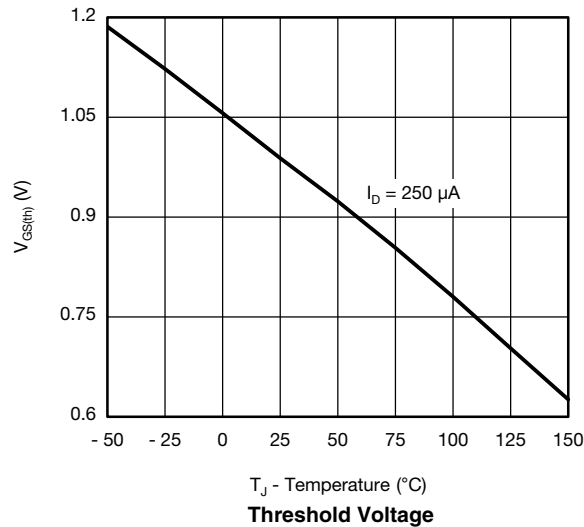
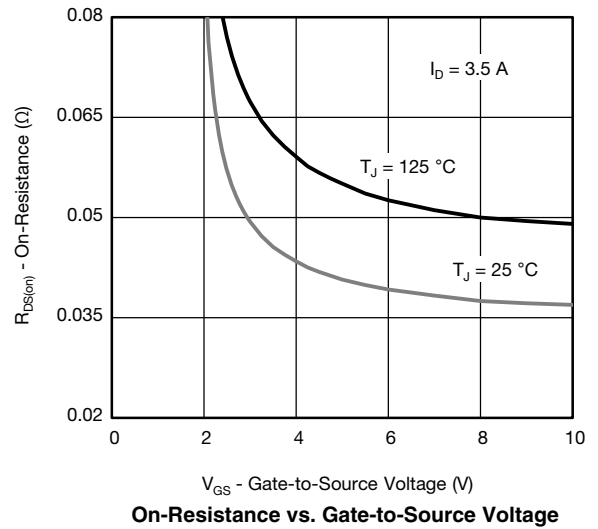
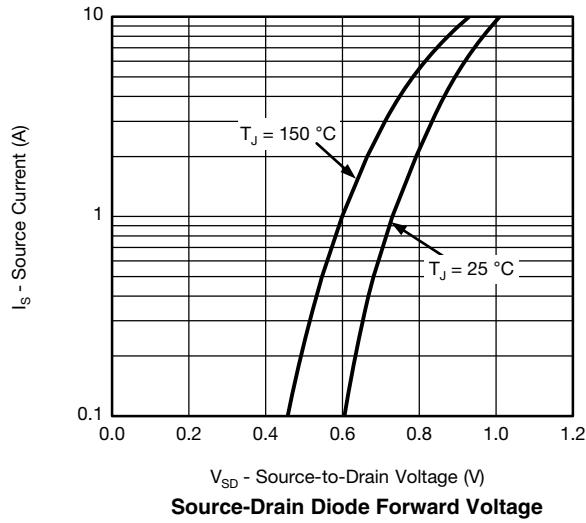
a. Guaranteed by design, not subject to production testing.

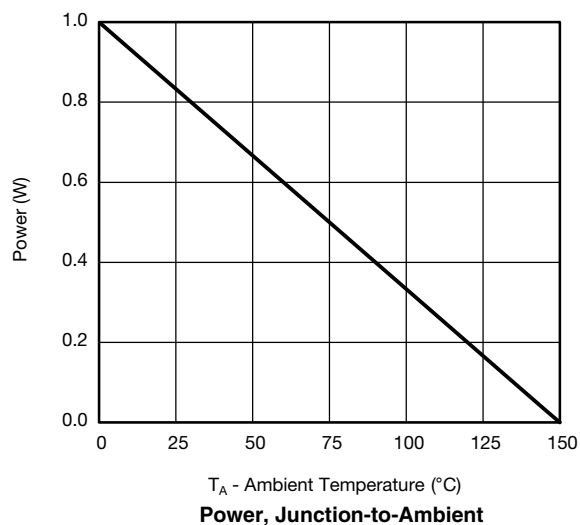
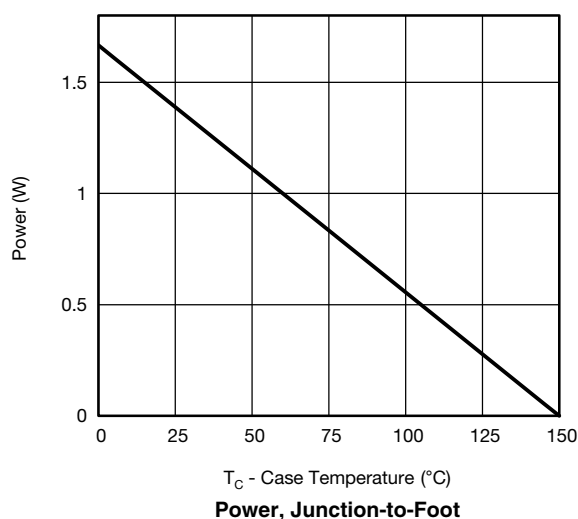
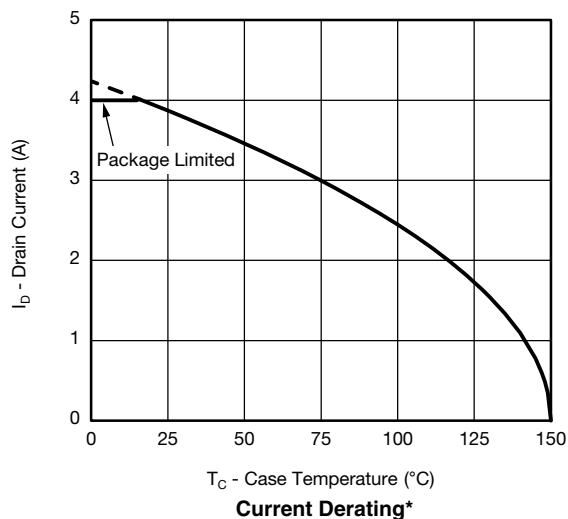
b. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

N-CHANNEL TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

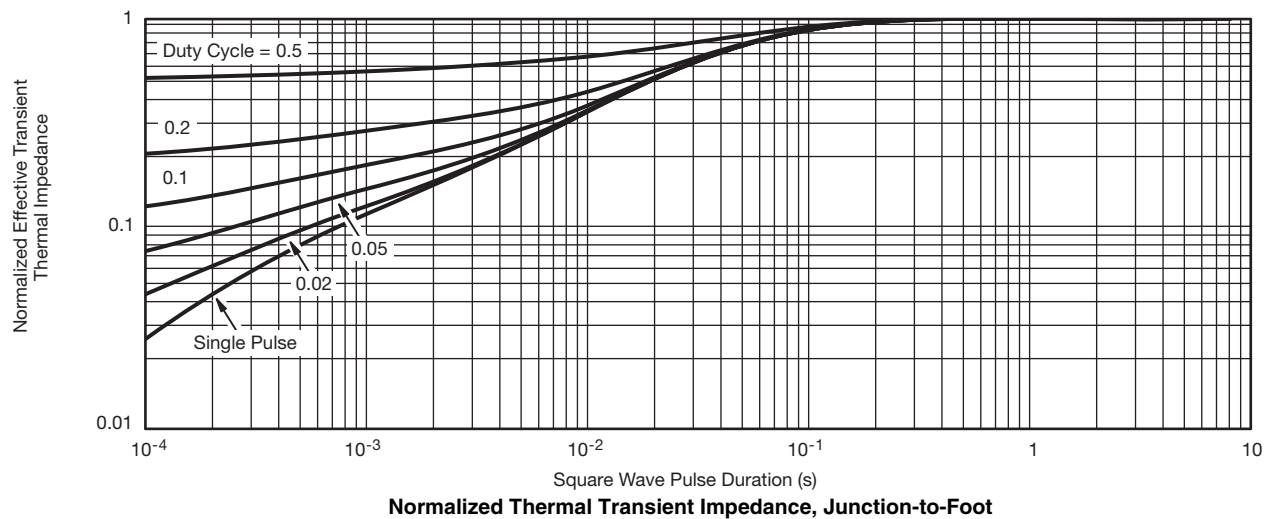
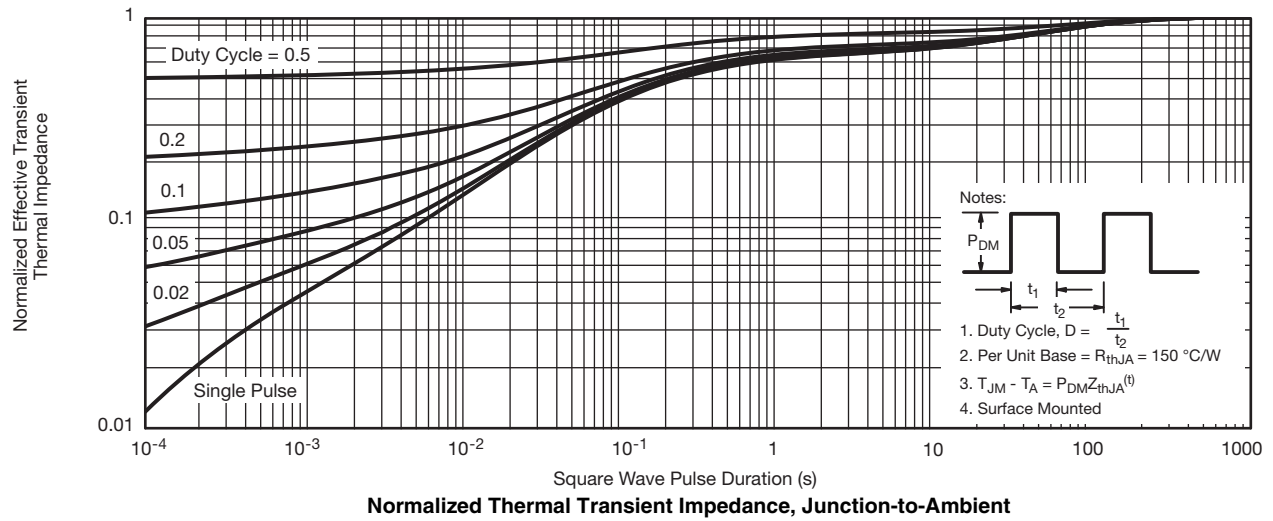
N-CHANNEL TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

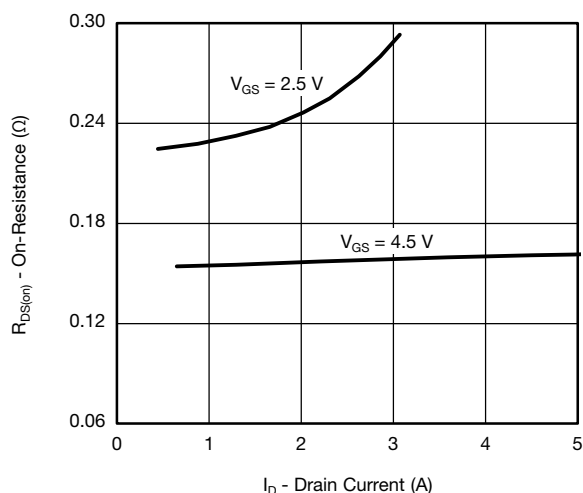
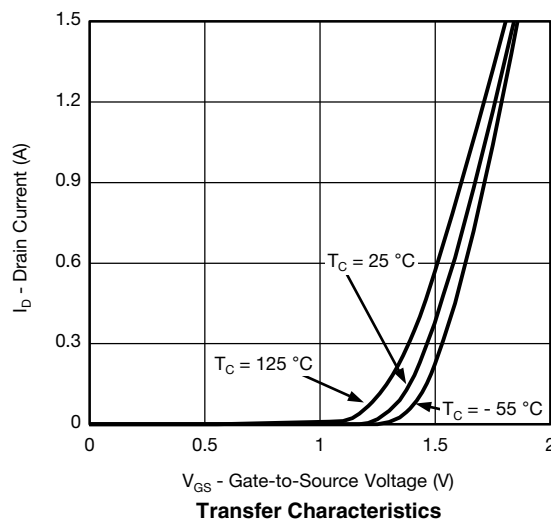
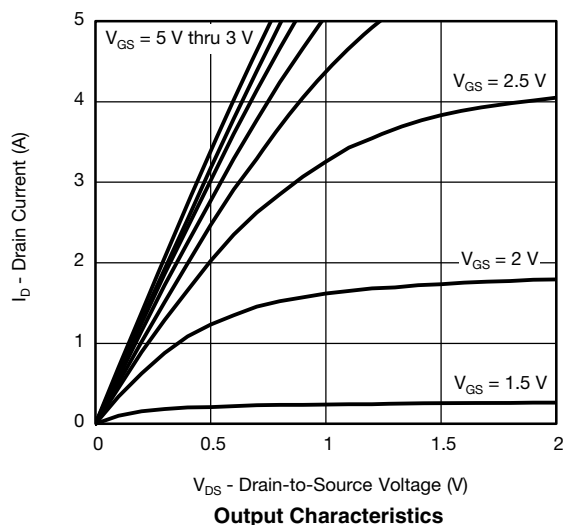
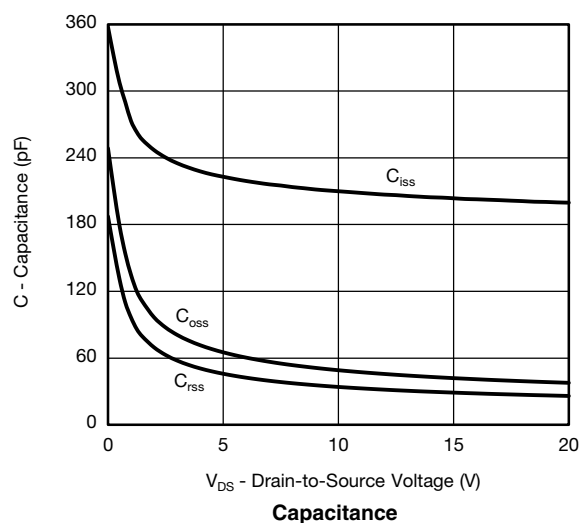
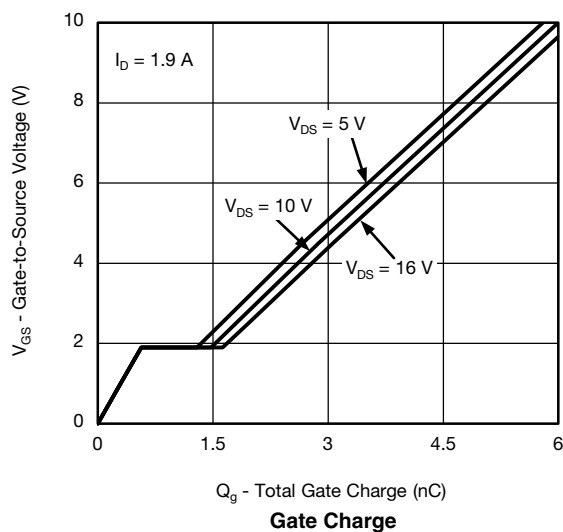
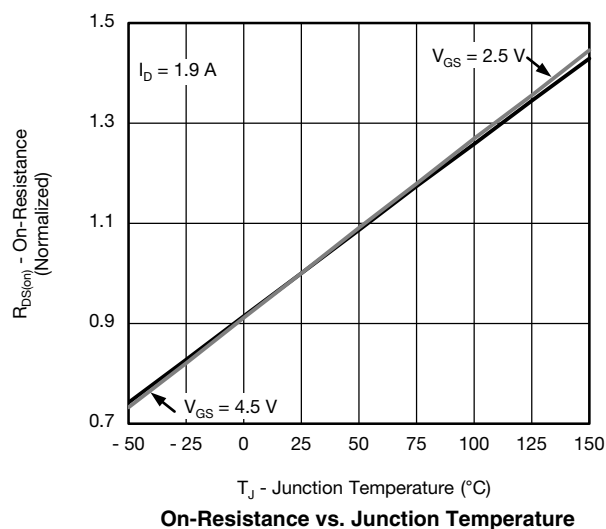


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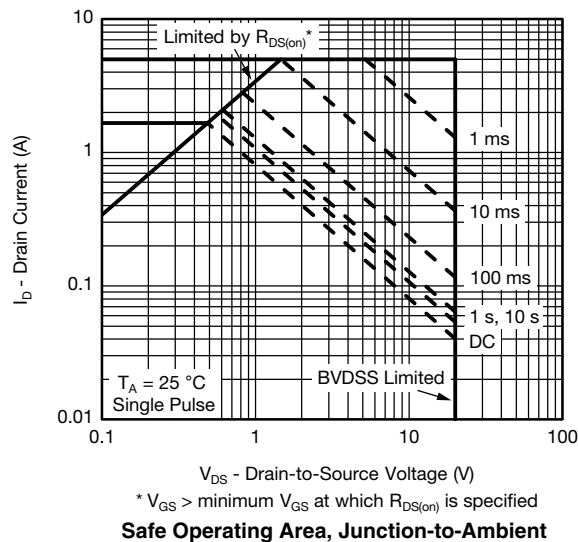
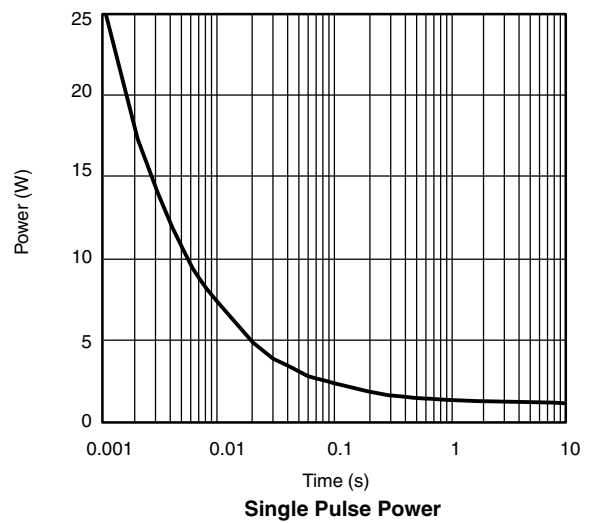
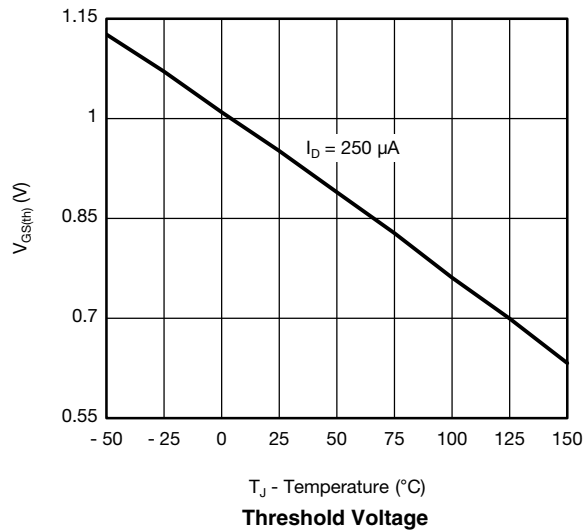
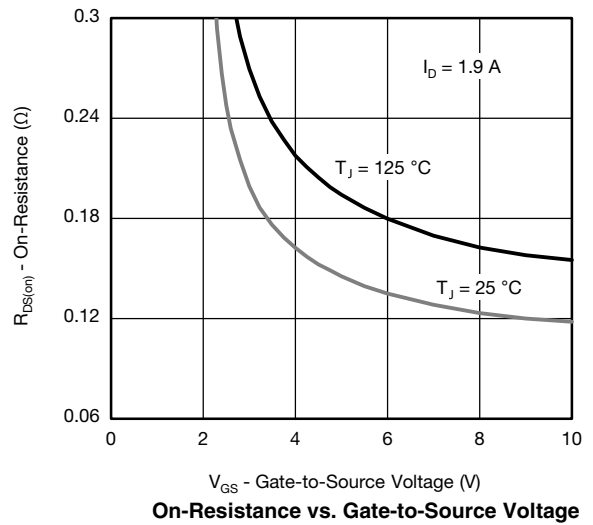
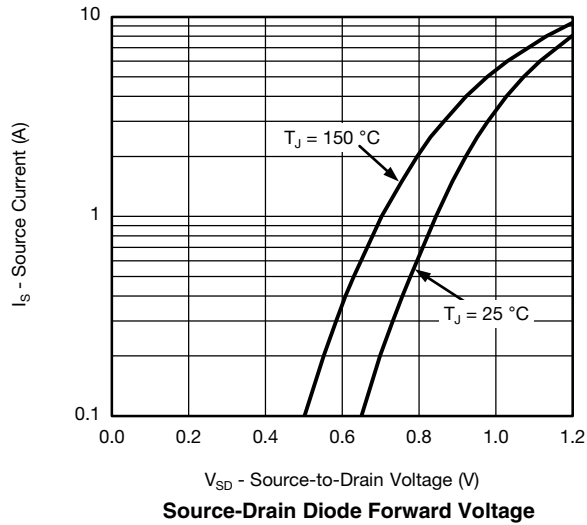
* The power dissipation P_D is based on $T_{J(max.)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

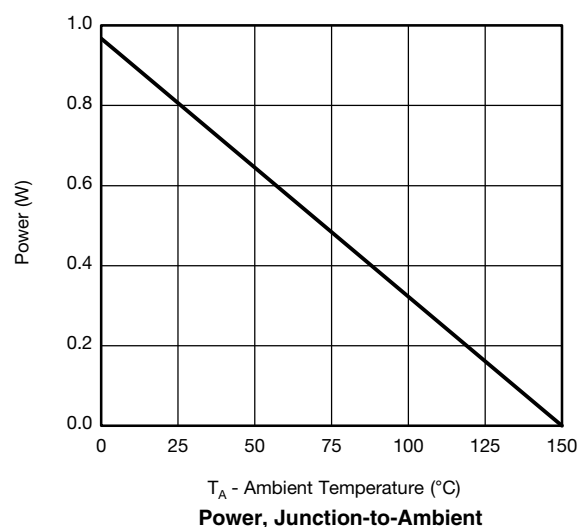
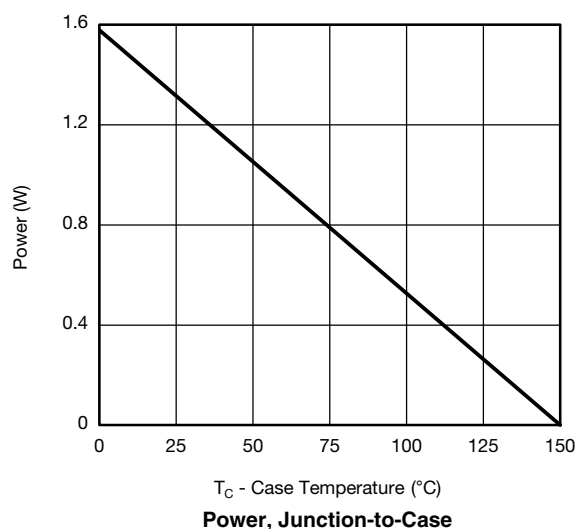
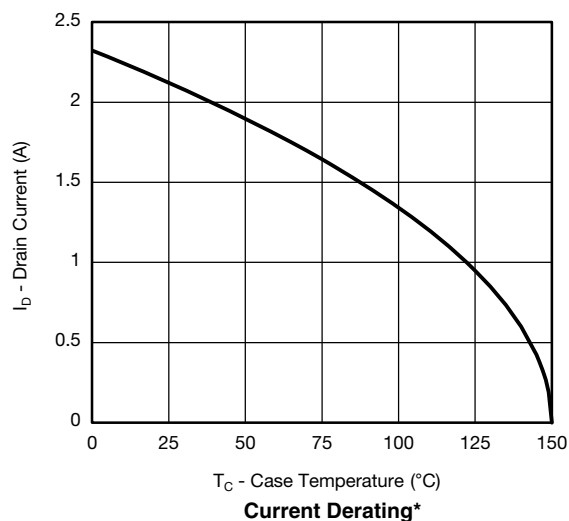
N-CHANNEL TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



P-CHANNEL TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)**On-Resistance vs. Drain Current and Gate Voltage****Capacitance****Gate Charge****On-Resistance vs. Junction Temperature**

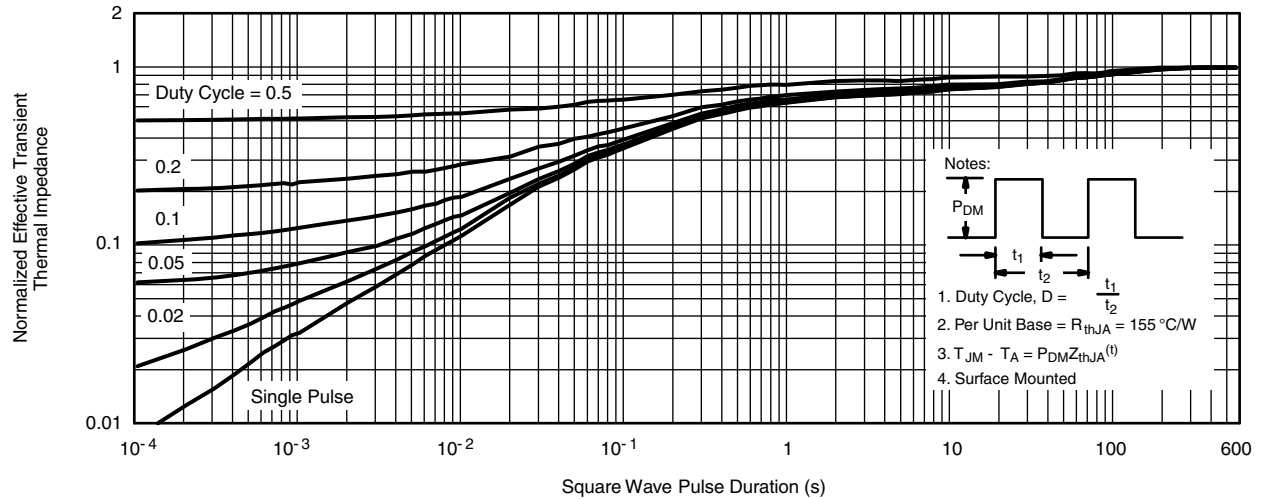
P-CHANNEL TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



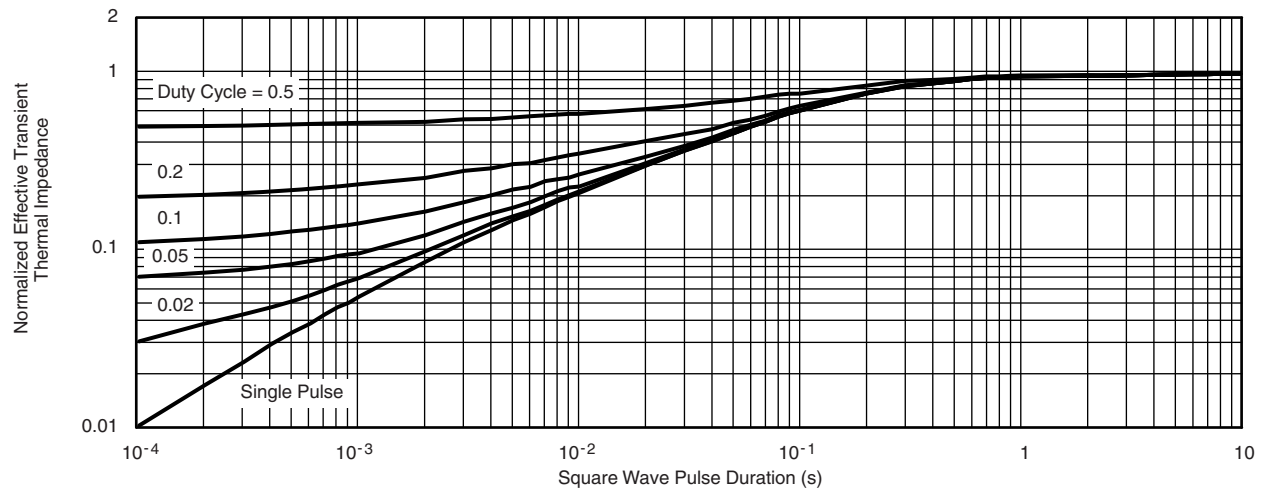
P-CHANNEL TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)


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P-CHANNEL TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



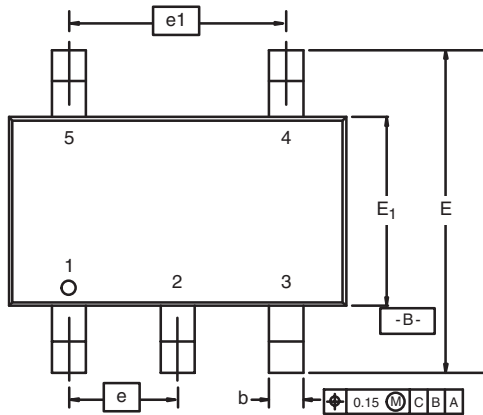
Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?67470.

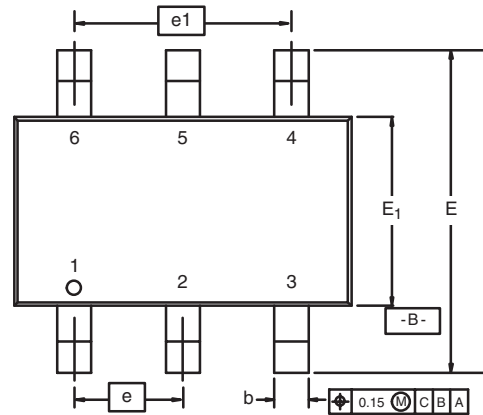


TSOP: 5/6-LEAD

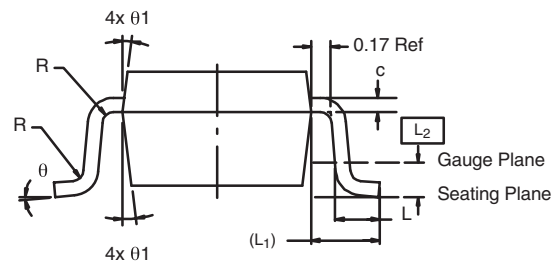
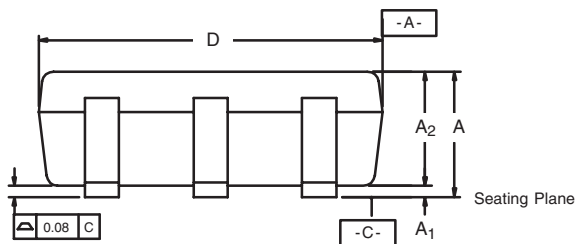
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5-LEAD TSOP

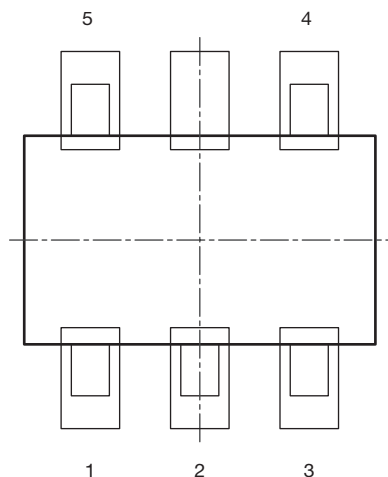


6-LEAD TSOP

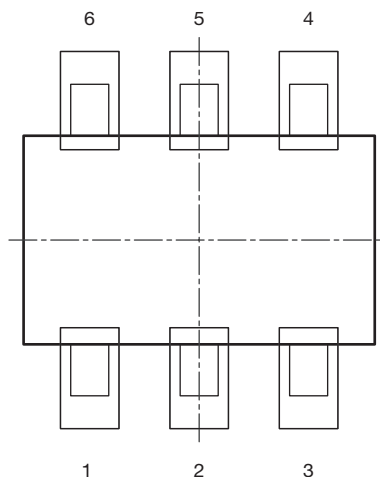


	MILLIMETERS			INCHES		
Dim	Min	Nom	Max	Min	Nom	Max
A	0.91	-	1.10	0.036	-	0.043
A ₁	0.01	-	0.10	0.0004	-	0.004
A ₂	0.90	-	1.00	0.035	0.038	0.039
b	0.30	0.32	0.45	0.012	0.013	0.018
c	0.10	0.15	0.20	0.004	0.006	0.008
D	2.95	3.05	3.10	0.116	0.120	0.122
E	2.70	2.85	2.98	0.106	0.112	0.117
E ₁	1.55	1.65	1.70	0.061	0.065	0.067
e	0.95 BSC			0.0374 BSC		
e ₁	1.80	1.90	2.00	0.071	0.075	0.079
L	0.32	-	0.50	0.012	-	0.020
L ₁	0.60 Ref			0.024 Ref		
L ₂	0.25 BSC			0.010 BSC		
R	0.10	-	-	0.004	-	-
θ	0°	4°	8°	0°	4°	8°
θ ₁	7° Nom			7° Nom		
ECN: C-06593-Rev. I, 18-Dec-06						
DWG: 5540						

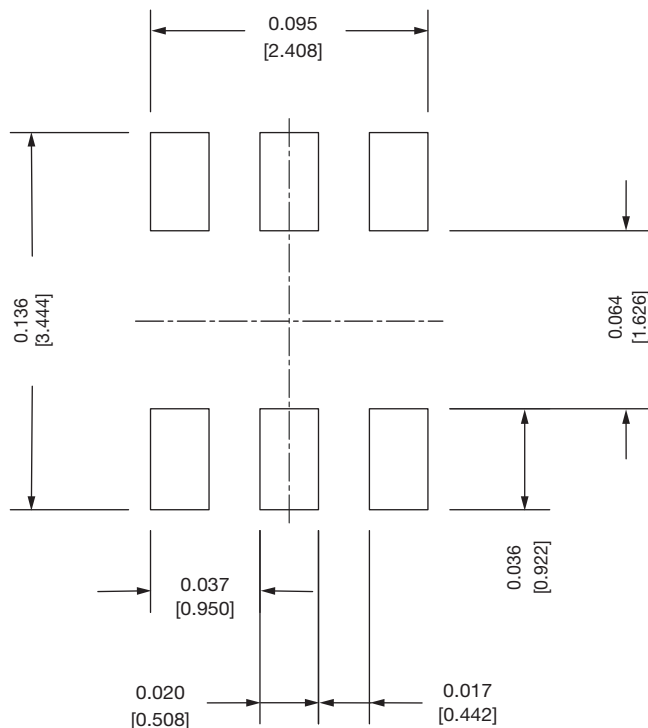
Recommended Land Pattern For TSOP-5L / TSOP-6L



TSOP 5L



TSOP 6L


Note

- All dimensions are in inches (millimeter)

ECN: C22-0860-Rev. B, 24-Oct-2022
DWG: 3010



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