

N-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY

V _{DS} (V)	R _{DS(on)} (Ω) MAX.	I _D (A) ^d	Q _g (TYP.)
20	0.030 at V _{GS} = 4.5 V	5.9	7.7 nC
	0.034 at V _{GS} = 2.5 V	5.5	
	0.041 at V _{GS} = 1.8 V	5	

FEATURES

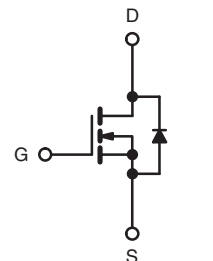
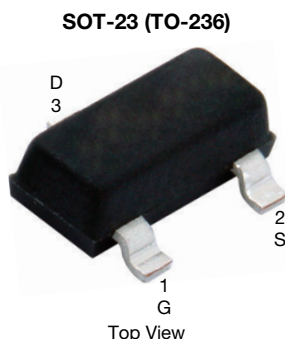
- TrenchFET® power MOSFET
- 100 % R_g tested
- Material categorization:
For definitions of compliance please see www.vishay.com/doc?99912



RoHS
COMPLIANT
HALOGEN
FREE

APPLICATIONS

- Load switch
- Power management



N-Channel MOSFET

Marking Code: F5

Ordering Information:

Si2374DS-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C, unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V _{DS}	20	V
Gate-Source Voltage	V _{GS}	± 8	
Continuous Drain Current (T _J = 150 °C)	I _D	5.9	A
		4.7	
		4.5 ^{a, b}	
		3.6 ^{a, b}	
Pulsed Drain Current (t = 100 μs)	I _{DM}	25	
Continuous Source-Drain Diode Current	I _S	1.4	
		0.8 ^{a, b}	
Maximum Power Dissipation	P _D	1.7	W
		1.1	
		0.96 ^{a, b}	
		0.62 ^{a, b}	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	TYPICAL	MAXIMUM	UNIT
Maximum Junction-to-Ambient ^{a, c}	R _{thJA}	100	130	°C/W
Maximum Junction-to-Foot (Drain)	R _{thJF}	60	75	

Notes

- Surface mounted on 1" x 1" FR4 board.
- t = 5 s.
- Maximum under steady state conditions is 175 °C/W.
- T_C = 25 °C.



SPECIFICATIONS ($T_J = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0\text{ V}$, $I_D = 250\text{ }\mu\text{A}$	20	-	-	V
V_{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	$I_D = 250\text{ }\mu\text{A}$	-	34	-	mV/ $^{\circ}\text{C}$
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}/T_J$		-	-5	-	
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 250\text{ }\mu\text{A}$	0.4	-	1	V
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0\text{ V}$, $V_{GS} = \pm 8\text{ V}$	-	-	± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 20\text{ V}$, $V_{GS} = 0\text{ V}$	-	-	1	μA
		$V_{DS} = 20\text{ V}$, $V_{GS} = 0\text{ V}$, $T_J = 55\text{ }^{\circ}\text{C}$	-	-	10	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} \geq 5\text{ V}$, $V_{GS} = 10\text{ V}$	10	-	-	A
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = 4.5\text{ V}$, $I_D = 4\text{ A}$	-	0.025	0.030	Ω
		$V_{GS} = 2.5\text{ V}$, $I_D = 3\text{ A}$	-	0.028	0.034	
		$V_{GS} = 1.8\text{ V}$, $I_D = 2\text{ A}$	-	0.031	0.041	
Forward Transconductance ^a	g_{fs}	$V_{DS} = 10\text{ V}$, $I_D = 4\text{ A}$	-	29	-	S
Dynamic ^b						
Input Capacitance	C_{iss}	$V_{DS} = 10\text{ V}$, $V_{GS} = 0\text{ V}$, $f = 1\text{ MHz}$	-	735	-	pF
Output Capacitance	C_{oss}		-	110	-	
Reverse Transfer Capacitance	C_{rss}		-	40	-	
Total Gate Charge	Q_g	$V_{DS} = 10\text{ V}$, $V_{GS} = 10\text{ V}$, $I_D = 4.5\text{ A}$	-	13.4	20	nC
Gate-Source Charge	Q_{gs}	$V_{DS} = 10\text{ V}$, $V_{GS} = 4.5\text{ V}$, $I_D = 4.5\text{ A}$	-	7.7	12	
Gate-Drain Charge	Q_{gd}		-	1	-	
Gate Resistance	R_g		-	1	-	
Turn-On Delay Time	$t_{d(on)}$	$f = 1\text{ MHz}$	0.12	0.6	1.2	Ω
Rise Time	t_r		-	4	8	ns
Turn-Off Delay Time	$t_{d(off)}$		-	22	33	
Fall Time	t_f		-	16	24	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 10\text{ V}$, $R_L = 2.8\text{ }\Omega$ $I_D \cong 3.6\text{ A}$, $V_{GEN} = 8\text{ V}$, $R_g = 1\text{ }\Omega$	-	9	18	
Rise Time	t_r		-	10	20	
Turn-Off Delay Time	$t_{d(off)}$		-	23	35	
Fall Time	t_f		-	16	24	
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I_S	$T_C = 25\text{ }^{\circ}\text{C}$	-	-	1.4	A
Pulse Diode Forward Current ($t = 100\text{ }\mu\text{s}$)	I_{SM}		-	-	25	
Body Diode Voltage	V_{SD}	$I_S = 3.6\text{ A}$, $V_{GS} = 0\text{ V}$	-	0.8	1.2	V
Body Diode Reverse Recovery Time	t_{rr}	$I_F = 3.6\text{ A}$, $dI/dt = 100\text{ A}/\mu\text{s}$, $T_J = 25\text{ }^{\circ}\text{C}$	-	13	20	ns
Body Diode Reverse Recovery Charge	Q_{rr}		-	6	12	nC
Reverse Recovery Fall Time	t_a		-	9	-	ns
Reverse Recovery Rise Time	t_b		-	4	-	

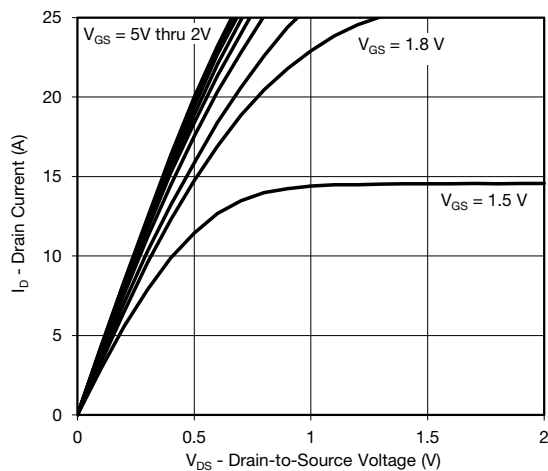
Notes

- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
b. Guaranteed by design, not subject to production testing.

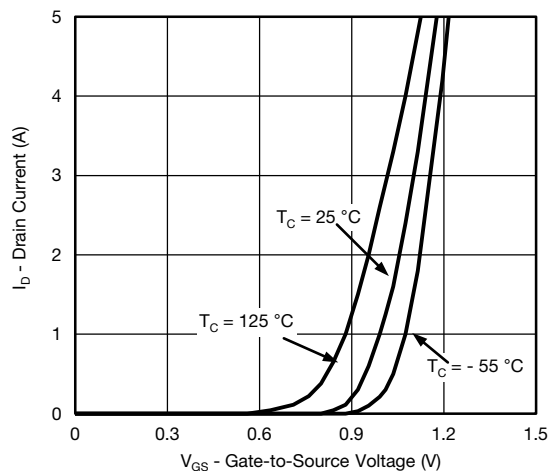
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



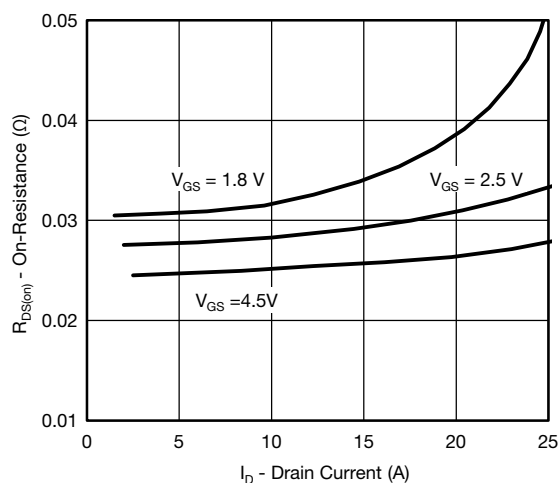
TYPICAL CHARACTERISTICS ($T_J = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)



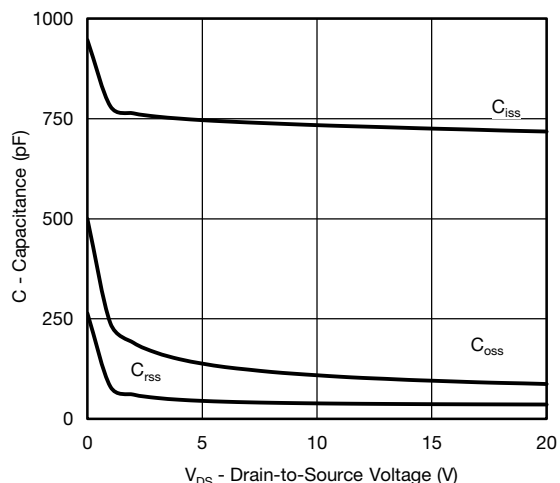
Output Characteristics



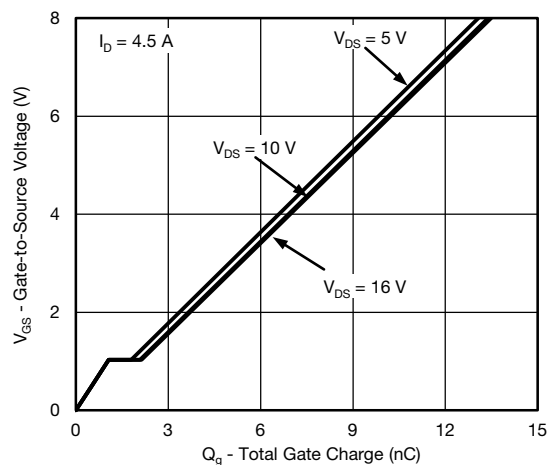
Transfer Characteristics



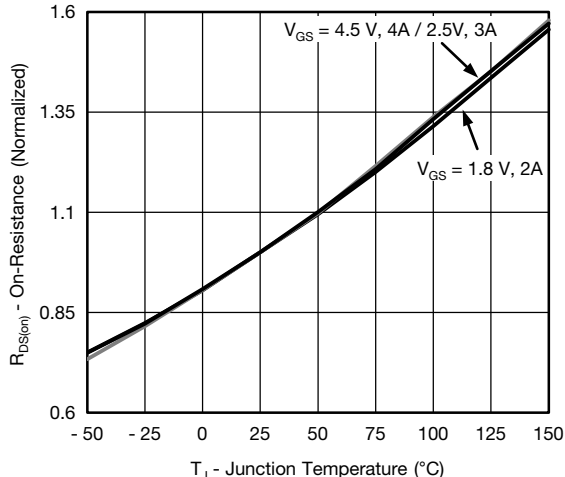
On-Resistance vs. Drain Current and Gate Voltage



Capacitance



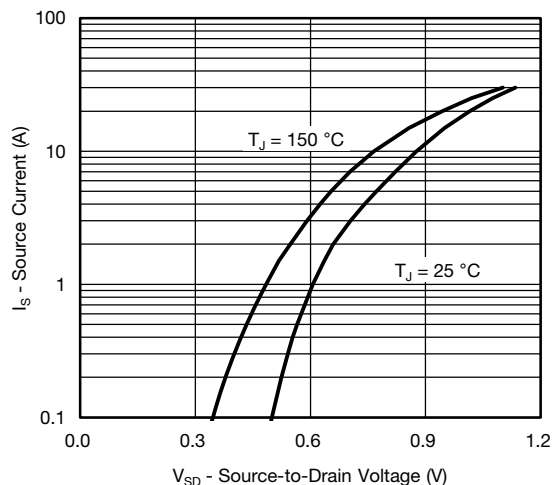
Gate Charge



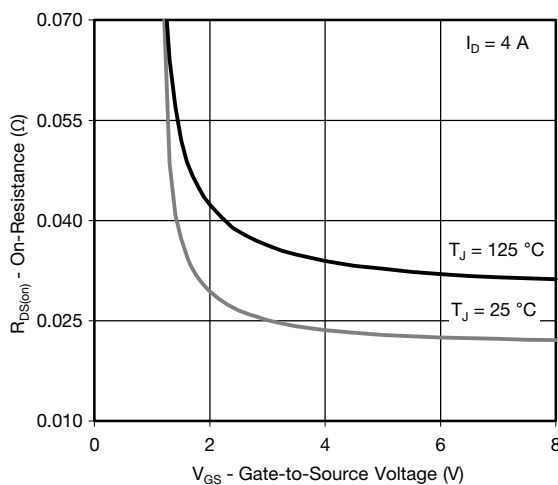
On-Resistance vs. Junction Temperature



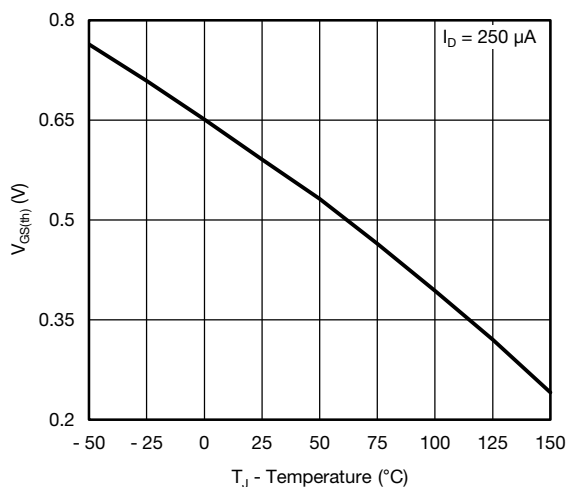
TYPICAL CHARACTERISTICS ($T_J = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)



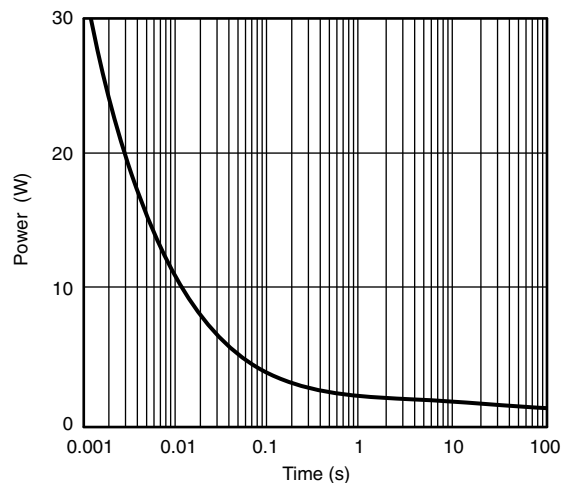
Source-Drain Diode Forward Voltage



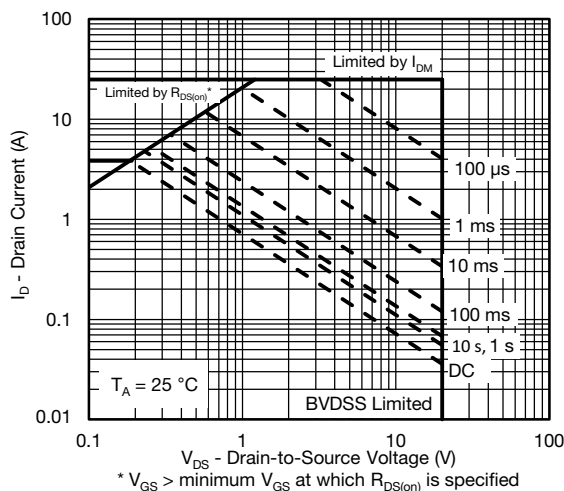
On-Resistance vs. Gate-to-Source Voltage



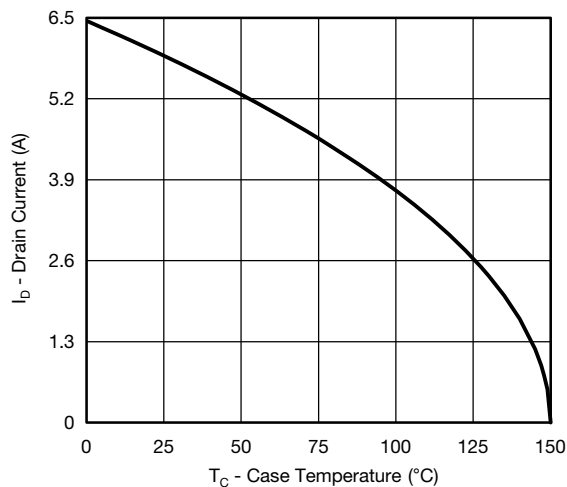
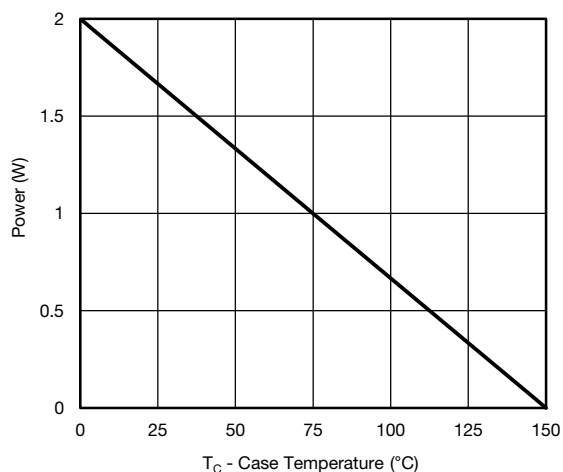
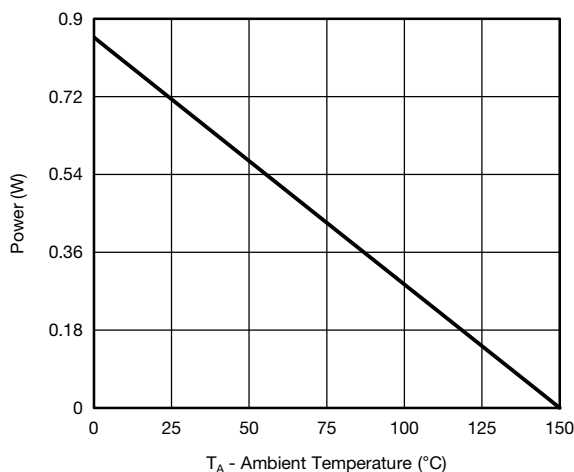
Threshold Voltage



Single Pulse Power (Junction-to-Ambient)



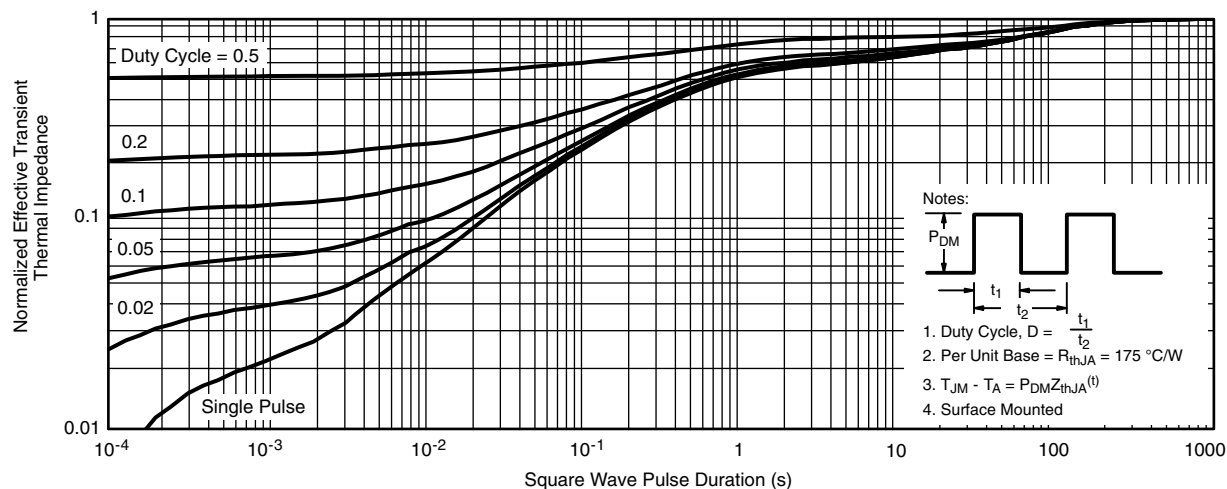
Safe Operating Area, Junction-to-Ambient

TYPICAL CHARACTERISTICS ($T_J = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)

Current Derating*

Power Junction-to-Foot

Power Junction-to-Ambient

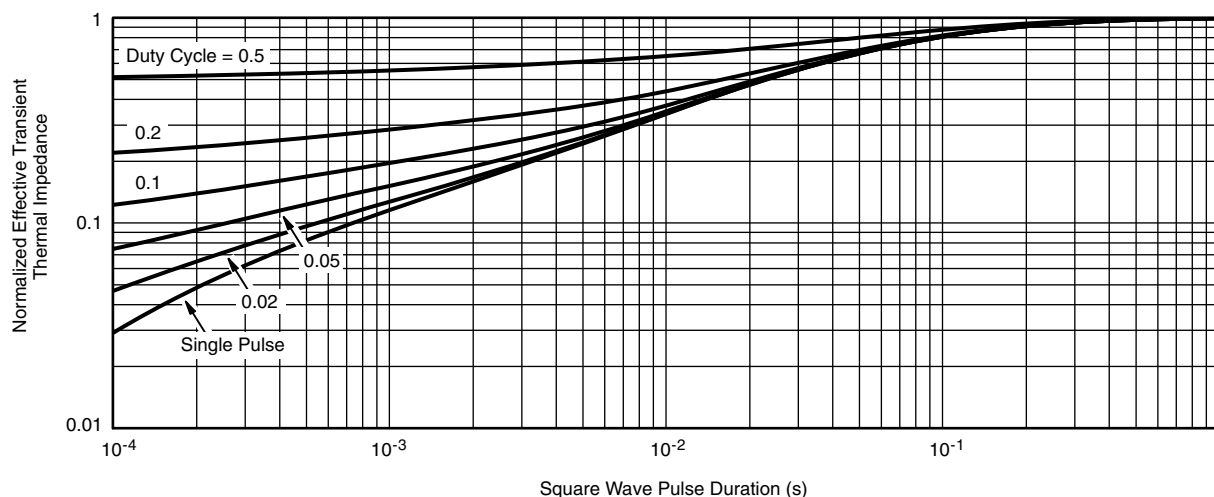
* The power dissipation P_D is based on $T_{J(max)} = 150\text{ }^{\circ}\text{C}$, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



TYPICAL CHARACTERISTICS ($T_J = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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SOT-23 (TO-236): 3-LEAD



Dim	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	0.89	1.12	0.035	0.044
A ₁	0.01	0.10	0.0004	0.004
A ₂	0.88	1.02	0.0346	0.040
b	0.35	0.50	0.014	0.020
c	0.085	0.18	0.003	0.007
D	2.80	3.04	0.110	0.120
E	2.10	2.64	0.083	0.104
E ₁	1.20	1.40	0.047	0.055
e	0.95 BSC		0.0374 Ref	
e ₁	1.90 BSC		0.0748 Ref	
L	0.40	0.60	0.016	0.024
L ₁	0.64 Ref		0.025 Ref	
S	0.50 Ref		0.020 Ref	
q	3°	8°	3°	8°
ECN: S-03946-Rev. K, 09-Jul-01				
DWG: 5479				

RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads
Dimensions in Inches/(mm)

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