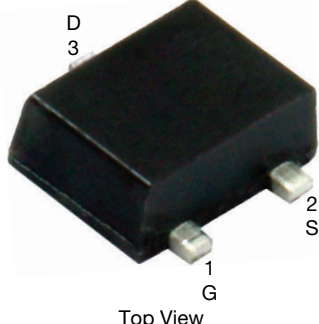


N-Channel 20 V (D-S) MOSFET

SC-89 (3 leads)



Top View

Marking code: J

PRODUCT SUMMARY

V_{DS} (V)	20
$R_{DS(on)}$ max. (Ω) at $V_{GS} = 4.5$ V	0.420
$R_{DS(on)}$ max. (Ω) at $V_{GS} = 2.5$ V	0.492
$R_{DS(on)}$ max. (Ω) at $V_{GS} = 1.8$ V	0.597
$R_{DS(on)}$ max. (Ω) at $V_{GS} = 1.5$ V	0.762
Q_g typ. (nC)	1
I_D (A)	0.53
Configuration	Single

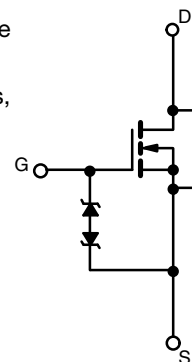
FEATURES

- TrenchFET® power MOSFET
- Gate-source ESD protected: 1000 V
- Material categorization:
for definitions of compliance please see
www.vishay.com/doc?99912


RoHS
COMPLIANT
HALOGEN
FREE

APPLICATIONS

- Load / power switching for portable devices
- Drivers: relays, solenoids, lamps, hammers, displays, memories
- Battery operated systems
- Power supply converter circuits



N-Channel MOSFET

ORDERING INFORMATION

Package	SC-89
Lead (Pb)-free and halogen-free	Si1062X-T1-GE3

ABSOLUTE MAXIMUM RATINGS ($T_A = 25$ °C, unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-source voltage	V_{DS}	20	V
Gate-source voltage	V_{GS}	± 8	V
Continuous drain current ($T_J = 150$ °C) ^a	$T_A = 25$ °C	I_D 0.53 ^{a, b}	A
	$T_A = 70$ °C	0.43 ^{a, b}	
Pulsed drain current ($t = 300$ μ s)	I_{DM}	2	A
Continuous source-drain diode current	$T_A = 25$ °C	I_S 0.18 ^{a, b}	W
Maximum power dissipation ^a	$T_A = 25$ °C	P_D 0.22 ^{a, b}	
	$T_A = 70$ °C	0.14 ^{a, b}	
Operating junction and storage temperature range	T_J, T_{stg}	-55 to +150	°C

THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	TYP.	MAX.	UNIT
Maximum junction-to-ambient ^b	R_{thJA}	440	530	°C/W
		540	650	

Notes

- a. Surface mounted on 1" x 1" FR4 board
b. $t = 5$ s

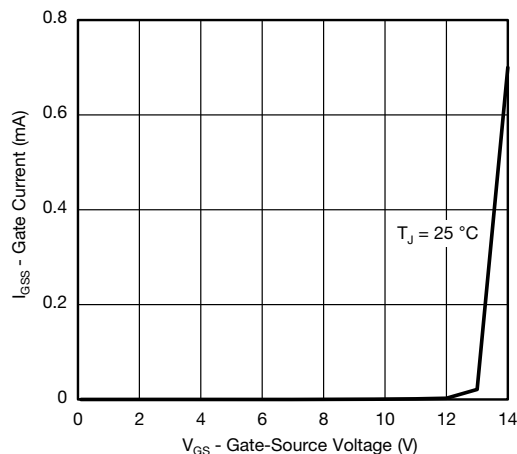
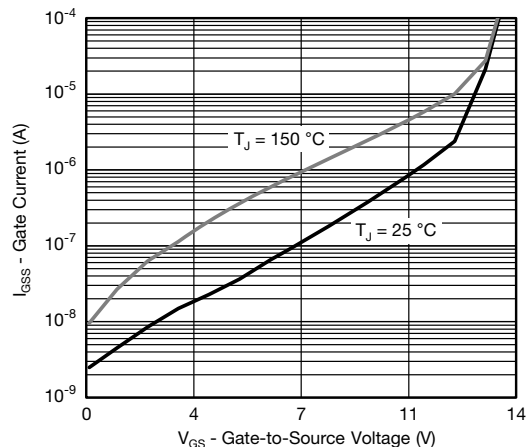
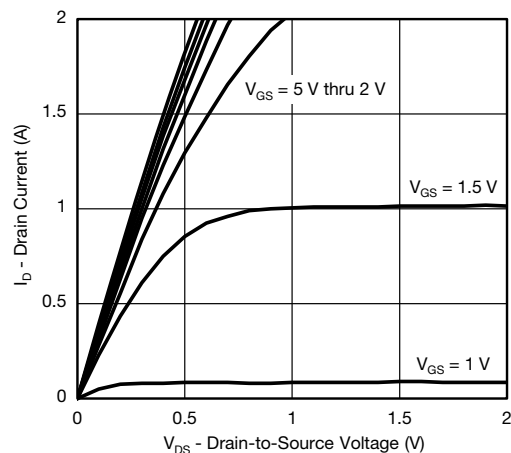
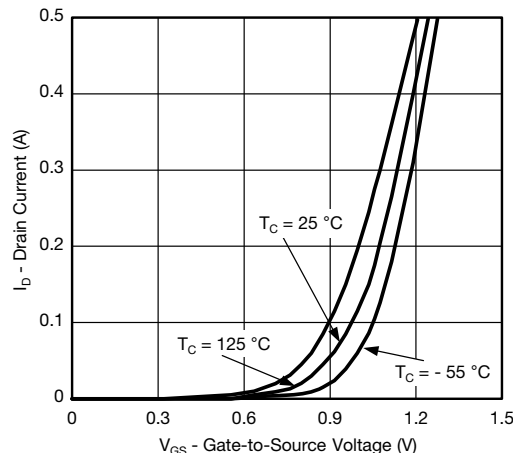
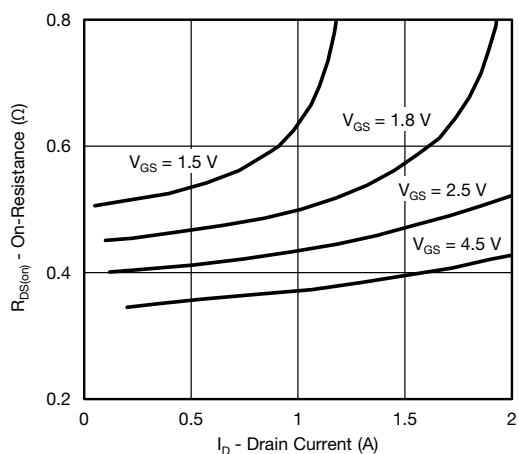
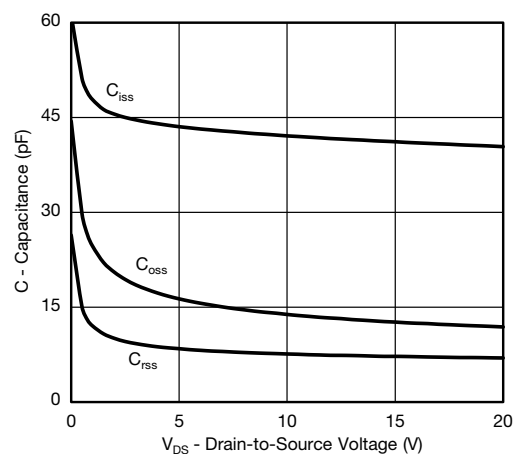


SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-source breakdown voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	20	-	-	V
V _{DS} temperature coefficient	ΔV _{DS} /T _J	I _D = 250 μA	-	11	-	mV/°C
V _{GS(th)} temperature coefficient	ΔV _{GS(th)} /T _J		-	-1.8	-	
Gate-source threshold voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	0.4	-	1	V
Gate-source leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 8 V	-	-	± 30	μA
		V _{DS} = 0 V, V _{GS} = ± 4.5 V	-	-	± 1	
Zero gate voltage drain current	I _{DSS}	V _{DS} = 20 V, V _{GS} = 0 V	-	-	1	
		V _{DS} = 20 V, V _{GS} = 0 V, T _J = 85 °C	-	-	10	
On-state drain current ^a	I _{D(on)}	V _{DS} = ≥ 5 V, V _{GS} = 4.5 V	2	-	-	A
Drain-source on-state resistance ^a	R _{DS(on)}	V _{GS} = 4.5 V, I _D = 0.5 A	-	0.350	0.420	Ω
		V _{GS} = 2.5 V, I _D = 0.2 A	-	0.410	0.492	
		V _{GS} = 1.8 V, I _D = 0.2 A	-	0.459	0.597	
		V _{GS} = 1.5 V, I _D = 0.05 A	-	0.510	0.762	
Forward transconductance	g _{fs}	V _{DS} = 10 V, I _D = 0.5 A	-	7.5	-	S
Dynamic ^b						
Input capacitance	C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	-	43	-	pF
Output capacitance	C _{oss}		-	14	-	
Reverse transfer capacitance	C _{rss}		-	8	-	
Total gate charge	Q _g	V _{DS} = 10 V, V _{GS} = 8 V, I _D = 0.5 A	-	1.8	2.7	nC
		V _{DS} = 10 V, V _{GS} = 4.5 V, I _D = 0.5 A	-	1	2	
Gate-source charge	Q _{gs}		-	0.16	-	
Gate-drain charge	Q _{gd}		-	0.13	-	
Gate resistance	R _g	f = 1 MHz	-	12.2	-	Ω
Turn-on delay time	t _{d(on)}	V _{DD} = 10 V, R _L = 20 Ω, I _D ≅ 0.4 A, V _{GEN} = 4.5 V, R _g = 1 Ω	-	2	4	ns
Rise time	t _r		-	14	24	
Turn-off delay time	t _{d(off)}		-	16	30	
Fall time	t _f		-	11	20	
Drain-Source Body Diode Characteristics						
Pulse diode forward current ^a	I _{SM}		-	-	2	A
Body diode voltage	V _{SD}	I _S = 0.4 A	-	0.8	1.2	V
Body diode reverse recovery time	t _{rr}	I _F = 0.4 A, di/dt = 100 A/μs	-	10	15	ns
Body diode reverse recovery charge	Q _{rr}		-	2	4	nC
Reverse recovery fall time	t _a		-	5	-	ns
Reverse recovery rise time	t _b		-	5	-	

Notes

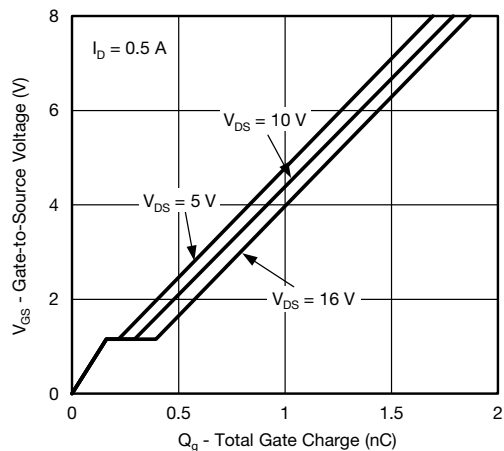
- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$
b. Guaranteed by design, not subject to production testing

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

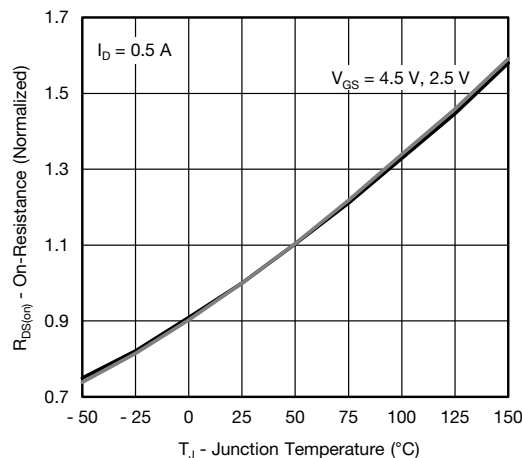
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

Gate Current vs. Gate-Source Voltage

Gate Current vs. Gate-Source Voltage

Output Characteristics

Transfer Characteristics

On-Resistance vs. Drain Current

Capacitance



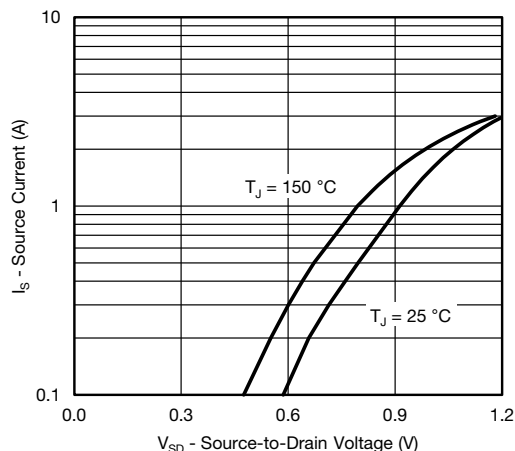
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



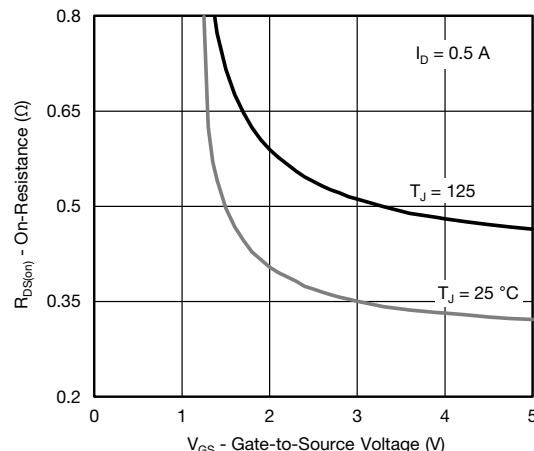
Gate Charge



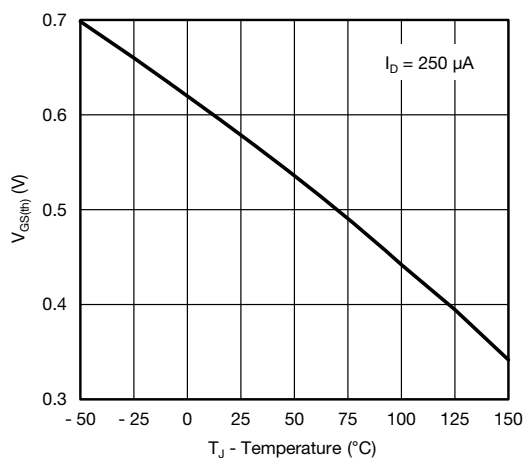
On-Resistance vs. Junction Temperature



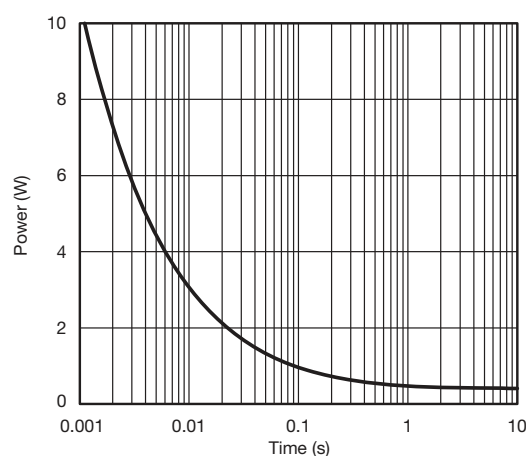
Source-Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage



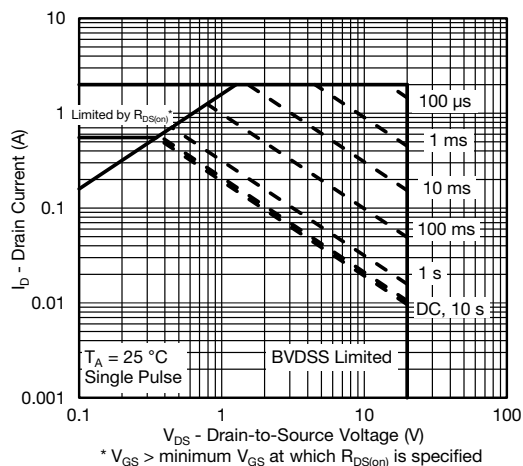
Threshold Voltage



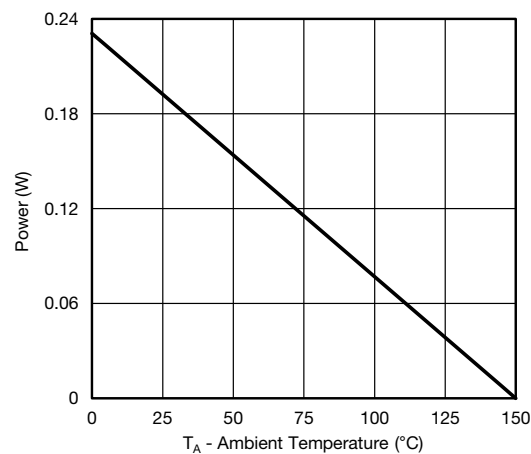
Single Pulse Power, Junction-to-Ambient



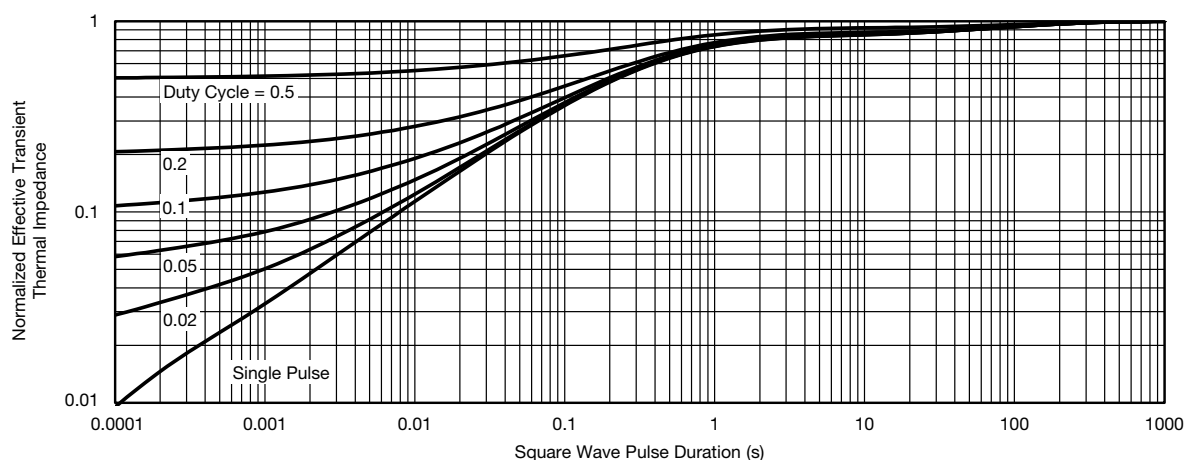
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Safe Operating Area, Junction-to-Ambient



Power Derating, Junction-to-Ambient

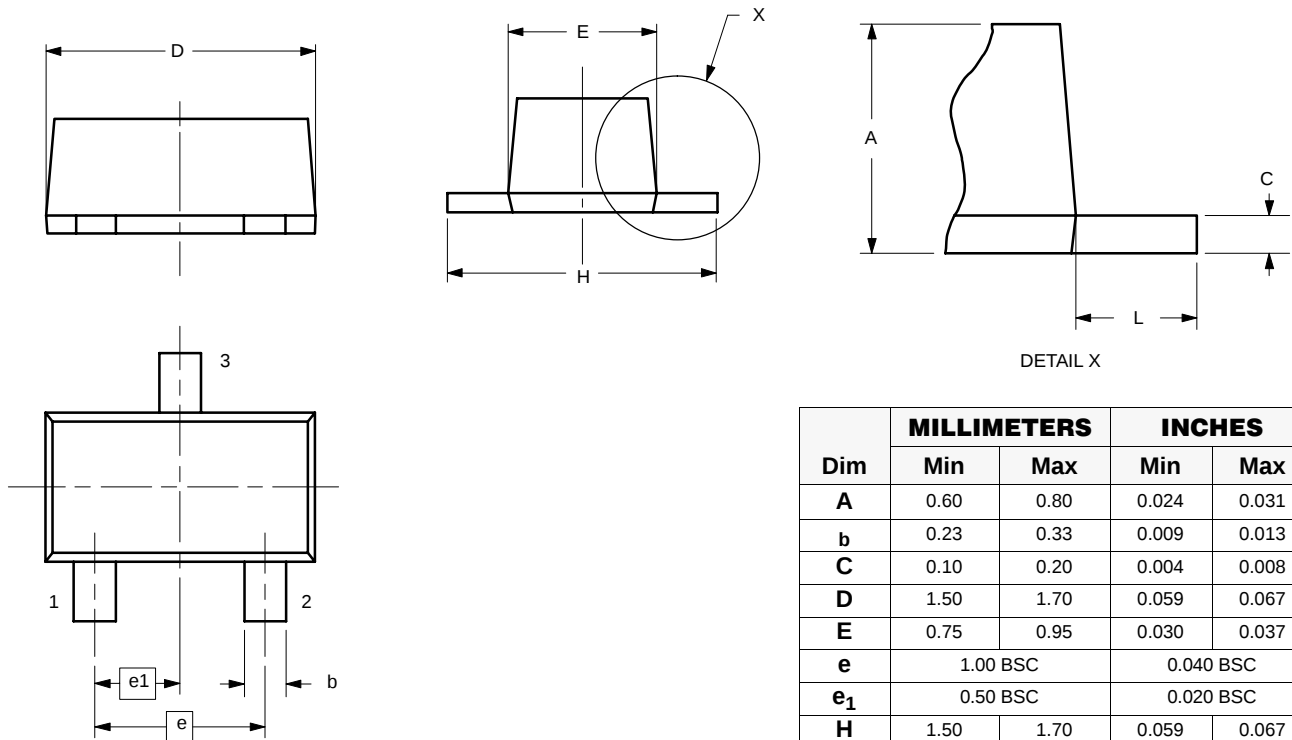


Normalized Thermal Transient Impedance, Junction-to-Ambient

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package / tape drawings, part marking, and reliability data, see www.vishay.com/ppg?62661.



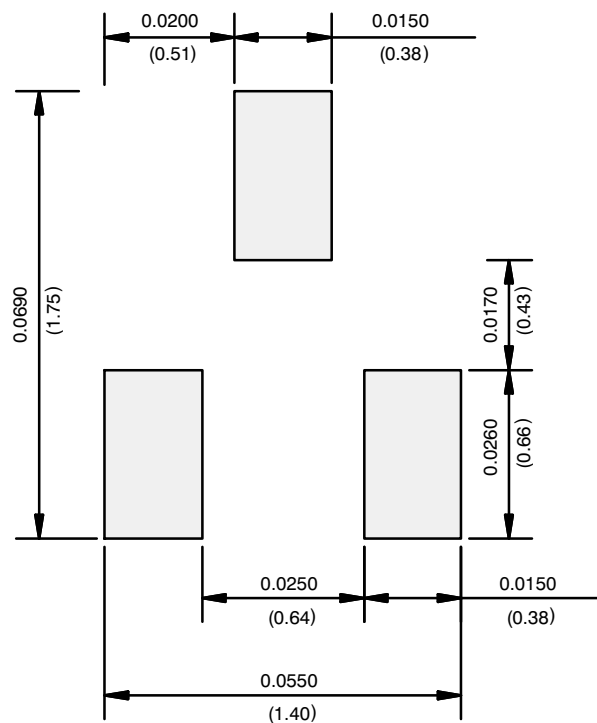
SC89-3



Dim	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	0.60	0.80	0.024	0.031
b	0.23	0.33	0.009	0.013
C	0.10	0.20	0.004	0.008
D	1.50	1.70	0.059	0.067
E	0.75	0.95	0.030	0.037
e	1.00 BSC		0.040 BSC	
e ₁	0.50 BSC		0.020 BSC	
H	1.50	1.70	0.059	0.067
L	0.30	0.50	0.012	0.020

ECN: S-03946—Rev. B, 09-Jul-01
DWG: 5869

RECOMMENDED MINIMUM PADS FOR SC-89: 3-Lead



Recommended Minimum Pads
Dimensions in Inches/(mm)

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