

General Purpose Transistors

NPN Silicon

BC847ATT1, BC847BTT1, BC847CTT1

These transistors are designed for general purpose amplifier applications. They are housed in the SC-75/SOT-416 package which is designed for low power surface mount applications.

Features

- NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- Pb-Free Packages are Available

MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$)

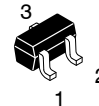
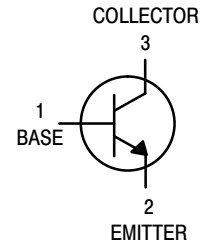
Rating	Symbol	Max	Unit
Collector-Emitter Voltage	V_{CEO}	45	V
Collector-Base Voltage	V_{CBO}	50	V
Emitter-Base Voltage	V_{EBO}	6.0	V
Collector Current – Continuous	I_C	100	mAdc

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

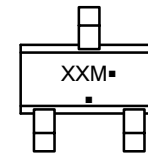
Characteristic	Symbol	Max	Unit
Total Device Dissipation, FR-4 Board (Note 1) $T_A = 25^\circ\text{C}$ Derated above 25°C	P_D	200 1.6	mW mW/ $^\circ\text{C}$
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\theta JA}$	600	$^\circ\text{C}/\text{W}$
Total Device Dissipation, FR-4 Board (Note 2) $T_A = 25^\circ\text{C}$ Derated above 25°C	P_D	300 2.4	mW mW/ $^\circ\text{C}$
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	400	$^\circ\text{C}/\text{W}$
Junction and Storage Temperature Range	T_J, T_{stg}	-55 to +150	$^\circ\text{C}$

1. FR-4 @ min pad.
2. FR-4 @ 1.0×1.0 in pad.



CASE 463
SC-75/SOT-416
STYLE 1

MARKING DIAGRAM



XX = Device Code
M = Date Code
▪ = Pb-Free Package
(Note: Microdot may be in either location)

ORDERING INFORMATION

See detailed ordering, marking and shipping information on page 5 of this data sheet.

NOTE: Some of the devices on this data sheet have been **DISCONTINUED**. Please refer to the table on page 5.

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ELECTRICAL CHARACTERISTICS (T_A = 25 °C unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
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OFF CHARACTERISTICS

Collector - Emitter Breakdown Voltage (I _C = 10 mA) BC847 Series	V _{(BR)CEO}	45	–	–	V
Collector - Emitter Breakdown Voltage (I _C = 10 μA, V _{EB} = 0) BC847 Series	V _{(BR)CES}	50	–	–	V
Collector - Base Breakdown Voltage (I _C = 10 μA) BC847 Series	V _{(BR)CBO}	50	–	–	V
Emitter - Base Breakdown Voltage (I _E = 1.0 μA) BC847 Series	V _{(BR)EBO}	6.0	–	–	V
Collector Cutoff Current (V _{CB} = 30 V) (V _{CB} = 30 V, T _A = 150 °C)	I _{CBO}	–	–	15 5.0	nA μA

ON CHARACTERISTICS

DC Current Gain (I _C = 10 μA, V _{CE} = 5.0 V) BC847A BC847B BC847C (I _C = 2.0 mA, V _{CE} = 5.0 V) BC847A BC847B BC847C	h _{FE}	– – – 110 200 420	90 150 270 180 290 520	– – – 220 450 800	–
Collector - Emitter Saturation Voltage (I _C = 10 mA, I _B = 0.5 mA) (I _C = 100 mA, I _B = 5.0 mA)	V _{CE(sat)}	– –	– –	0.25 0.6	V
Base - Emitter Saturation Voltage (I _C = 10 mA, I _B = 0.5 mA) (I _C = 100 mA, I _B = 5.0 mA)	V _{BE(sat)}	– –	0.7 0.9	– –	V
Base - Emitter Voltage (I _C = 2.0 mA, V _{CE} = 5.0 V) (I _C = 10 mA, V _{CE} = 5.0 V)	V _{BE(on)}	580 –	660 –	700 770	mV

SMALL-SIGNAL CHARACTERISTICS

Current - Gain – Bandwidth Product (I _C = 10 mA, V _{CE} = 5.0 Vdc, f = 100 MHz)	f _T	100	–	–	MHz
Output Capacitance (V _{CB} = 10 V, f = 1.0 MHz)	C _{obo}	–	–	4.5	pF
Noise Figure (I _C = 0.2 mA, V _{CE} = 5.0 Vdc, R _S = 2.0 kΩ, f = 1.0 kHz, BW = 200 Hz)	NF	–	–	10	dB

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

BC847ATT1, BC847BTT1, BC847CTT1

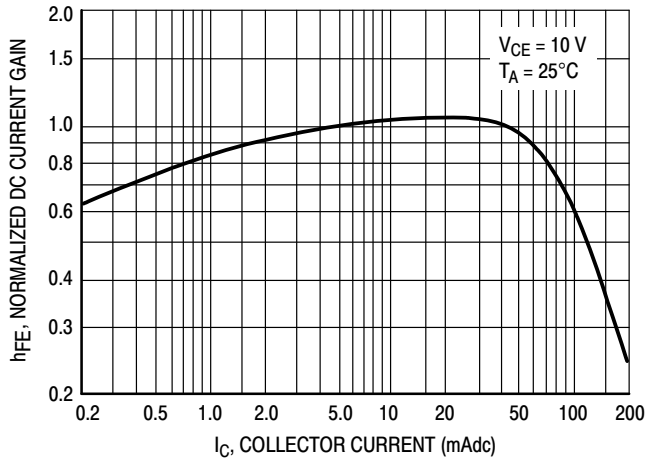


Figure 1. Normalized DC Current Gain

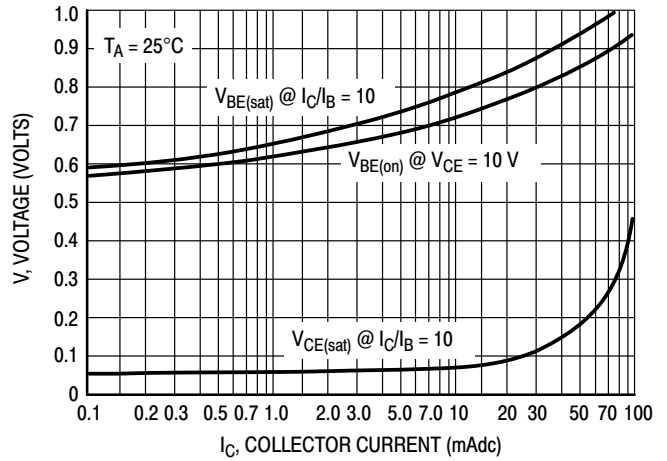


Figure 2. "Saturation" and "On" Voltages

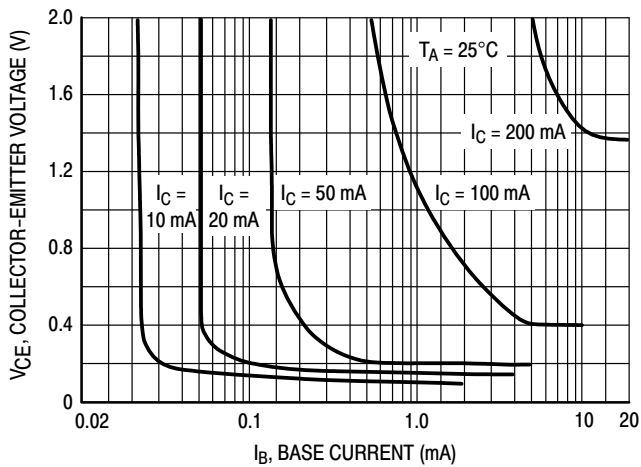


Figure 3. Collector Saturation Region

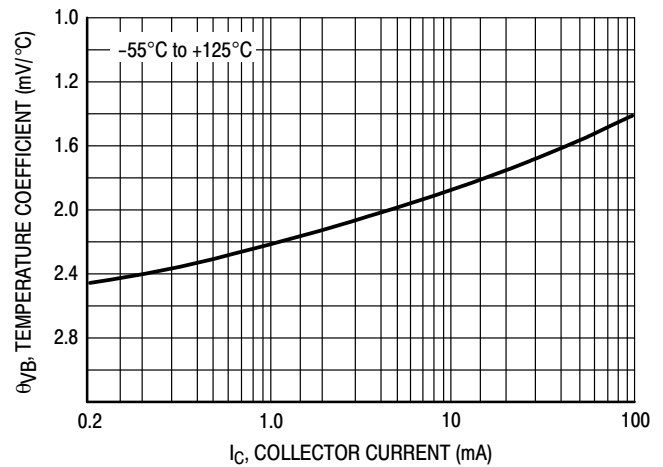


Figure 4. Base-Emitter Temperature Coefficient

BC847ATT1, BC847BTT1, BC847CTT1

BC847

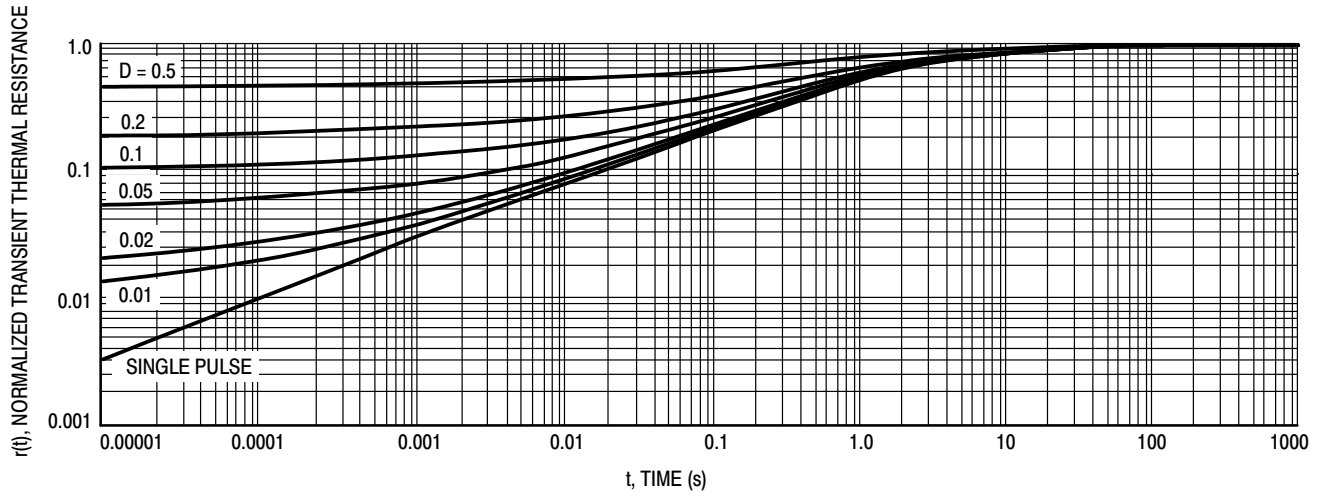


Figure 5. Normalized Thermal Response

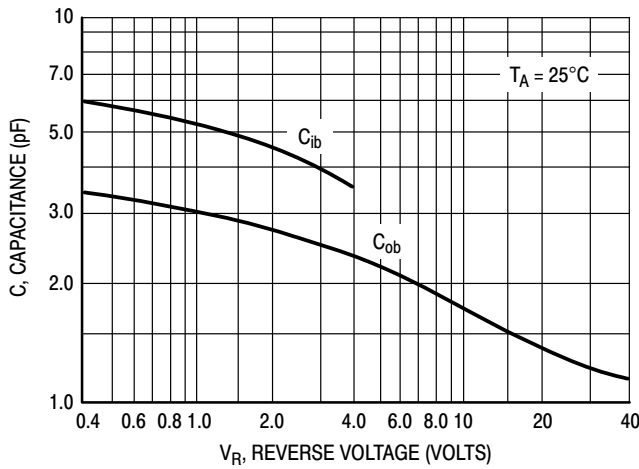


Figure 6. Capacitances

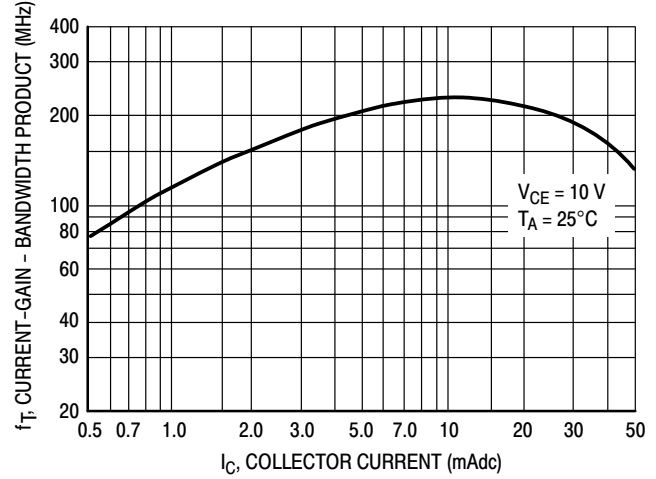


Figure 7. Current-Gain - Bandwidth Product

BC847ATT1, BC847BTT1, BC847CTT1

ORDERING INFORMATION

Device	Marking	Package	Shipping†
BC847ATT1	1E	SC-75/SOT-416	3,000 / Tape & Reel
BC847BTT1G	1F	SC-75/SOT-416 (Pb-Free)	3,000 / Tape & Reel
NSVBC847BTT1G*	1F	SC-75/SOT-416 (Pb-Free)	3,000 / Tape & Reel

DISCONTINUED (Note 3)

BC847BTT1	1F	SC-75/SOT-416	3,000 / Tape & Reel
BC847CTT1G	1G	SC-75/SOT-416 (Pb-Free)	3,000 / Tape & Reel

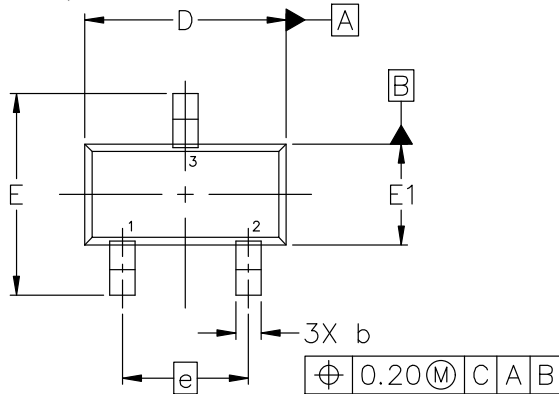
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

*NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable.

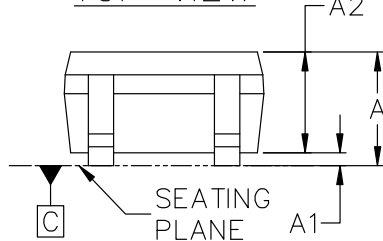
3. **DISCONTINUED:** These devices are not recommended for new design. Please contact your **onsemi** representative for information. The most current information on these devices may be available on www.onsemi.com.

SC75-3 1.60x0.80x0.80, 1.00P
CASE 463
ISSUE H

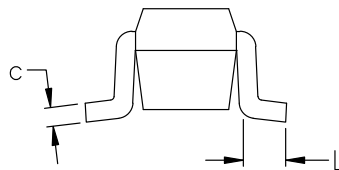
DATE 01 FEB 2024



TOP VIEW

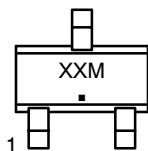


SIDE VIEW



END VIEW

GENERIC MARKING DIAGRAM*



XX = Specific Device Code
M = Date Code
▪ = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

STYLE 1:
PIN 1. BASE
2. EMITTER
3. COLLECTOR

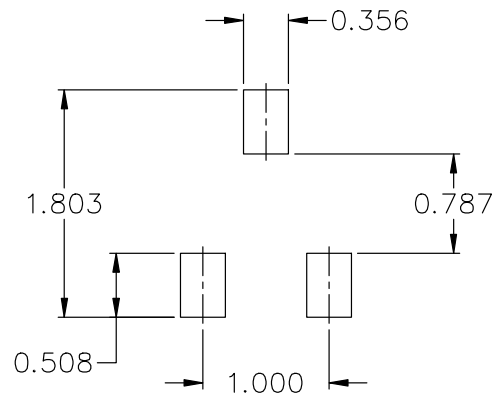
STYLE 2:
PIN 1. ANODE
2. N/C
3. CATHODE

STYLE 3:
PIN 1. ANODE
2. ANODE
3. CATHODE

STYLE 4:
PIN 1. CATHODE
2. CATHODE
3. ANODE

STYLE 5:
PIN 1. GATE
2. SOURCE
3. DRAIN

DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.70	0.80	0.90
A1	0.00	0.05	0.10
A2	0.80 REF.		
b	0.15	0.20	0.30
c	0.10	0.15	0.25
D	1.55	1.60	1.65
E	1.50	1.60	1.70
E1	0.70	0.80	0.90
e	1.00 BSC		
L	0.10	0.15	0.20



RECOMMENDED MOUNTING FOOTPRINT*

* FOR ADDITIONAL INFORMATION ON OUR Pb-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

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DESCRIPTION:	SC75-3 1.60x0.80x0.80, 1.00P	PAGE 1 OF 1

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