



December 2004

FDM6296

Single N-Channel, Logic-Level, PowerTrench® MOSFET

Features

- 11.5 A, 30 V $R_{DS(ON)} = 10.5 \text{ m}\Omega @ V_{GS} = 10 \text{ V}$
 $R_{DS(ON)} = 15 \text{ m}\Omega @ V_{GS} = 4.5 \text{ V}$
- Low Q_g , Q_{gd} and R_g for efficient switching performance
- Low Profile – MicroFET 3.3 x 3.3 mm

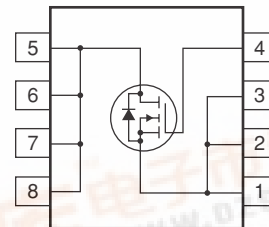
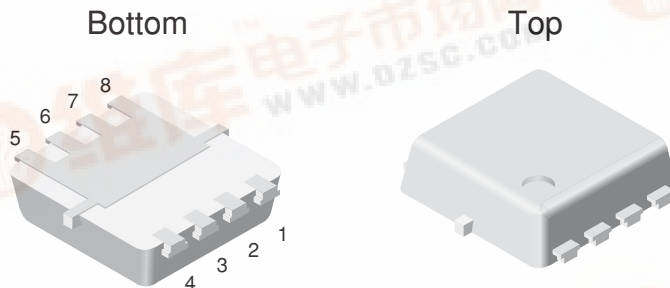
Applications

- Point of Load Converter
- 1/16 Brick Synchronous Rectifier

General Description

This single N-Channel MOSFET in the thermally efficient MicroFET package has been specifically designed to perform well in Point of Load converters. Providing an optimized balance between $R_{ds(on)}$ and gate charge this device can be effectively used as a “high side” control switch or “low side” synchronous rectifier.

MicroFET



Absolute Maximum Ratings $T_A=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Ratings	Units
V_{DSS}	Drain-Source Voltage	30	V
V_{GSS}	Gate-Source Voltage	± 20	V
I_D	Drain Current – Continuous (Note 1a)	11.5	A
	– Pulsed	40	
P_D	Power Dissipation (Steady State) (Note 1a)	2.5	W
		1.2 (Note 1b)	
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to $+150$	$^\circ\text{C}$

Thermal Characteristics

$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient (Note 1a)	52	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient (Note 1b)	108	
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case (Note 1)	5	

Package Marking and Ordering Information

Device Marking	Device	Reel Size	Tape width	Quantity
6296	FDM6296	7"	12mm	3000 units

FDM6296 Single N-Channel, Logic-Level, PowerTrench® MOSFET

Electrical Characteristics $T_A = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Off Characteristics						
BV _{DSS}	Drain–Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA	30			V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C		29		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 24 V, V _{GS} = 0 V			1	μA
I _{GSS}	Gate–Body Leakage	V _{GS} = ±20 V, V _{DS} = 0 V			±100	nA
On Characteristics (Note 2)						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250 μA	1	1.9	3	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate Threshold Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C		–5		mV/°C
R _{DS(on)}	Static Drain–Source On–Resistance	V _{GS} = 10 V, I _D = 11.5 A V _{GS} = 4.5 V, I _D = 10 A V _{GS} = 10 V, I _D = 11.5A, T _J = 125°C		8.7 10.6 13	10.5 15 17	mΩ
g _{FS}	Forward Transconductance	V _{DS} = 5 V, I _D =11.5 A		47		S
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = 15 V, V _{GS} = 0 V, f = 1.0 MHz		1507		pF
C _{oss}	Output Capacitance			415		pF
C _{rss}	Reverse Transfer Capacitance			128		pF
R _G	Gate Resistance	V _{GS} = 15 mV, f = 1.0 MHz		1.1		Ω
Switching Characteristics (Note 2)						
t _{d(on)}	Turn–On Delay Time	V _{DD} = 15 V, I _D = 1 A, V _{GS} = 10 V, R _{GEN} = 6 Ω		10	20	ns
t _r	Turn–On Rise Time			5	10	ns
t _{d(off)}	Turn–Off Delay Time			27	44	ns
t _f	Turn–Off Fall Time			13	23	ns
Q _g	Total Gate Charge	V _{DS} = 15 V, I _D = 11.5 A, V _{GS} = 5 V		12	17	nC
Q _{gs}	Gate–Source Charge			4		nC
Q _{gd}	Gate–Drain Charge			3		nC
Drain–Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain–Source Diode Forward Current				2	A
V _{SD}	Drain–Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 2 A (Note 2)		0.9	1.2	V
t _{rr}	Diode Reverse Recovery Time	I _F = 11.5 A, dI _F /dt = 100 A/μs		29		nS
Q _{rr}	Diode Reverse Recovery Charge			20		nC

Notes:

- $R_{\theta JA}$ is determined with the device mounted on a 1 in_ 2 oz. copper pad on a 1.5 x 1.5 in. board of FR-4 material. $R_{\theta JC}$ are guaranteed by design while $R_{\theta JA}$ is determined by the user's board design.
 - $R_{\theta JA} = 52^\circ\text{C/W}$ when mounted on a 1in² pad of 2 oz copper, 1.5" x 1.5" x 0.062" thick PCB
 - $R_{\theta JA} = 108^\circ\text{C/W}$ when mounted on a minimum pad of 2 oz copper
- Pulse Test: Pulse Width < 300ms, Duty Cycle < 2.0%

Typical Characteristics

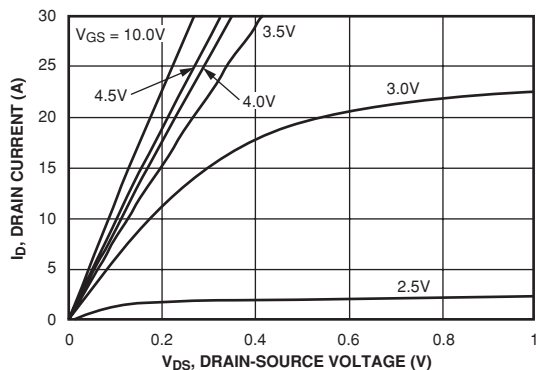


Figure 1. On-Region Characteristics

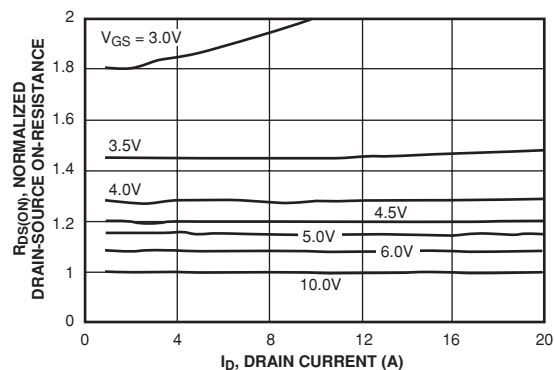


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage

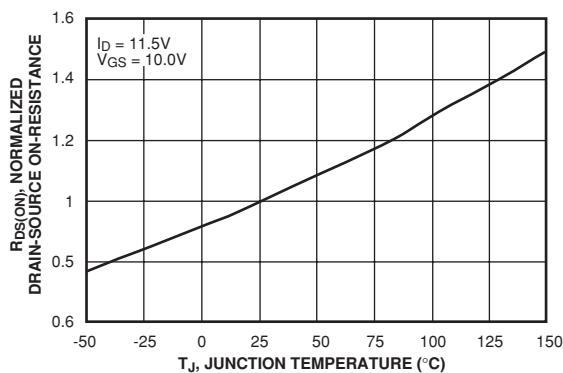


Figure 3. On-Resistance Variation with Temperature

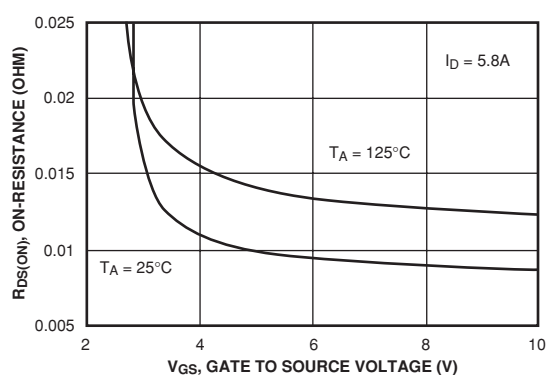


Figure 4. On-Resistance Variation with Gate-to-Source Voltage

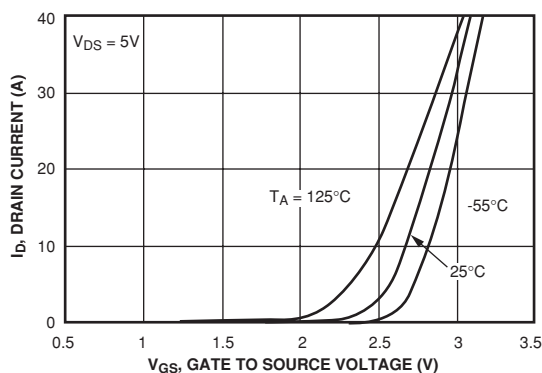


Figure 5. Transfer Characteristics

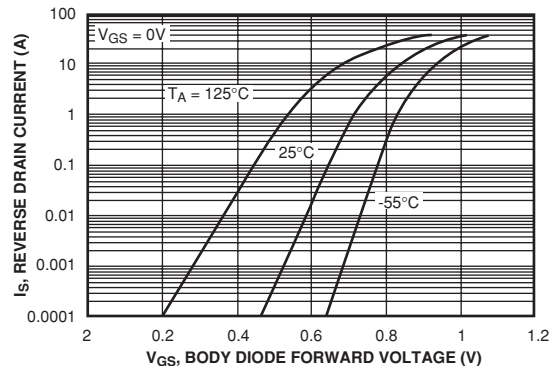


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature

Typical Characteristics

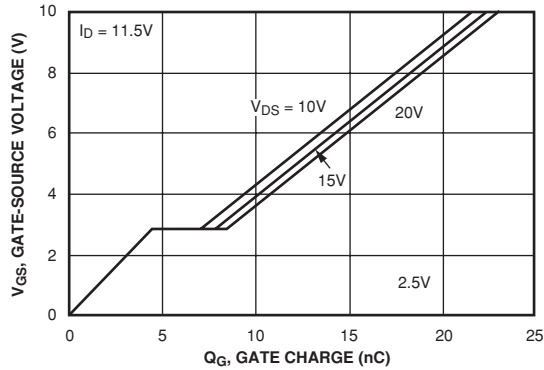


Figure 7. Gate Charge Characteristics

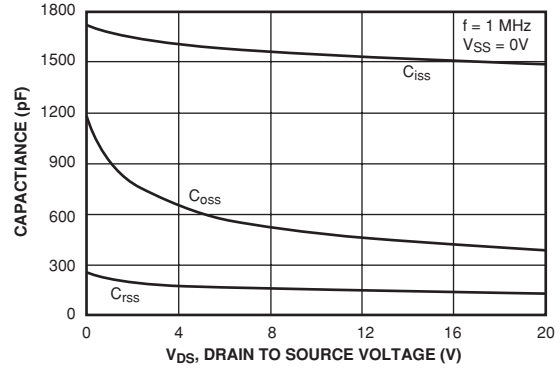


Figure 8. Capacitance Characteristics

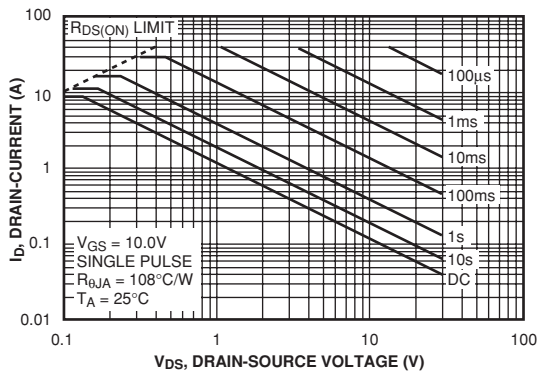


Figure 9. Maximum Safe Operating Area

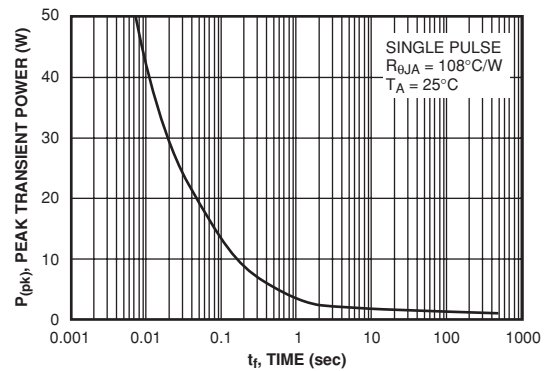


Figure 10. Single Pulse Maximum Power Dissipation

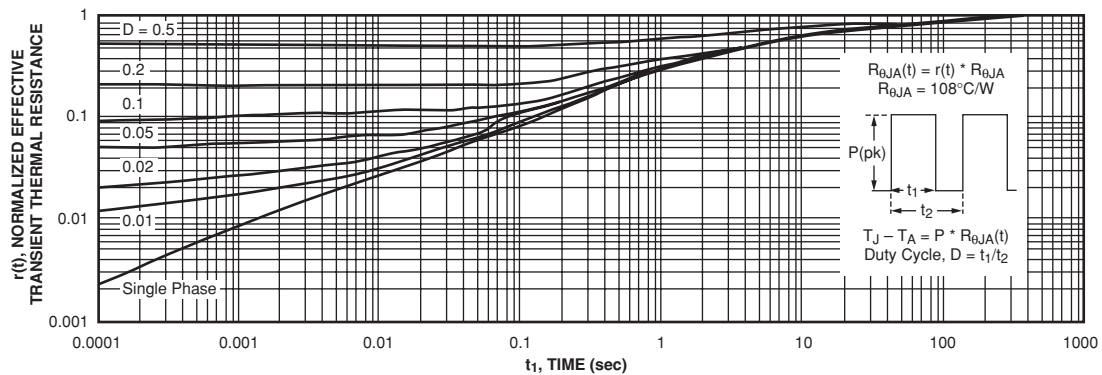
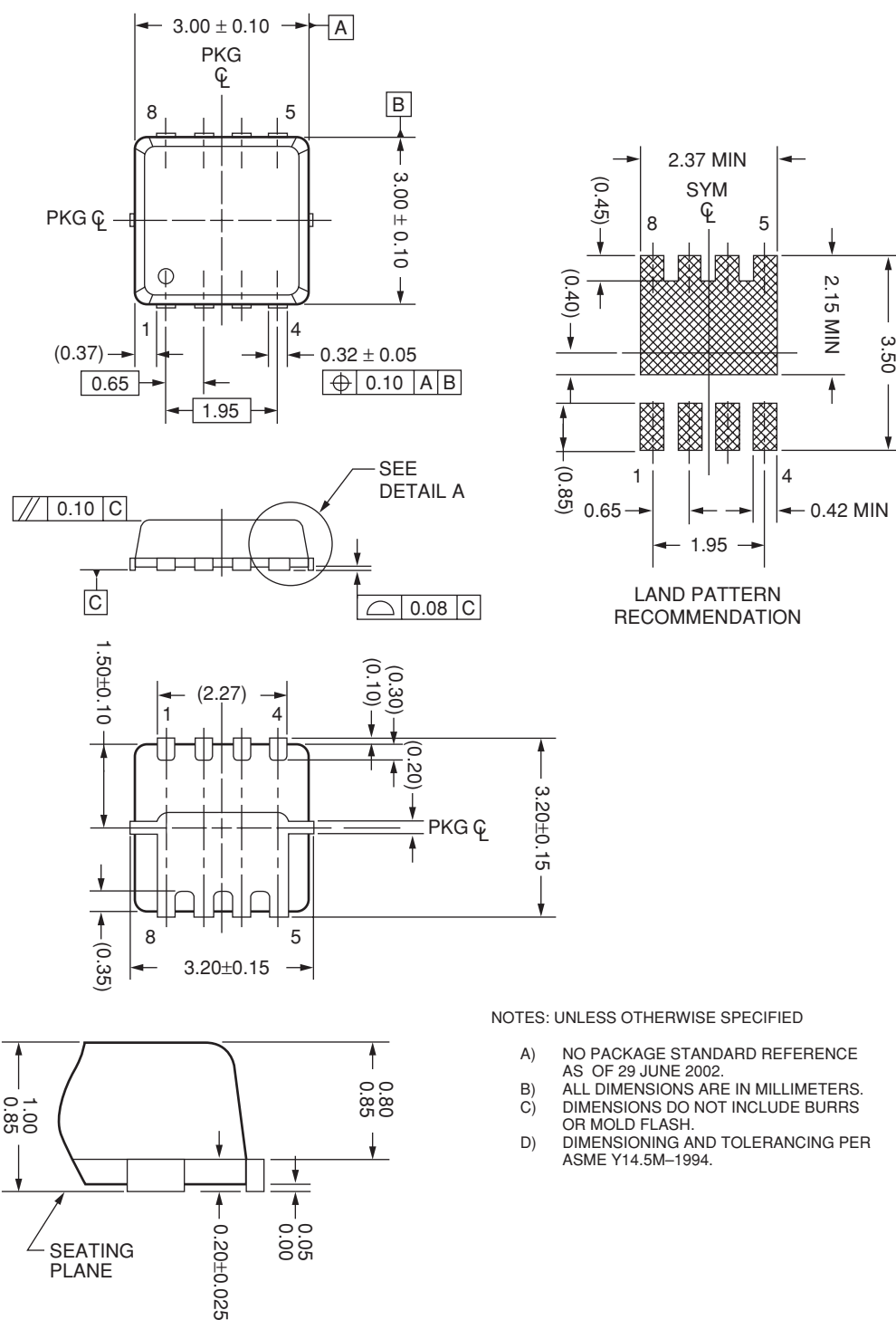


Figure 5. Transfer Characteristics

Thermal characterization performed using the conditions described in Note 1b.
Transient thermal response will change depending on the circuit board design.

Dimensional Outline and Pad Layout



NOTES: UNLESS OTHERWISE SPECIFIED

- A) NO PACKAGE STANDARD REFERENCE AS OF 29 JUNE 2002.
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DIMENSIONS DO NOT INCLUDE BURRS OR MOLD FLASH.
- D) DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.

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