



PMEG3010EB

30 V, 1 A very low VF Schottky barrier rectifier

6 August 2026

Product data sheet

1. General description

Planar Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in a SOD523 (SC-79) ultra small and flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Forward current: $I_F \leq 1$ A
- Reverse voltage: $V_R \leq 30$ V
- Very low forward voltage
- Ultra small and flat lead SMD plastic package

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch mode power supply
- Reverse polarity protection
- Low power consumption applications

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
I_F	forward current	$T_{sp} \leq 55$ °C	-	-	1	A
V_R	reverse voltage		-	-	30	V
V_F	forward voltage	$I_F = 1$ A; pulsed; $t_p \leq 300$ μ s; $\delta \leq 0.02$; $T_{amb} = 25$ °C	-	610	680	mV

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode[1]	 SC-79 (SOD523)	 K $\rightarrow$ A sym001
2	A	anode		

[1] The marking bar indicates the cathode.

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PMEG3010EB	SC-79	plastic, surface-mounted package; 2 leads; 1.2 mm x 0.8 mm x 0.6 mm body	SOD523

7. Marking

Table 4. Marking codes

Type number	Marking code
PMEG3010EB	KA

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _R	reverse voltage			-	30	V
I _F	forward current	T _{sp} ≤ 55 °C		-	1	A
I _{FRM}	repetitive peak forward current	t _p ≤ 1 ms; δ ≤ 0.25		-	3	A
I _{FSM}	non-repetitive peak forward current	t _p = 8 ms; square wave; T _{j(init)} = 25 °C		-	5	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	310	mW
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

9. Thermal characteristics

Table 6. Thermal characteristics

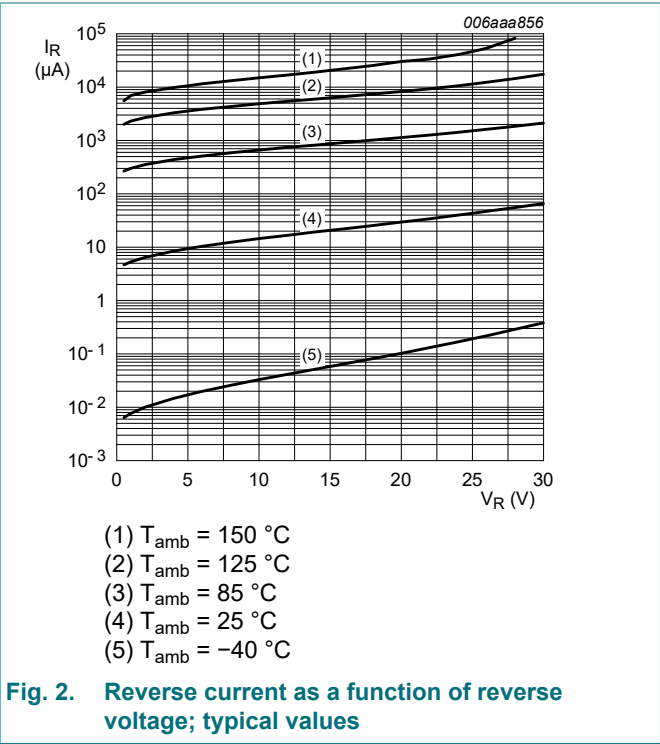
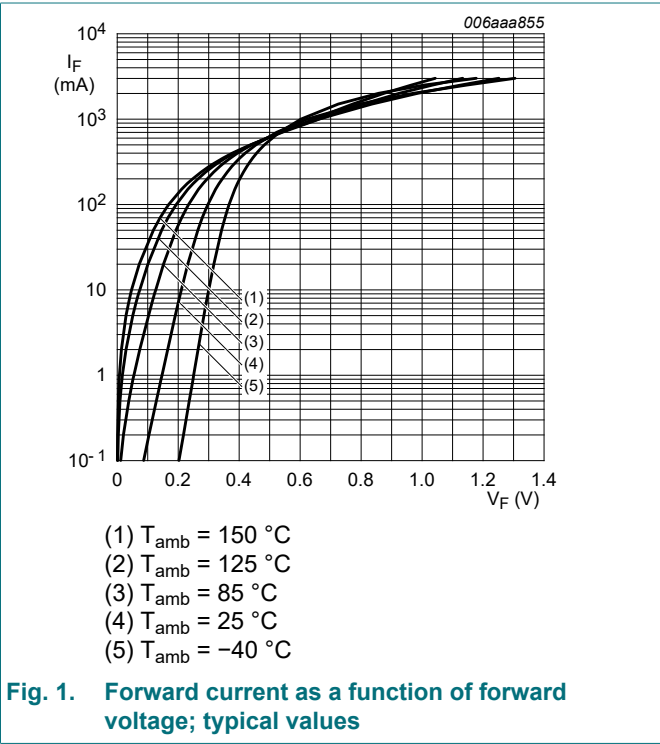
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	400	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		[3]	-	-	75	K/W

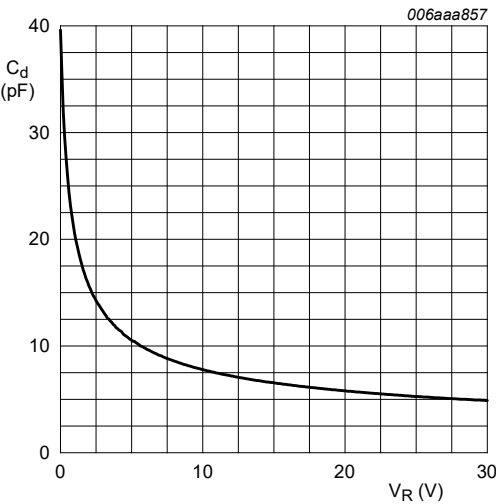
- [1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [3] Soldering point of cathode tab.

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V_F	forward voltage	$I_F = 0.1\text{ mA}$; pulsed; $t_p \leq 300\text{ }\mu\text{s}$; $\delta \leq 0.02$; $T_{\text{amb}} = 25\text{ }^\circ\text{C}$	-	90	180	mV
		$I_F = 1\text{ mA}$; pulsed; $t_p \leq 300\text{ }\mu\text{s}$; $\delta \leq 0.02$; $T_{\text{amb}} = 25\text{ }^\circ\text{C}$	-	150	200	mV
		$I_F = 10\text{ mA}$; pulsed; $t_p \leq 300\text{ }\mu\text{s}$; $\delta \leq 0.02$; $T_{\text{amb}} = 25\text{ }^\circ\text{C}$	-	210	270	mV
		$I_F = 100\text{ mA}$; pulsed; $t_p \leq 300\text{ }\mu\text{s}$; $\delta \leq 0.02$; $T_{\text{amb}} = 25\text{ }^\circ\text{C}$	-	295	360	mV
		$I_F = 500\text{ mA}$; pulsed; $t_p \leq 300\text{ }\mu\text{s}$; $\delta \leq 0.02$; $T_{\text{amb}} = 25\text{ }^\circ\text{C}$	-	430	500	mV
		$I_F = 1\text{ A}$; pulsed; $t_p \leq 300\text{ }\mu\text{s}$; $\delta \leq 0.02$; $T_{\text{amb}} = 25\text{ }^\circ\text{C}$	-	610	680	mV
I_R	reverse current	$V_R = 10\text{ V}$; $T_{\text{amb}} = 25\text{ }^\circ\text{C}$	-	15	200	μA
		$V_R = 30\text{ V}$; $T_{\text{amb}} = 25\text{ }^\circ\text{C}$	-	70	500	μA
C_d	diode capacitance	$V_R = 1\text{ V}$; $f = 1\text{ MHz}$; $T_{\text{amb}} = 25\text{ }^\circ\text{C}$	-	24	30	pF





$f = 1\text{ MHz}$; $T_{\text{amb}} = 25\text{ }^{\circ}\text{C}$

Fig. 3. Diode capacitance as a function of reverse voltage; typical values

11. Test information

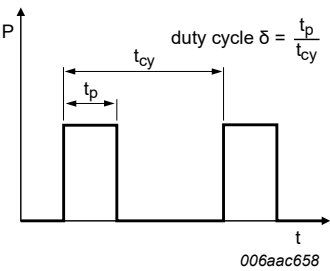


Fig. 4. Duty cycle definition

12. Package outline

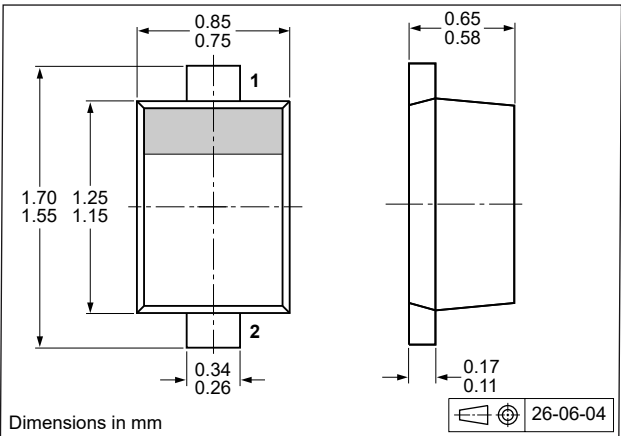


Fig. 5. Package outline SC-79 (SOD523)

13. Soldering

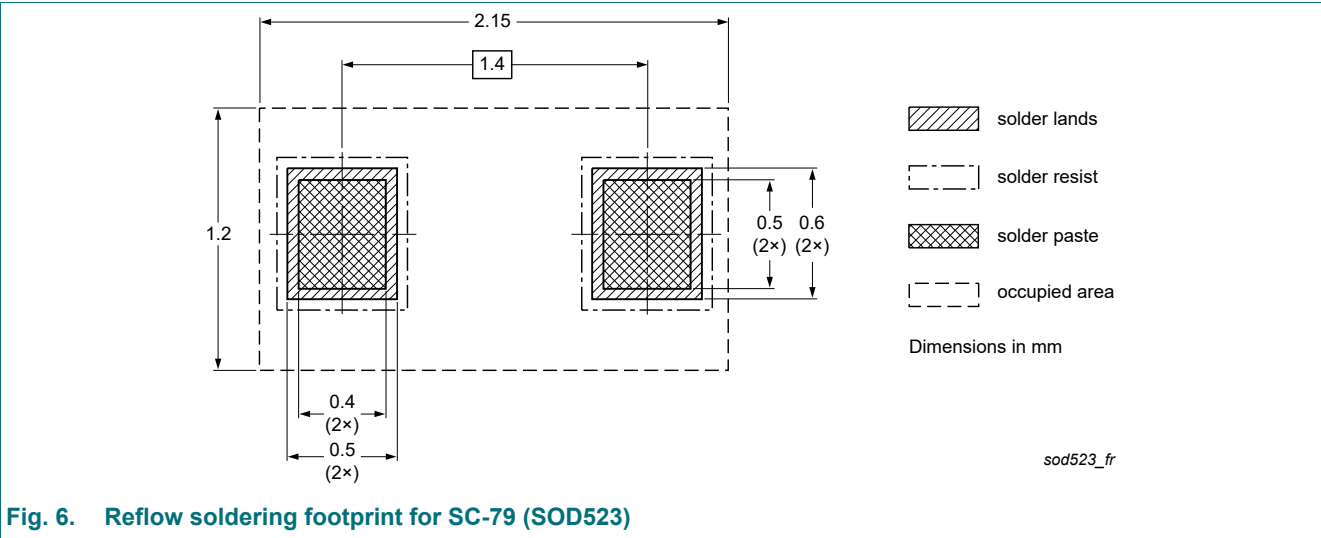


Fig. 6. Reflow soldering footprint for SC-79 (SOD523)

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMEG3010EB v.5	20260806	Product data sheet	-	PMEG3010EB v.4
Modifications:	• Aligned SOD523 package outline drawing			
PMEG3010EB v.4	20241008	Product data sheet	-	PMEG3010EB v.3
PMEG3010EB v.3	20210407	Product data sheet	-	PMEG3010EB v.2
PMEG3010EB v.2	20120315	Product data sheet	-	PMEG3010EB v.1
PMEG3010EB v.1	20061201	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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