

SiC SBD P3D06004E2

650V SiC Schottky Diode



TO-252-2

Cathode	1
Anode	2

Features

- Qualified to AEC-Q101
- Ultra-Fast Switching
- Zero Reverse Recovery Current
- High-Frequency Operation
- Positive Temperature Coefficient on V_F
- High Surge Current
- 100% UIS tested



Standards Benefits

- Improve System Efficiency
- Reduction of Heat Sink Requirement
- Essentially No Switching Losses
- Parallel Devices Without Thermal Runaway



Application

- Consumer SMPS
- Boost Diodes in PFC or DC/DC Stages
- AC/DC Converters



Order Information

Part Number	Package	Marking
P3D06004E2	TO-252-2	P3D06004E2

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1. Maximum Ratings

At $T_J = 25^\circ\text{C}$, unless specified otherwise

Parameter	Symbol	Value	Unit	Test condition
Repetitive Peak Reverse Voltage	V_{RRM}	650	V	$T_C = 25^\circ\text{C}$
Surge Peak Reverse Voltage	V_{RSM}	650	V	$T_C = 25^\circ\text{C}$
DC Blocking Voltage	V_R	650	V	$T_C = 25^\circ\text{C}$
Forward Current	I_F	12	A	$T_C = 25^\circ\text{C}$
		6		$T_C = 125^\circ\text{C}$
		4		$T_C = 150^\circ\text{C}$
Repetitive Peak Forward Surge Current	I_{FRM}	22	A	$T_C = 25^\circ\text{C}, t_p = 10\text{ms}$
		11		$T_C = 125^\circ\text{C}, t_p = 10\text{ms}$
Non-Repetitive Forward Surge Current	I_{FSM}	26	A	$T_C = 25^\circ\text{C}, t_p = 10\text{ms}$
		20		$T_C = 125^\circ\text{C}, t_p = 10\text{ms}$
Power Dissipation	P_{tot}	52	W	$T_C = 25^\circ\text{C}$
Operating Junction and Storage Temperature	T_J, T_{STG}	-55 to +175	$^\circ\text{C}$	
TO-252 Mounting Torque M3 Screw	T_{orq}	1	Nm lbf-in	
		8.8		

2. Thermal Characteristics

Parameter	Symbol	Values	Unit
Thermal Resistance from Junction to Case	$R_{\theta JC}$	2.9	$^\circ\text{C}/\text{W}$

3. Electrical Characteristics

At $T_J = 25^\circ\text{C}$, unless specified otherwise

Parameter	SymAbol	Values			Unit	Test condition
		Min.	Typ.	Max.		
Forward Voltage	V_F	/	1.5	1.7	V	$I_F = 4\text{A}, T_J = 25^\circ\text{C}$
			1.8	/		$I_F = 4\text{A}, T_J = 175^\circ\text{C}$
Reverse Current	I_R	/	1	20	μA	$V_R = 650\text{V}, T_J = 25^\circ\text{C}$
			45	/		$V_R = 650\text{V}, T_J = 175^\circ\text{C}$
Total Capacitance	C	/	145	/	pF	$V_R = 0\text{V}, T_J = 25^\circ\text{C}$ $f = 1\text{MHz}$
			15			$V_R = 200\text{V}, T_J = 25^\circ\text{C}$ $f = 1\text{MHz}$
			12			$V_R = 400\text{V}, T_J = 25^\circ\text{C}$ $f = 1\text{MHz}$
Total Capacitive Charge	Q_C	/	9	/	nC	$V_R = 400\text{V}$
Capacitance Stored Energy	E_C	/	1.3	/	μJ	$V_R = 400\text{V}$

4. Typical Performance

At $T_J = 25^\circ\text{C}$, unless specified otherwise

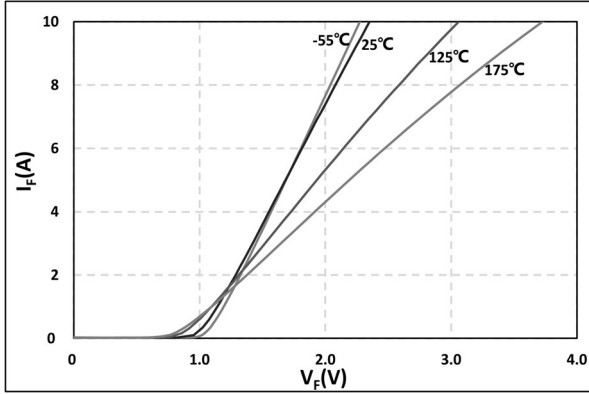


Fig. 1 Typical Forward Characteristics
 $I_F = f(V_F)$; $T_J = -55^\circ\text{C}, 25^\circ\text{C}, 125^\circ\text{C}, 175^\circ\text{C}$

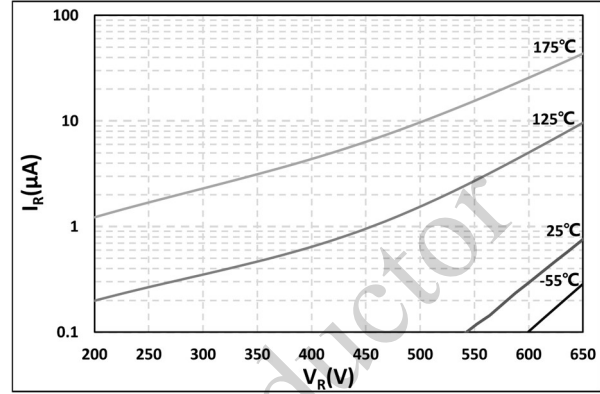


Fig. 2 Reverse Characteristics
 $I_R = f(V_R)$; $T_J = -55^\circ\text{C}, 25^\circ\text{C}, 125^\circ\text{C}, 175^\circ\text{C}$

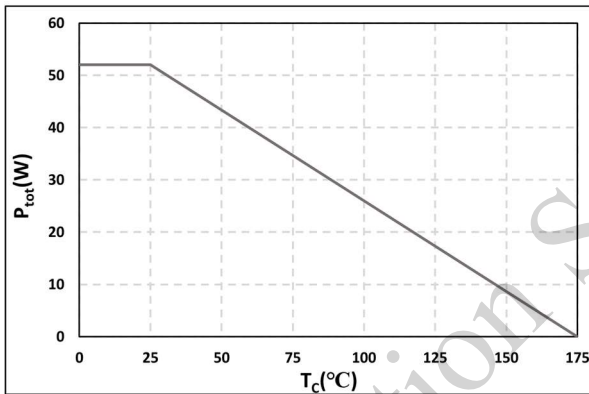


Fig. 3 Typical Power Derating
 $P_{\text{tot}} = f(T_C)$

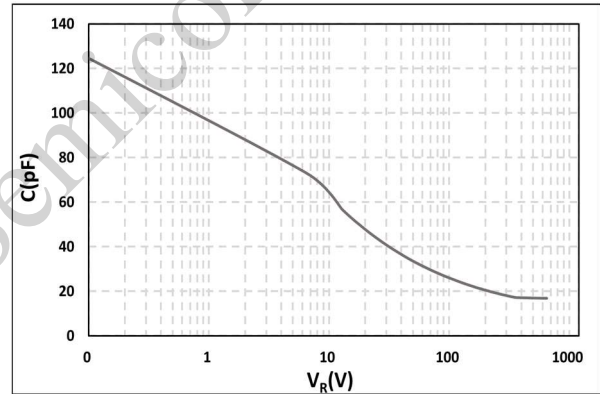


Fig. 4 Typical Total Capacitance
 $C = f(V_R)$

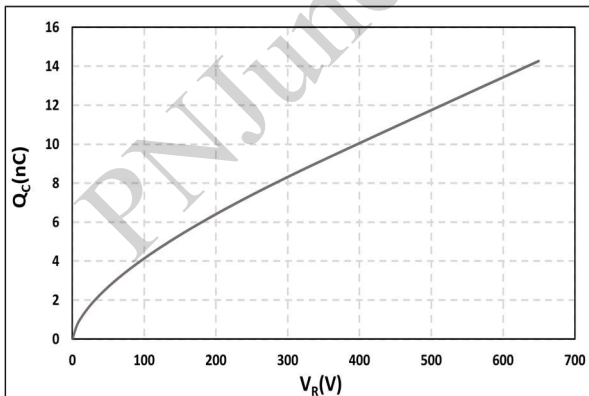


Fig. 5 Typical Total Capacitive Charge
 $Q_C = f(V_R)$

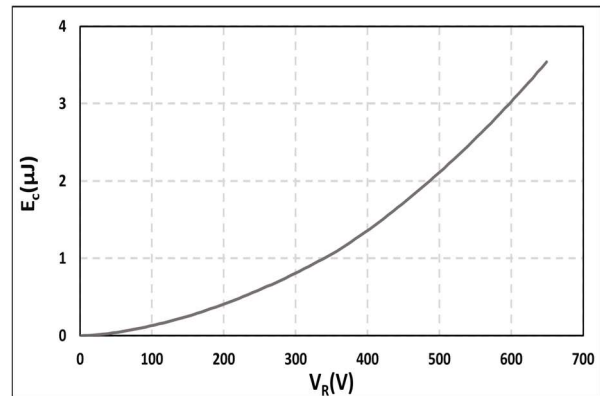


Fig. 6 Capacitance Stored Energy
 $E_C = f(V_R)$

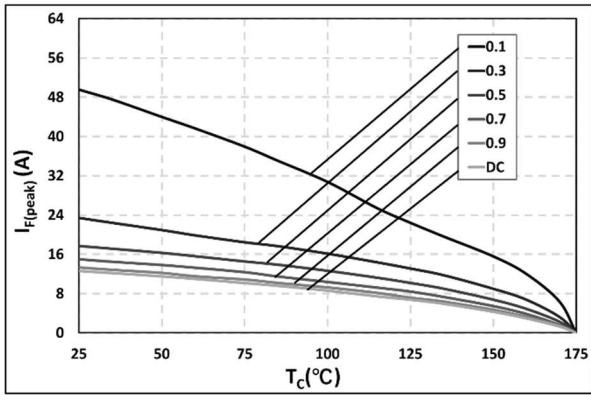


Fig. 7 Current Derating

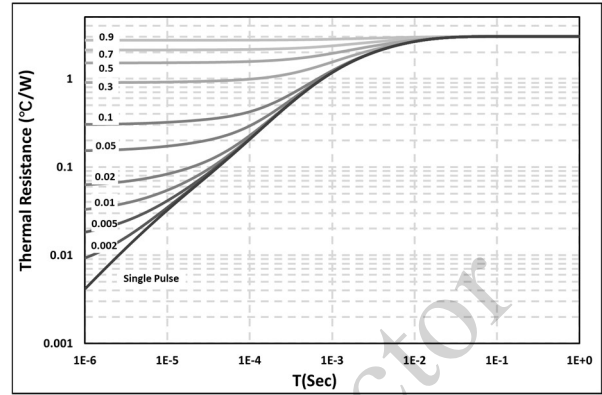
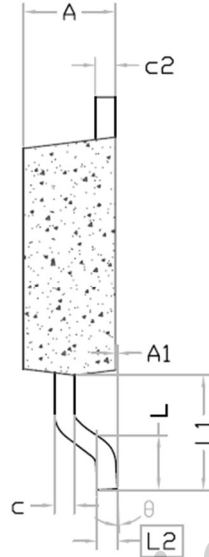
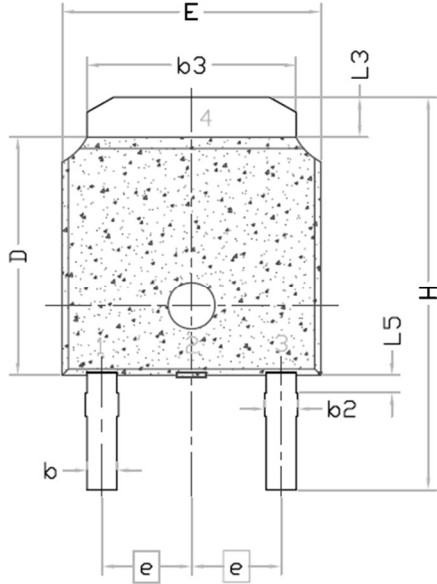


Fig. 8 Transient Thermal Impedance

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5. Package Outlines



SYMBOL	DIMENSIONAL REQMTS		
	MIN	NDM	MAX
E	6.40	6.60	6.731
L	1.40	1.52	1.77
L1	2.743 REF		
L2	0.508 BSC		
L3	0.89	--	1.27
L5	--	--	--
D	6.00	6.10	6.223
H	9.40	10.00	10.40
b	0.64	0.76	0.88
b2	0.77	0.84	1.14
b3	5.21	5.34	5.46
e	2.286 BSC		
A	2.20	2.30	2.38
A1	0	--	0.127
c	0.46	0.50	0.60
c2	0.46	0.50	0.58
D1	5.21	--	--
E1	4.40	--	--
F	--	--	0.45
theta	0°	--	10°

Drawing and dimensions

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