

Revision History

BS83A02A-4/BS83A04A-3/BS83A04A-4 Touch I/O Flash MCU

Revision # 1.80 Modifications made on October 13, 2025

- The 6-pin DFN package type was removed.
- The Storage Temperature Range was modified.

Revision # 1.72 Modifications made on November 11, 2021

- The Development Tools section was added.
- The Instruction Set and Instruction Set Summary sections were modified.

Revision # 1.71 Modifications made on April 11, 2017

- Remove the 8-pin package type marketing limitation.

Revision # 1.70 Modifications made on July 1, 2016

- In the Features and other related sections, an 8-pin SOP package for the BS83A02A-4 device was added.

Revision # 1.60 Modifications made on April 11, 2016

- In the Pin Assignment section, the 16-pin NSOP package OCDS EV chip was modified.

Revision # 1.50 Modifications made on February 26, 2016

- In the Feature and other related sections, the BS83A02A-4 6-pin DFN package was added.

Revision # 1.41 Modifications made on January 26, 2016

- In the Package Information sections, the SOT23-6 Outline Dimensions was modified.

Revision # 1.40 Modifications made on August 8, 2015

- In the Selection Table section, the Marking information was added.

Revision # 1.30 Modifications made on January 23, 2015

- In the A.C. Characteristics section, the f_{HIRC} entry was modified.

Revision # 1.20 Modifications made on November 19, 2014

- A new device, the BS83A02A, was added to the datasheet.

Revision # 1.10 Modifications made on June 22, 2014

- The Product Title was modified.
- In the Features and other related sections, an 8-pin SOP package type was added.

Revision # 1.00 This was posted on February 28, 2013