CUI INC
a bel group

SERIES: VHK200W-DIN | DESCRIPTION: DC-DC CONVERTER

FEATURES

- up to 200 W isolated output
- rugged metal enclosure with integrated heat sink
- 4:1 input range (10~36 Vdc, 18~75 Vdc)
- single output from 12~48 Vdc
- 1,500 Vdc isolation
- over current, over temperature, over voltage, and short circuit protections
- remote on/off
- efficiency up to 88%
- comes with DIN-rail mount



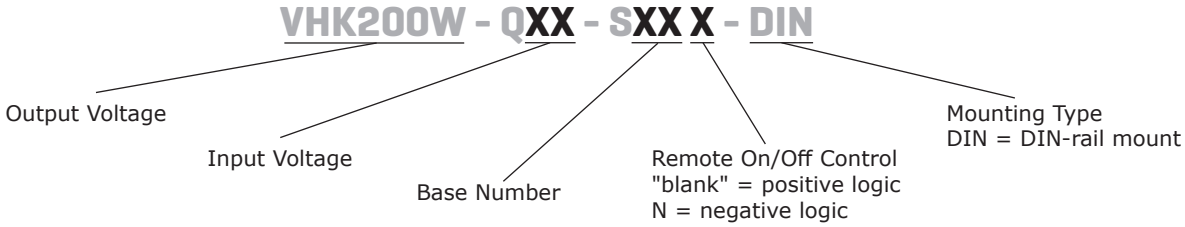
RoHS CE

MODEL

MODEL	input voltage range (Vdc)	output voltage (Vdc)	output current max (A)	output power max (W)	ripple and noise[1] max (mVp-p)	efficiency typ (%)
VHK200W-Q24-S12-DIN	10 ~ 36	12	16.7	200	150	84
VHK200W-Q24-S15-DIN	10 ~ 36	15	13.3	200	150	84
VHK200W-Q24-S24-DIN	10 ~ 36	24	8.3	200	240	84
VHK200W-Q24-S28-DIN	10 ~ 36	28	7.14	200	280	87
VHK200W-Q24-S48-DIN	10 ~ 36	48	4.2	200	480	87
VHK200W-Q48-S12-DIN	18 ~ 75	12	16.7	200	150	86
VHK200W-Q48-S15-DIN	18 ~ 75	15	13.3	200	150	86
VHK200W-Q48-S24-DIN	18 ~ 75	24	8.3	200	240	86
VHK200W-Q48-S28-DIN	18 ~ 75	28	7.14	200	280	87
VHK200W-Q48-S48-DIN	18 ~ 75	48	4.2	200	480	88

Note: 1. Ripple and noise are measured at full load, 20 MHz BW with 10μF tantalum capacitor and 1μF ceramic capacitor across output. The 48 Vdc output models require a 10uF aluminum capacitor and 1uF ceramic capacitor across the output.
2. An external input capacitor of 470μF for 24 Vdc input models and 47μF for 48 Vdc input models is recommended to reduce input ripple voltage.

PART NUMBER KEY



INPUT

parameter	conditions/description		min	typ	max	units
operating input voltage	24 Vdc input models 48 Vdc input models		10 18	24 48	36 75	Vdc Vdc
under voltage shutdown	24 Vdc input	power up power down		9.5 8.5		Vdc Vdc
	48 Vdc input	power up power down		17 16		Vdc Vdc
CTRL[1]	positive logic	models ON (>3.5 Vdc or open circuit)				
		models OFF (0~1.2 Vdc)				
	negative logic	models ON (0~1.2 Vdc)				
		models OFF (>3.5 Vdc or open circuit)				
filter	pi filter					
input fuse	40A time delay fuse for 24 Vin models, 20A time delay fuse for 48 Vin models					

Note: 1. Open collector refer to -Vin

OUTPUT

parameter	conditions/description	min	typ	max	units
maximum capacitive load	12, 15, & 24 V output models 28 V output models 48 V output models	 100 47		2,200 2,200 2,200	µF µF µF
line regulation[2]	measured from low line to high line			±0.2	%
load regulation[2]	measured from zero load to full load			±0.2	%
voltage accuracy				±1.5	%
adjustability			±10		%
switching frequency			250		kHz
transient response	25% step load change			500	µs
temperature coefficient			±0.03		%/°C

Note: 2. A 100 µF aluminum capacitor is required on the output for the 28 Vdc output models. A 47 µF aluminum capacitor is required on the output for 48 Vdc output models.

PROTECTIONS

parameter	conditions/description	min	typ	max	units
short circuit protection	continuous				
over current protection	at 90% output voltage	110		160	%
over voltage protection		115		140	%
over temperature protection	shutdown		110		°C

SAFETY AND COMPLIANCE

parameter	conditions/description	min	typ	max	units
isolation voltage	for 1 minute: input to output; input to case; output to case	1,500			Vdc
isolation resistance		10			MΩ
RoHS	2011/65/EU (CE)				

ENVIRONMENTAL

parameter	conditions/description	min	typ	max	units
operating temperature	see derating curve	-40		85	°C
storage temperature		-55		105	°C

MECHANICAL

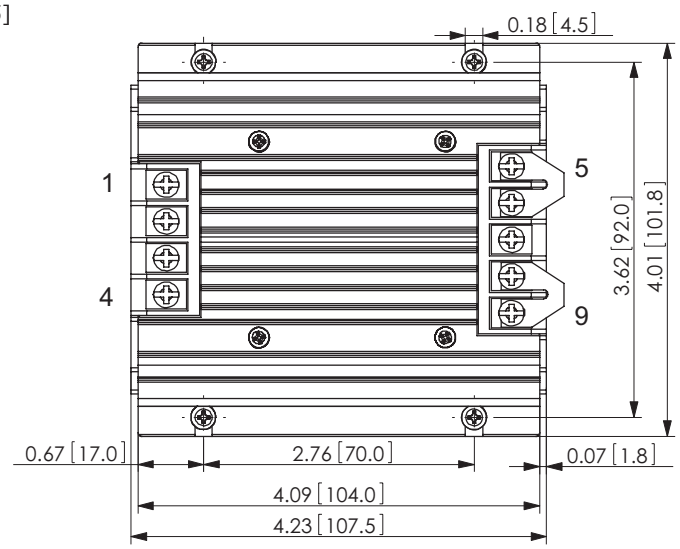
parameter	conditions/description	min	typ	max	units
dimensions	4.23 x 4.01 x 2.07 (107.5 x 101.8 x 52.6 mm)				inch
case material	steel and aluminum extrusion				
weight			651		g

MECHANICAL DRAWING

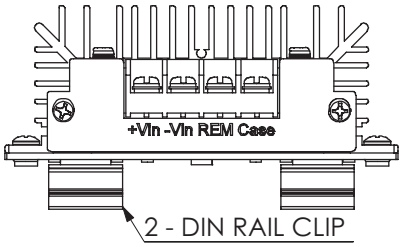
units: inch[mm]
tolerance: X.XX = ±0.02[±0.5]
X.XXX = ±0.010[±0.25]

wire range: 22~12 AWG
screw size: #6-32
mounts to TS35 rails

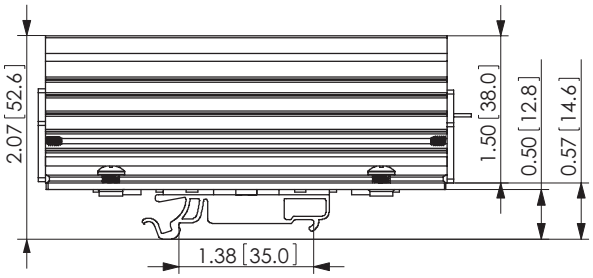
PIN CONNECTIONS	
PIN	FUNCTION
1	+Vin
2	-Vin
3	REM
4	CASE
5	+Vo
6	+S
7	TRIM
8	-S
9	-Vo



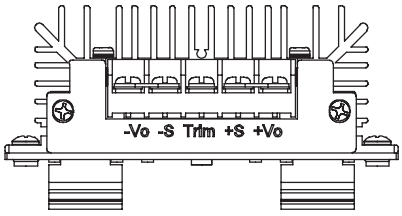
Top View



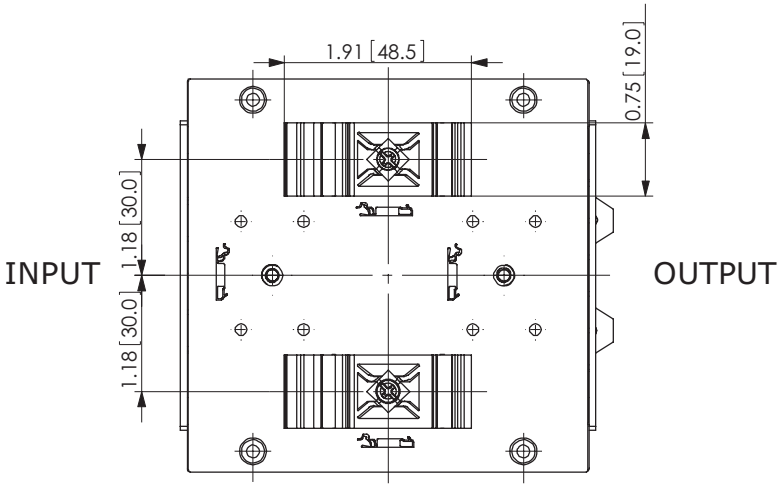
Front View



Side View

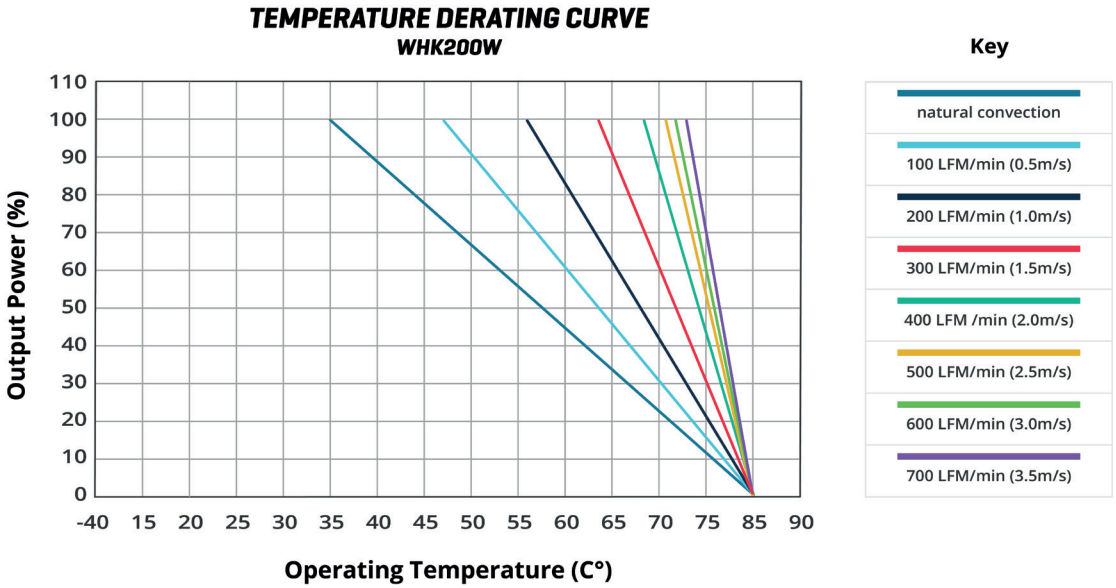


Back View



Bottom View

DERATING CURVES



TEST CONFIGURATION

Figure 1

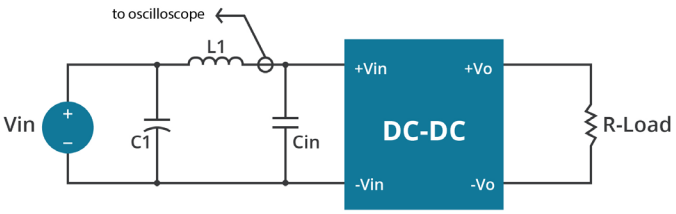


Table 1

External components	
for 24 Vdc input models	
L1	1.2µH
Cin	470µF, ESR < 0.2Ω at 100 KHz
for 48 Vdc input models	
L1	12µH
Cin	47µF, ESR < 0.7Ω at 100 KHz

Note: Input reflected-ripple current is measured with an inductor L1 to simulate source impedance.

EMC RECOMMENDED CIRCUITS

EN55022 CLASS A

Figure 2
Recommended Circuit for EN55022 Class A
(for all 12, 15, 24, & 28 Vdc output models)

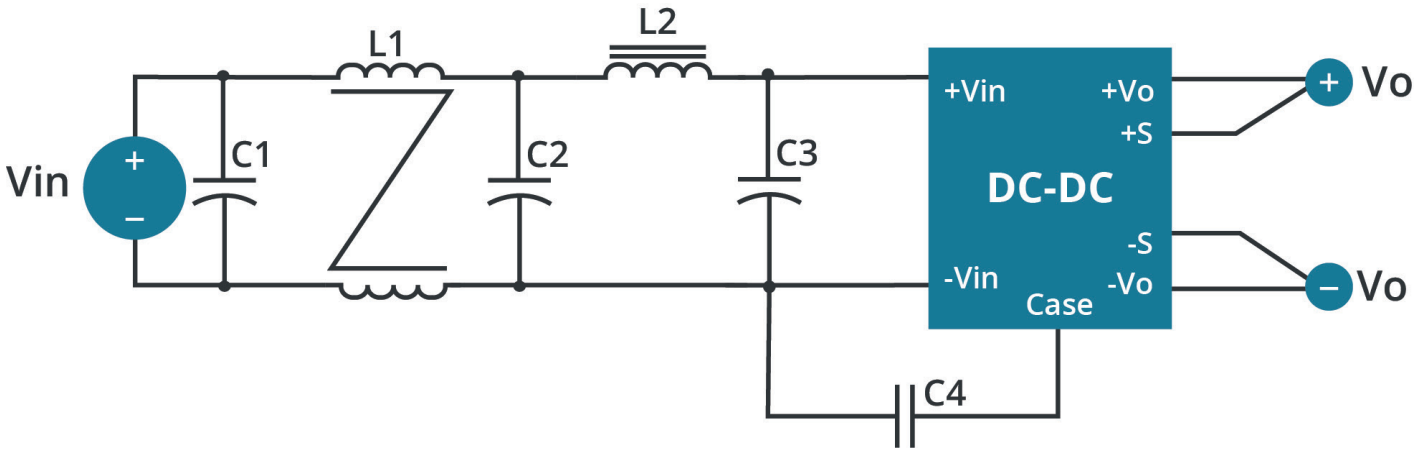


Figure 3
Recommended Circuit for EN55022 Class A
(for all 48 Vdc output models)

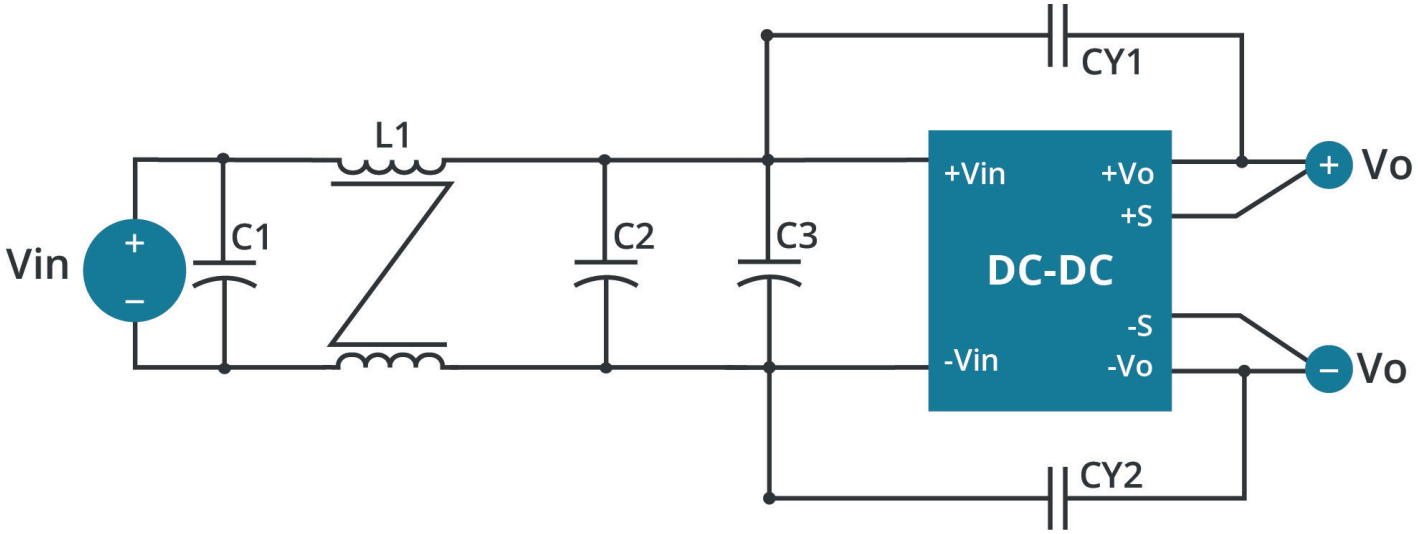


Table 2
Class A Recommended Components

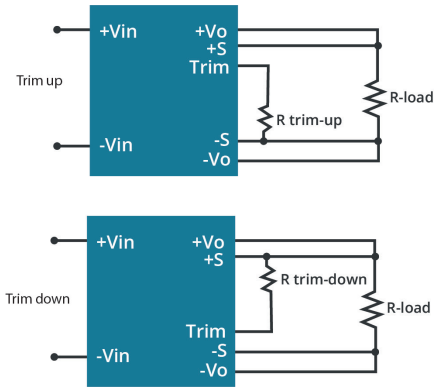
Model	C1[1]	C2[1]	C3[1]	CY1[2]	CY2[2]	L1	L2
VHK200W-Q24-S12	120 µF/100 V	120 µF/100 V	NC	NC	NC	0.5 mH	SHORT
VHK200W-Q24-S15	47 µF/100 V	47 µF/100 V	NC	NC	NC	0.5 mH	SHORT
VHK200W-Q24-S24	100 µF/100 V	100 µF/100 V	NC	NC	NC	0.5 mH	SHORT
VHK200W-Q24-S28	100 µF/100 V	100 µF/100 V	NC	NC	NC	0.5 mH	SHORT
VHK200W-Q24-S48	100 µF/100 V	100 µF/100 V	100 µF/100 V	680 pF/2 KV	680 pF/2 KV	1.0 mH	NC
VHK200W-Q48-S12	82 µF/100 V	82 µF/100 V	NC	NC	NC	0.5 mH	SHORT
VHK200W-Q48-S15	82 µF/100 V	82 µF/100 V	NC	NC	NC	0.5 mH	SHORT
VHK200W-Q48-S24	82 µF/100 V	82 µF/100 V	NC	NC	NC	0.7 mH	SHORT
VHK200W-Q48-S28	150 µF/100 V	150 µF/100 V	NC	NC	NC	0.5 mH	SHORT
VHK200W-Q48-S48	100 µF/100 V	100 µF/100 V	100 µF/100 V	680 pF/2 KV	680 pF/2 KV	1.0 mH	NC

Notes: 1. Aluminum capacitors.
2. Ceramic capacitors.

APPLICATION NOTES

1. **Output Voltage Trimming**
   Leave open if not used.

Figure 4
Application Circuit for Trim pin



Formula for Trim Resistor

Trim-Up

$$\Delta\% = \left(\frac{V_o - V_{o,\,nom}}{V_{o,\,nom}}\right)100$$

Trim-Up Formula
(for all 12, 15, 24, & 28[1] Vdc Output Models)

$$R_{trim\text{-}up} = \left(\frac{5.11V_{O,\,nom}(100 + \Delta\%)}{1.225 \times \Delta\%} - \frac{511}{\Delta\%} - 10.22\right)(K\Omega)$$

Trim-Up Formula
(for all 48 Vdc Output Models)

$$R_{trim\text{-}up} = \left(\frac{20V_{O,\,nom}(100 + \Delta\%)}{1.225 \times \Delta\%} - \frac{2000}{\Delta\%} - 40\right)(K\Omega)$$

Trim-Down

$$\Delta\% = \left(\frac{V_{o,\,nom} - V_o}{V_{o,\,nom}}\right)100$$

Trim-Down Formula
(for all 12, 15, 24, & 28[1] Vdc Output Models)

$$R_{trim\text{-}down} = \left(\frac{511}{\Delta\%} - 10.22\right)(K\Omega)$$

Trim-Down Formula
(for all 48 Vdc Output Models)

$$R_{trim\text{-}down} = \left(\frac{2000}{\Delta\%} - 40\right)(K\Omega)$$

Note: $R_{trim\text{-}up}$ is the external resistor in KΩ
$R_{trim\text{-}down}$ is the external resistor in KΩ
$V_{O,\,nom}$ is the nominal output voltage
V_O is the desired output voltage

Notes: 1. For the 28 Vdc output models, a minimum input voltage of 10.8 Vdc is required to trim the 24 Vdc input models up 10%, and a minimum input voltage of 19 Vdc is required to trim the 48 Vdc input models up 10%.
2. All specifications are measured at Ta=25°C, nominal input voltage and full output load unless otherwise specified.

REVISION HISTORY

rev.	description	date
1.0	initial release	12/16/2013
1.01	changed DIN-rail mount	06/16/2014
1.02	trim equation updated	11/13/2020
1.03	derating curve and circuit figures updated	09/07/2021

The revision history provided is for informational purposes only and is believed to be accurate.

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