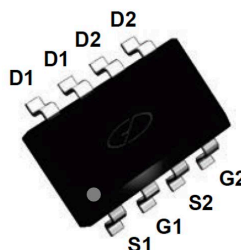
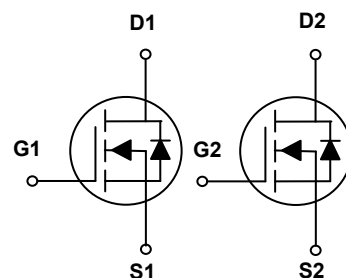


Main Product Characteristics

$V_{(BR)DSS}$	60V
$R_{DS(ON)}$	28m Ω
I_D	20A



SOP-8



Schematic Diagram

Features and Benefits

- Advanced MOSFET process technology
- Ideal for high efficiency switched mode power supplies
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery



Description

The GSFQ6806 utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supply and a wide variety of other applications.

Absolute Maximum Ratings ($T_C=25^{\circ}C$ unless otherwise specified)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current – Continuous ($T_C=25^{\circ}C$)	I_D	20	A
Drain Current – Continuous ($T_C=100^{\circ}C$)		13	A
Drain Current – Pulsed ¹	I_{DM}	80	A
Single Pulse Avalanche Energy ²	E_{AS}	42	mJ
Single Pulse Avalanche Current ²	I_{AS}	29	A
Power Dissipation ($T_C=25^{\circ}C$)	P_D	2.1	W
Power Dissipation – Derate above 25 $^{\circ}C$		0.017	W/ $^{\circ}C$
Storage Temperature Range	T_{STG}	-50 to +150	$^{\circ}C$
Operating Junction Temperature Range	T_J	-50 to +150	$^{\circ}C$

Thermal Characteristics

Parameter	Symbol	Typ.	Max.	Unit
Thermal Resistance Junction to Ambient	$R_{\theta JA}$	---	60	$^{\circ}C/W$

Electrical Characteristics ($T_J=25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	60	---	---	V
BV_{DSS} Temperature Coefficient	$\Delta BV_{DSS}/\Delta T_J$	Reference to 25°C , $I_D=1\text{mA}$	---	0.07	---	$V/^{\circ}\text{C}$
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=60V, V_{GS}=0V, T_J=25^{\circ}\text{C}$	---	---	1	μA
		$V_{DS}=48V, V_{GS}=0V, T_J=125^{\circ}\text{C}$	---	---	10	μA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA
On Characteristics						
Static Drain-Source On-Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=20A$	---	23	28	$m\Omega$
		$V_{GS}=4.5V, I_D=12A$	---	27	35	$m\Omega$
Gate Threshold Voltage	$V_{GS(th)}$	$V_{GS}=V_{DS}, I_D=250\mu A$	1.2	1.8	2.5	V
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}$		---	5	---	$mV/^{\circ}\text{C}$
Forward Transconductance	g_{fs}	$V_{DS}=10V, I_D=10A$	---	9	---	S
Dynamic and Switching Characteristics						
Total Gate Charge ^{2, 3}	Q_g	$V_{DS}=30V, V_{GS}=10V, I_D=15A$	---	28	42	nC
Gate-Source Charge ^{2, 3}	Q_{gs}		---	3.5	7	
Gate-Drain Charge ^{2, 3}	Q_{gd}		---	6.5	10	
Turn-On Delay Time ^{2, 3}	$T_{d(on)}$	$V_{DD}=30V, V_{GS}=10V, R_G=6\Omega, I_D=1A$	---	7.2	14	nS
Rise Time ^{2, 3}	T_r		---	38	72	
Turn-Off Delay Time ^{2, 3}	$T_{d(off)}$		---	34	65	
Fall Time ^{2, 3}	T_f		---	8.2	16	
Input Capacitance	C_{iss}	$V_{DS}=20V, V_{GS}=0V, F=1\text{MHz}$	---	1680	2440	pF
Output Capacitance	C_{oss}		---	115	170	
Reverse Transfer Capacitance	C_{rss}		---	85	125	
Gate Resistance	R_g	$V_{GS}=0V, V_{DS}=0V, F=1\text{MHz}$	---	2.2	4.4	Ω
Drain-Source Diode Characteristics and Maximum Ratings						
Continuous Source Current	I_S	$V_G=V_D=0V, \text{Force Current}$	---	---	20	A
Pulsed Source Current	I_{SM}		---	---	80	A
Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_S=1A, T_J=25^{\circ}\text{C}$	---	---	1	V
Reverse Recovery Time ²	t_{rr}	$V_{GS}=0V, I_S=1A, di/dt=100A/\mu S, T_J=25^{\circ}\text{C}$	---	19.6	---	nS
Reverse Recovery Charge ²	Q_{rr}		---	14.2	---	nC

Note:

1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
2. $V_{DD}=25V, V_{GS}=10V, L=0.1\text{mH}, I_{AS}=29A, R_G=25\Omega$, Starting $T_J=25^{\circ}\text{C}$.
3. The data tested by pulsed, pulse width $\leq 300\mu S$, duty cycle $\leq 2\%$.
4. Essentially independent of operating temperature.

Typical Electrical and Thermal Characteristic Curves

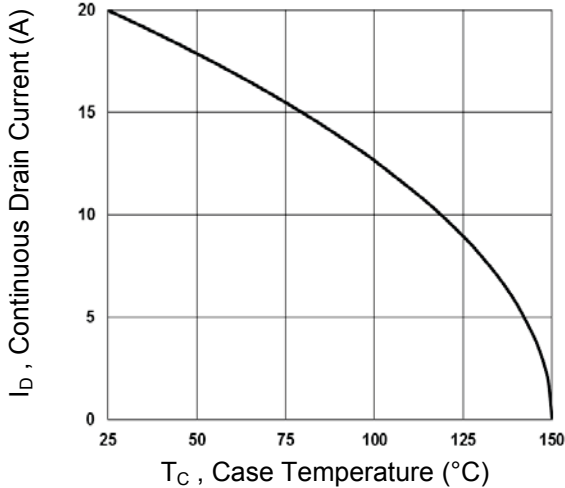


Fig.1 Continuous Drain Current vs. T_C

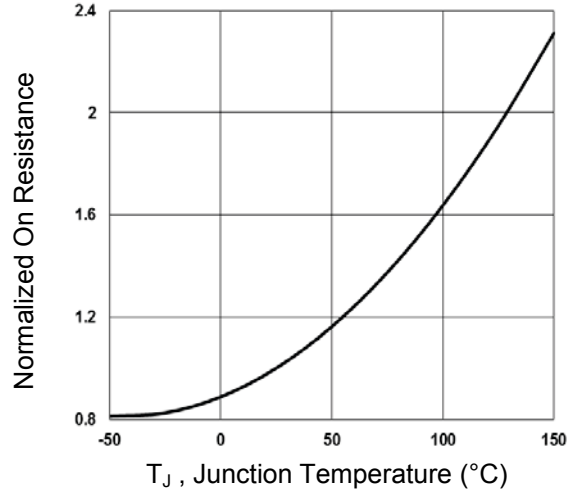


Fig.2 Normalized $R_{DS(ON)}$ vs. T_J

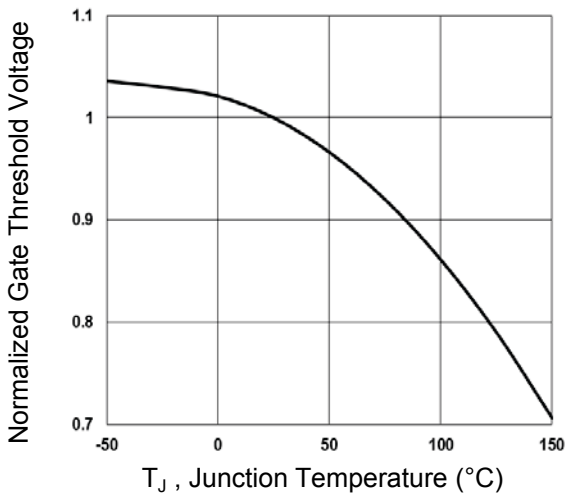


Fig.3 Normalized V_{th} vs. T_J

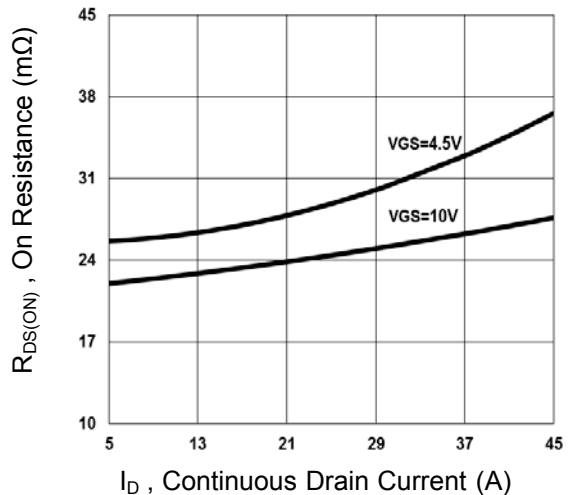


Fig.4 $R_{DS(ON)}$ vs. Continuous Drain Current

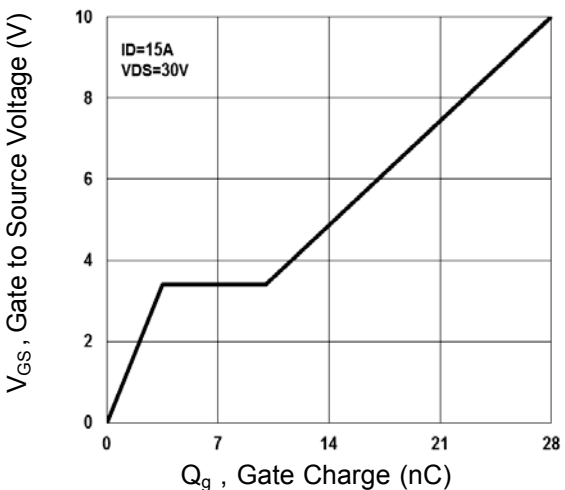


Fig.5 Gate Charge Waveform

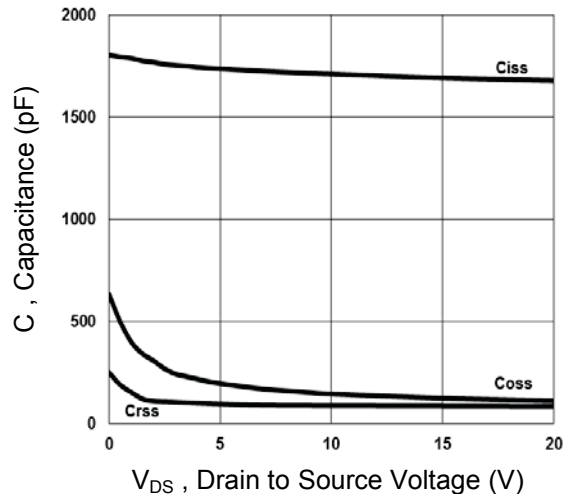


Fig.6 Capacitance Characteristics

Typical Electrical and Thermal Characteristic Curves

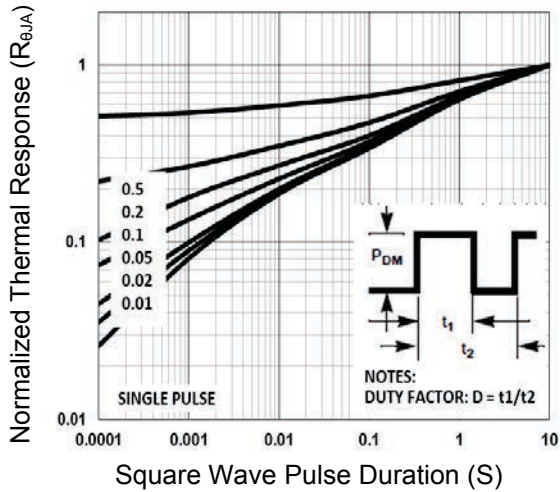


Fig.7 Normalized Transient Impedance

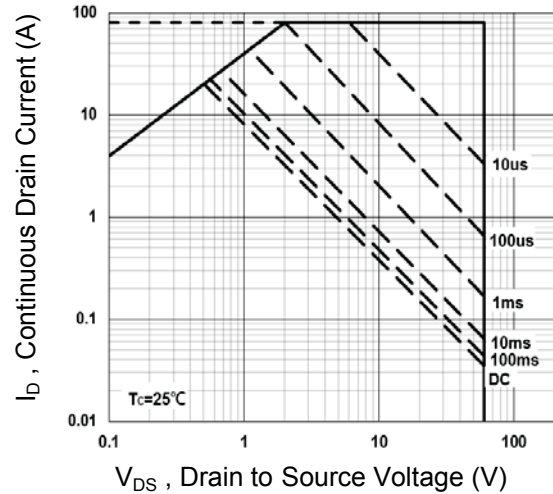


Fig.8 Maximum Safe Operation Area

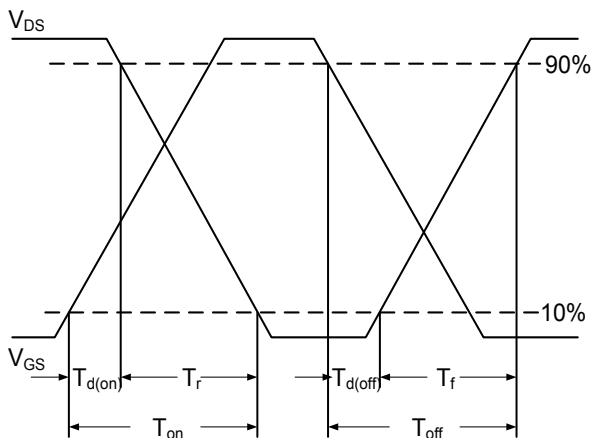


Fig.9 Switching Time Waveform

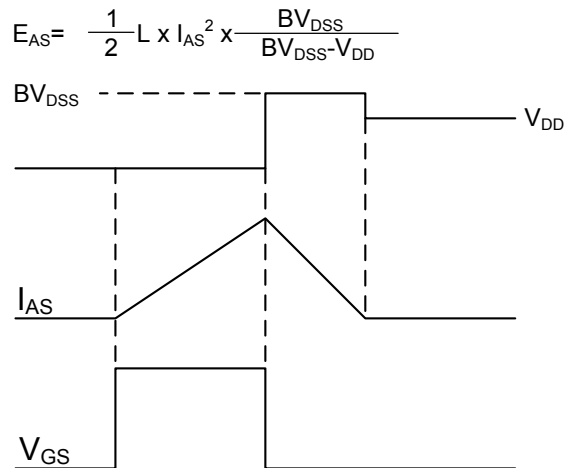
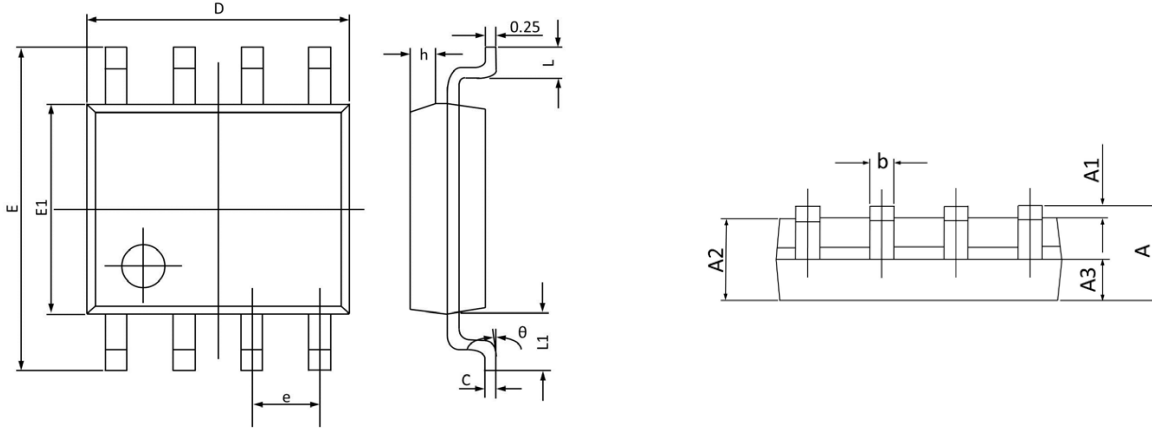


Fig.10 E_{AS} Waveform

$$E_{AS} = \frac{1}{2} L \times I_{AS}^2 \times \frac{BV_{DSS}}{BV_{DSS} - V_{DD}}$$

Package Outline Dimensions (SOP-8)



Symbol	Dimensions in Millimeters		Dimensions in Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.068
A1	0.100	0.250	0.004	0.009
A2	1.300	1.500	0.052	0.059
A3	0.600	0.700	0.024	0.027
b	0.390	0.480	0.016	0.018
c	0.210	0.260	0.009	0.010
D	4.700	5.100	0.186	0.200
E	5.800	6.200	0.229	0.244
E1	3.700	4.100	0.146	0.161
e	1.270 BSC		0.050 BSC	
h	0.250	0.500	0.010	0.019
L	0.500	0.800	0.019	0.031
L1	1.050 BSC		0.041 BSC	
θ	0°	8°	0°	8°

Recommended Pad Layout

