

SECTION 13

ORDERING INFORMATION AND MECHANICAL DATA

This section contains the ordering information, pin assignments, and package dimensions of the MC68060, MC68LC060, and MC68EC060.

13.1 ORDERING INFORMATION

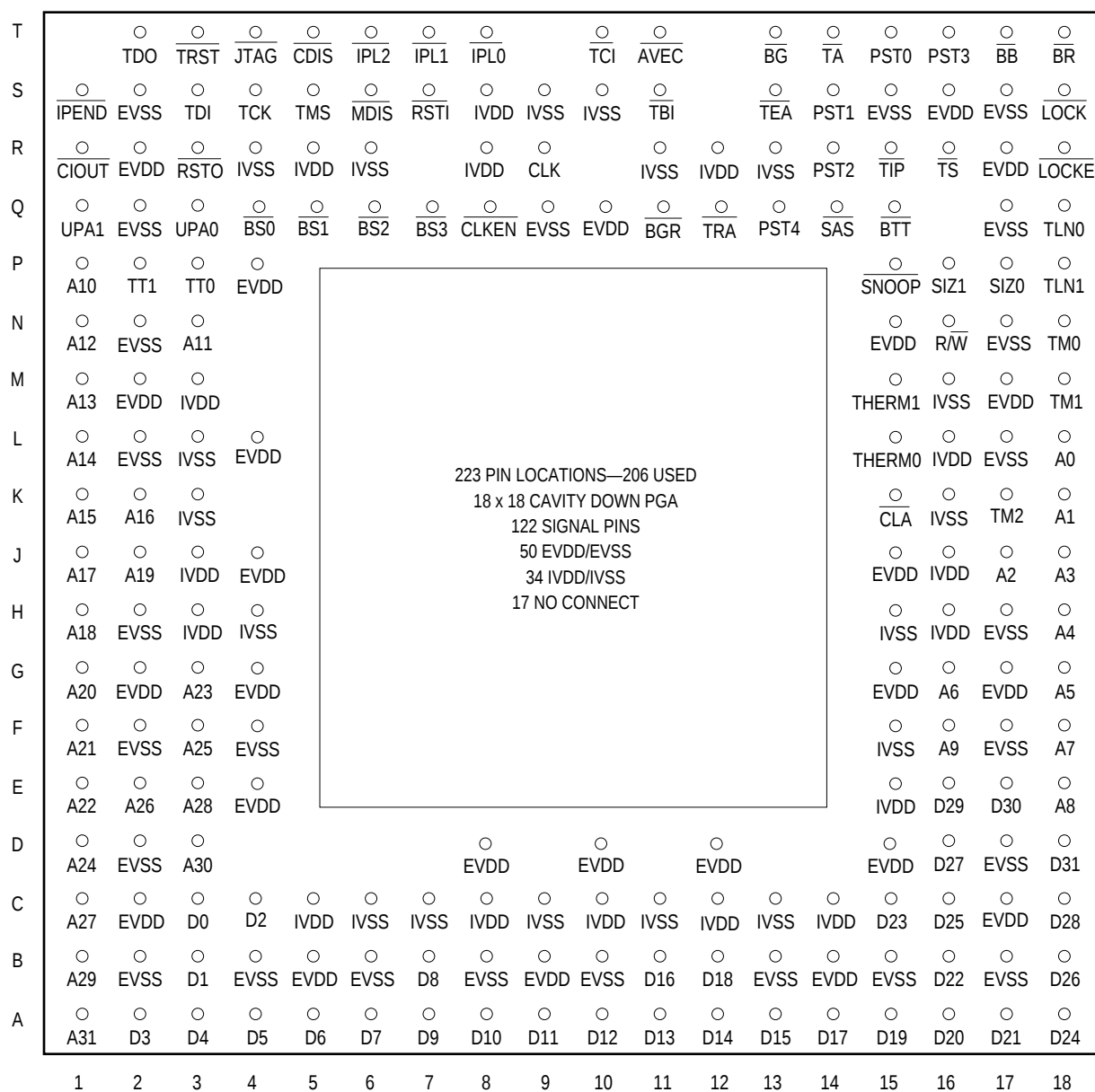
The following table provides ordering information pertaining to the MC68060, MC68LC060, and MC68EC060 package types, frequencies, temperatures, and Motorola order numbers.

Package Type	Frequency	Maximum Junction Temperature	Minimum Ambient Temperature	Order Number
PGA—RC Suffix	50 MHz	110°C	0°C	MC68060RC50
PGA—RC Suffix	66 MHz	110°C	0°C	MC68060RC66
PGA—RC Suffix	50 MHz	110°C	0°C	MC68LC060RC50
PGA—RC Suffix	66 MHz	110°C	0°C	MC68LC060RC66
PGA—RC Suffix	40 MHz	110°C	0°C	MC68EC060RC40
PGA—RC Suffix	50 MHz	110°C	0°C	MC68EC060RC50
PGA—RC Suffix	66 MHz	110°C	0°C	MC68EC060RC66
208-Pin QFP—FE Suffix	50 MHz	110°C	0°C	MC68060FE50
208-Pin QFP—FE Suffix	66 MHz	110°C	0°C	MC68060FE66
208-Pin QFP—FE Suffix	50 MHz	110°C	0°C	MC68LC060FE50
208-Pin QFP—FE Suffix	66 MHz	110°C	0°C	MC68LC060FE66
208-Pin QFP—FE Suffix	40 MHz	110°C	0°C	MC68EC060FE40
208-Pin QFP—FE Suffix	50 MHz	110°C	0°C	MC68EC060FE50
208-Pin QFP—FE Suffix	66 MHz	110°C	0°C	MC68EC060FE66

13.2 PIN ASSIGNMENTS

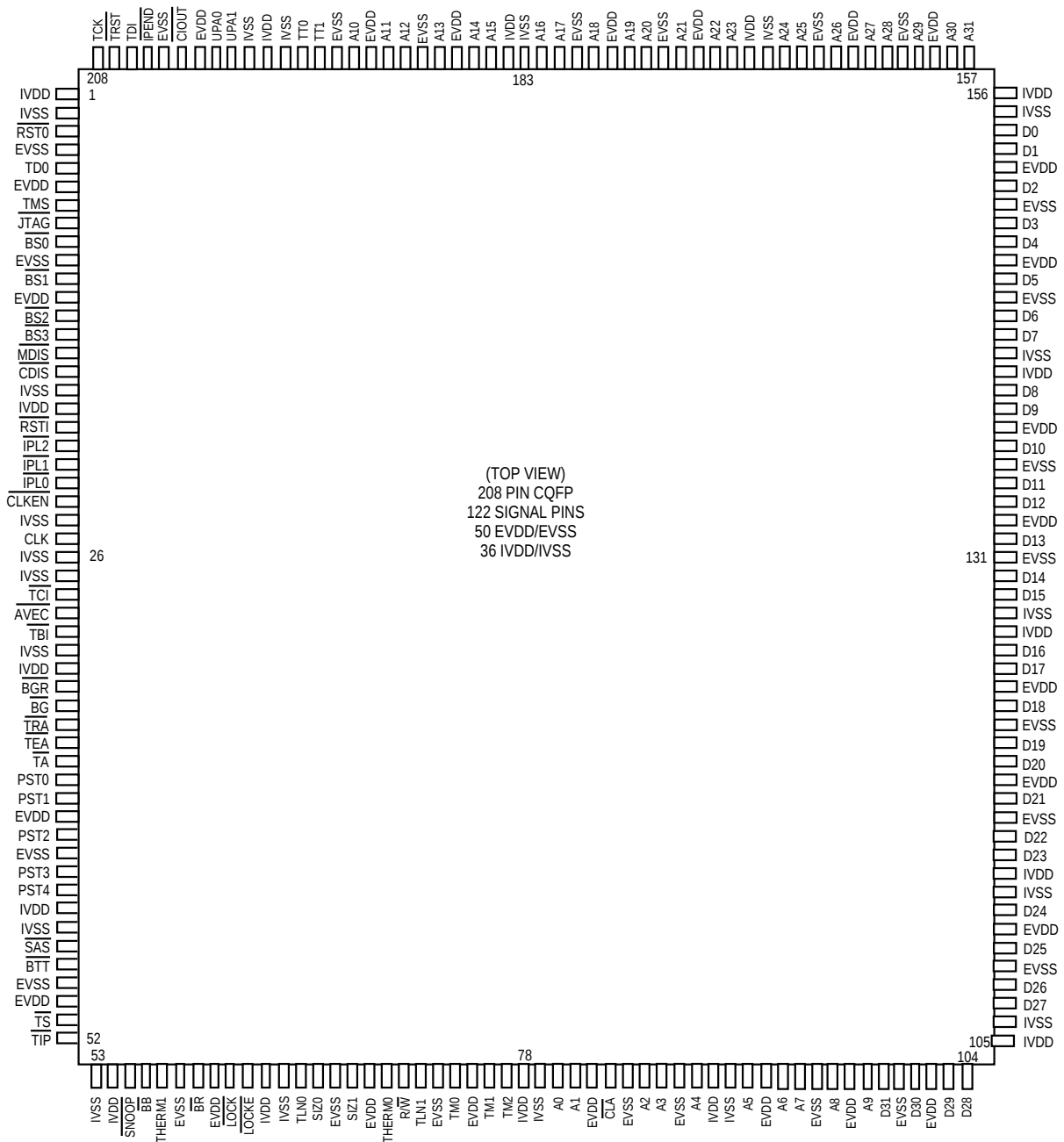
The following are the pin assignments for the MC68060, MC68LC060, and MC68EC060 package types.

13.2.1 MC68060, MC68LC060, and MC68EC060 Pin Grid Array (RC Suffix)



Pin Groups	GND (VSS)	VCC (VDD)
Internal Logic	C6, C7, C9, C11, C13, F15, H4, H15, K3, K16, L3, M16, R4, R6, R11, R13, S9, S10	C5, C8, C10, C12, C14, E15, H3, H16, J3, J16, L16, M3, R5, R8, R12, S8
Output Drivers	B2, B4, B6, B8, B10, B13, B15, B17, D2, D17, F2, F4, F17, H2, H17, L2, L17, N2, N17, Q2, Q9, Q17, S2, S15, S17	B5, B9, B14, C2, C17, D8, D10, D12, D15, E4, G2, G4, G15, G17, J4, J15, L4, M2, M17, N15, P4, Q10, R2, R17, S16

13.2.2 MC68060, MC68LC060, and MC68EC060 Quad Flat Pack (FE Suffix)

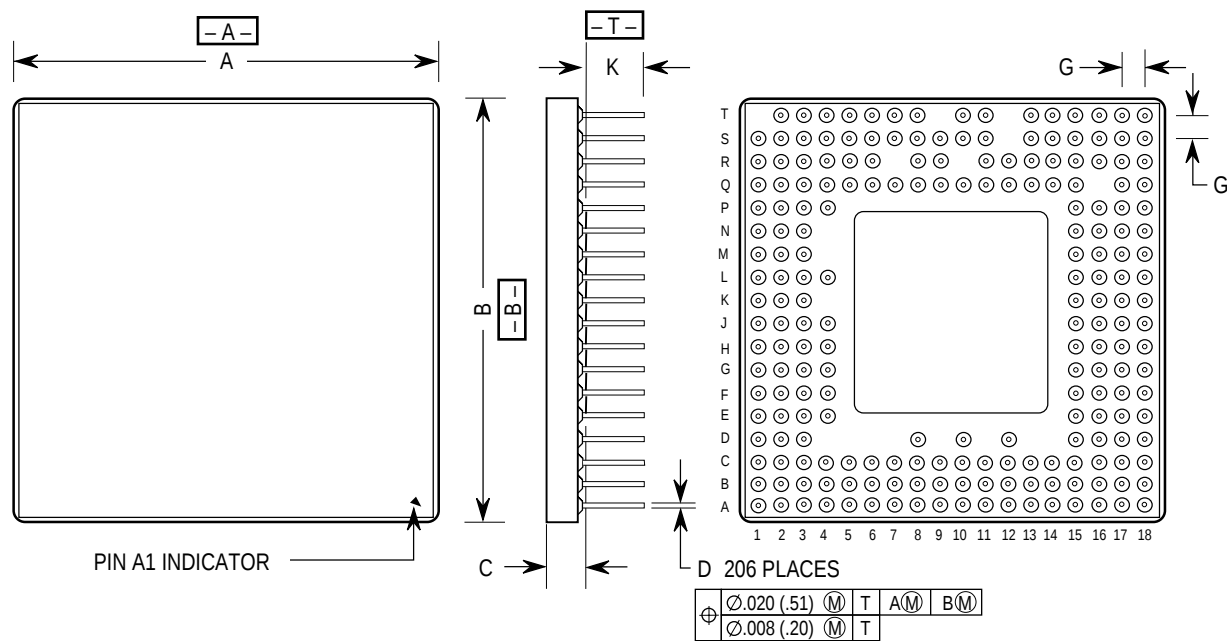


Pin Groups	GND (VSS)	VCC (VDD)
Internal Logic	2, 17, 24, 26, 27, 31, 46, 53, 64, 79, 90, 106, 113, 128, 142, 155, 169, 183, 197, 199	1, 18, 32, 54, 63, 78, 89, 105, 114, 127, 141, 156, 170, 184, 198
Output Drivers	4, 10, 42, 49, 58, 67, 73, 84, 87, 95, 100, 109, 117, 122, 131, 136, 145, 150, 161, 166, 175, 180, 189, 194, 204	6, 12, 40, 50, 60, 69, 75, 82, 92, 97, 102, 111, 119, 124, 133, 138, 147, 152, 159, 164, 173, 178, 187, 192, 202

13.3 MECHANICAL DATA

Figure 13-1 illustrates the MC68060, MC68LC060 and MC68EC060 PGA package dimensions. Figure 13-2 illustrates the MC68060, MC68LC060, and MC68EC060 CQFP package dimensions. Due to space limitation, Figure 13-2 is represented by a general (smaller) package outline rather than showing all 208 leads.

MC68060 PGA
CASE NUMBER: 993A-01



NOTES:
1. DIMENSIONS AND TOLERANCING PER
ANSI Y14.5M 1982.
2. CONTROLLING DIMENSION: INCH

Figure 13-1. PGA Package Dimensions (RC Suffix)

MC68060 CQFP
CASE NUMBER: 994-01

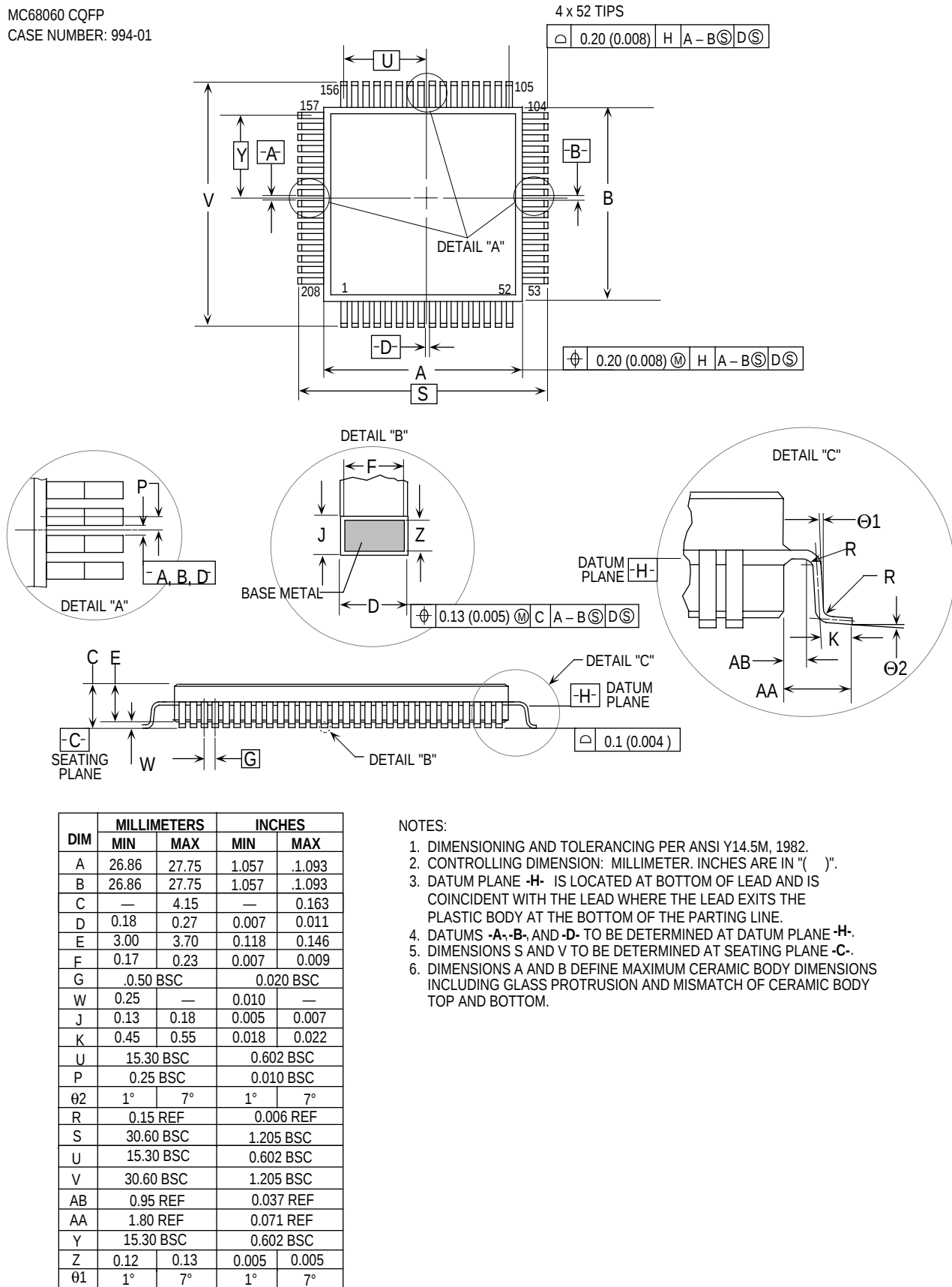


Figure 13-2. QFP Package Dimensions (FE Suffix)

